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Proceedings

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Preface

This volume contains the papers presented at the 6th International Symposium on Automated Technology for Verification and Analysis held during October 20–23 in Seoul, Korea. The primary objective of the ATVA conferences remains the same: to exchange and promote the latest advances of state-of-the-art research on theoretical and practical aspects of automated analysis, verification, and synthesis.

Among 66 research papers and 16 tool papers submitted to ATVA 2008, the Program Committee accepted 21 as regular papers, 7 as tool papers, and 5 as short papers. In all, 33 experts from 27 countries worked hard to make sure that every submission received as rigorous and fair an evaluation as possible. In addition, the program also included three excellent tutorials and keynote talks by David Dill (Stanford University), Sriram Rajamani (Microsoft Research India), and Natarajan Shankar (SRI International). The conference organizers were truly excited to have such distinguished researchers as keynote speakers.

Many worked hard and offered their valuable time so generously to make ATVA 2008 successful. First of all, the conference organizers thank all 218 researchers who worked hard to complete and submit papers to the conference. The PC members, reviewers, and Steering Committee members also deserve special recognition. Without them, a competitive and peer-reviewed international symposium simply cannot take place.

Many organizations sponsored the symposium. They include: The Korean Institute of Information Scientists and Engineers (SIGPL and Software Engineering Society), Korea University, Korea Advanced Institute of Science and Technology (KAIST), the Software Process Improvement Center and the Defense Software Research Center at KAIST. The conference organizers also thank the BK program at Korea University and the Department of Computer Science at KAIST for financial support.

We sincerely hope that the readers find the proceedings of ATVA 2008 informative and rewarding.

August 2008

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