

LOW-FREQUENCY NOISE IN ADVANCED MOS DEVICES

ANALOG CIRCUITS AND SIGNAL PROCESSING SERIES

Consulting Editor: Mohammed Ismail. Ohio State University

Titles in Series:

- CMOS SINGLE CHIP FAST FREQUENCY HOPPING SYNTHESIZERS FOR WIRELESS MULTI-GIGAHERTZ APPLICATIONS**
Bourdi, Taoufik, Kale, Izzet
ISBN: 978-1-4020-5927-8
- ANALOG CIRCUIT DESIGN TECHNIQUES AT 0.5V**
Chatterjee, S., Kinget, P., Tsividis, Y., Pun, K.P.
ISBN-10: 0-387-69953-8
- IQ CALIBRATION TECHNIQUES FOR CMOS RADIO TRANCEIVERS**
Chen, Sao-Jie, Hsieh, Yong-Hsiang
ISBN-10: 1-4020-5082-8
- FULL-CHIP NANOMETER ROUTING TECHNIQUES**
Ho, Tsung-Yi, Chang, Yao-Wen, Chen, Sao-Jie
ISBN: 978-1-4020-6194-3
- THE GM/ID DESIGN METHODOLOGY FOR CMOS ANALOG LOW POWER INTEGRATED CIRCUITS**
Jespers, Paul G.A.
ISBN-10: 0-387-47100-6
- PRECISION TEMPERATURE SENSORS IN CMOS TECHNOLOGY**
Pertjjs, Michiel A.P., Huijsing, Johan H.
ISBN-10: 1-4020-5257-X
- CMOS CURRENT-MODE CIRCUITS FOR DATA COMMUNICATIONS**
Yuan, Fei
ISBN: 0-387-29758-8
- RF POWER AMPLIFIERS FOR MOBILE COMMUNICATIONS**
Reynaert, Patrick, Steyaert, Michiel
ISBN: 1-4020-5116-6
- ADVANCED DESIGN TECHNIQUES FOR RF POWER AMPLIFIERS**
Rudiakova, A.N., Krizhanovski, V.
ISBN 1-4020-4638-3
- CMOS CASCADE SIGMA-DELTA MODULATORS FOR SENSORS AND TELECOM**
del Río, R., Medeiro, F., Pérez-Verdú, B., de la Rosa, J.M., Rodríguez-Vázquez, A.
ISBN 1-4020-4775-4
- SIGMA DELTA A/D CONVERSION FOR SIGNAL CONDITIONING**
Philips, K., van Roermund, A.H.M.
Vol. 874, ISBN 1-4020-4679-0
- CALIBRATION TECHNIQUES IN NYQUIST A/D CONVERTERS**
van der Ploeg, H., Nauta, B.
Vol. 873, ISBN 1-4020-4634-0
- ADAPTIVE TECHNIQUES FOR MIXED SIGNAL SYSTEM ON CHIP**
Fayed, A., Ismail, M.
Vol. 872, ISBN 0-387-32154-3
- WIDE-BANDWIDTH HIGH-DYNAMIC RANGE D/A CONVERTERS**
Doris, Konstantinos, van Roermund, Arthur, Leenaerts, Domine
Vol. 871 ISBN: 0-387-30415-0
- METHODOLOGY FOR THE DIGITAL CALIBRATION OF ANALOG CIRCUITS AND SYSTEMS: WITH CASE STUDIES**
Pastre, Marc, Kayal, Maher
Vol. 870, ISBN: 1-4020-4252-3
- HIGH-SPEED PHOTODIODES IN STANDARD CMOS TECHNOLOGY**
Radovanovic, Sasa, Annema, Anne-Johan, Nauta, Bram
Vol. 869, ISBN: 0-387-28591-1
- LOW-POWER LOW-VOLTAGE SIGMA-DELTA MODULATORS IN NANOMETER CMOS**
Yao, Libin, Steyaert, Michiel, Sansen, Willy
Vol. 868, ISBN: 1-4020-4139-X
- DESIGN OF VERY HIGH-FREQUENCY MULTIRATE SWITCHED-CAPACITOR CIRCUITS**
U, Seng Pan, Martins, Rui Paulo, Epifânio da Franca, José
Vol. 867, ISBN: 0-387-26121-4
- DYNAMIC CHARACTERISATION OF ANALOGUE-TO-DIGITAL CONVERTERS**
Dallet, Dominique; Machado da Silva, José (Eds.)
Vol. 860, ISBN: 0-387-25902-3
- ANALOG DESIGN ESSENTIALS**
Sansen, Willy
Vol. 859, ISBN: 0-387-25746-2

LOW-FREQUENCY NOISE IN ADVANCED MOS DEVICES

by

Martin von Haartman and Mikael Östling

*KTH, Royal Institute of Technology, School of Information and Communication
Technology, Kista, Sweden*

 Springer

A C.I.P. Catalogue record for this book is available from the Library of Congress.

ISBN 978-1-4020-5909-4 (HB)

ISBN 978-1-4020-5910-0 (e-book)

Published by Springer,
P.O. Box 17, 3300 AA Dordrecht, The Netherlands.

www.springer.com

Printed on acid-free paper

All Rights Reserved

© 2007 Springer

No part of this work may be reproduced, stored in a retrieval system, or transmitted in any form or by any means, electronic, mechanical, photocopying, microfilming, recording or otherwise, without written permission from the Publisher, with the exception of any material supplied specifically for the purpose of being entered and executed on a computer system, for exclusive use by the purchaser of the work.

This book is dedicated to Anne

Contents

Authors	ix
Preface	xi
Acknowledgments	xv
Chapter 1 – Fundamental noise mechanisms	1
Chapter 2 – Noise characterization	27
Chapter 3 – $1/f$ noise in MOSFETs	53
Chapter 4 – $1/f$ noise performance of advanced CMOS devices	103
Chapter 5 – Introduction to noise in RF/analog circuits	175
Appendix I – List of Symbols	189
Appendix II – List of Acronyms	197
Appendix III – Solutions to problems	199
Index	211

Authors

Martin von Haartman and Mikael Östling

KTH, Royal Institute of Technology, School of Information and
Communication Technology, Kista, Sweden

PREFACE

The excess noise above the well-known thermal noise and shot noise that shows up at low frequencies, the so-called *low-frequency noise* (other names are $1/f$ noise or flicker noise), has raised questions for a long time and has now become more important than ever. The low-frequency noise generated in the electronic devices is a key problem in analog circuits and systems since it sets a limit on how small signals that can be detected and processed in the circuits. In the early 1990s, the metal-oxide-semiconductor field-effect-transistor (MOSFET) had a channel length of around $0.5\ \mu\text{m}$ and was mainly used in digital electronics. The MOS transistor at that time had a conventional Si channel, SiO_2 gate dielectrics and few advanced features. The tremendous improvements in CMOS performance during the last decade, resulting from continuous advances in the CMOS technology, have stimulated the recent explosion in information and communication technology. Nowadays, MOS transistors are not used only in digital applications but also in a wide range of analog circuits. The low-frequency noise in the CMOS devices has therefore emerged as an important concern. The rapid shrinking of the device dimensions (the smallest gate length is around 30 nm in 2006) is not only a challenging technological problem, the low-frequency noise also increases as the dimensions become smaller with fewer and fewer charge carriers in the active region of the device. It has even been predicted that low-frequency noise will be a problem in digital applications in a few years time.

The CMOS technology has also evolved from the standard Si/ SiO_2 material system to more advanced material combinations and new types of device structures. This technology shift has had a pronounced impact on the low-frequency noise properties. The introduction of high-k materials or other

advanced features accompanied with more complex fabrication processes often lead to (more) defects and imperfections in the current path, which can cause a severe degradation of the low-frequency noise performance. A thorough understanding of the low-frequency noise mechanisms, potential noise sources, various noise models, and the impact of technology are thus important for professionals, researchers and students in the electronics field. In particular those working with CMOS device technology and design, characterization and modeling, and circuit design are expected to find great use of this book. The low-frequency noise cannot be completely eliminated, but with careful design of the devices and clever utilization or development of the technology the low-frequency noise can be substantially reduced. Accurate characterization and modeling of the low-frequency noise is not only immensely important for analog circuit designers but also in order to provide an understanding of the noise phenomenon itself. Furthermore, with deeper insights on how the low-frequency noise affects the output noise of a circuit, ways to optimize the circuit for low noise can be sought out.

This book spans from fundamental noise theory via characterization, MOSFET noise models and CMOS technology to address noise in analog/RF circuits. The purpose is both to give the reader an in-depth knowledge of low-frequency noise, while still presented in an easily comprehensible form, and bring together the different pieces all the way from the fundamental theories and physics level to the circuit level. The focus is on MOS devices and technology but the first two chapters about fundamental noise mechanisms and low-frequency noise characterization provide a general background. Other types of FET devices than the MOSFET, bipolar transistors or devices in other materials than Si/SiGe are beyond the scope of this book and are not treated in detail.

This book is structured as follows. In chapter 1, we will give an introduction to noise, describing the fundamental noise sources and basic circuit analysis. The characterization of low-frequency noise is discussed in detail in chapter 2. We will describe the equipment, measurement setups and diagnostic techniques including many useful practical advices. The various theoretical and compact low-frequency ($1/f$) noise models in MOS transistors are treated extensively in chapter 3, providing an in-depth understanding of the low-frequency noise mechanisms and the potential sources of the noise in MOS transistors. We will give an introduction to the MOS transistor and present its noise equivalent circuit. The number and mobility fluctuation noise models are discussed in detail and the $1/f$ noise dependence on device parameters and operating conditions are explained. We also review the most popular compact noise models; the SPICE and Berkeley short channel IGFET (BSIM3) models. In chapter 4, a comprehensive overview of state-of-the-art CMOS technology is presented

together with an exhaustive investigation of the low-frequency noise properties in the various types of advanced CMOS devices. Our presentation includes nanometer scaled devices, strained Si, SiGe, SOI, high-k gate dielectrics, metal gates and finally multiple gates. The book ends with an introduction to noise in analog/RF circuits and describes how the low-frequency noise can affect these circuits. We particularly discuss the voltage controlled oscillator and the upconversion of $1/f$ noise to phase noise as well as the noise properties of mixers and low-noise voltage amplifiers. In order to enhance the understanding of the various aspects of noise fundamentals and the noise implications in advanced CMOS technology, we have composed a number of relevant problems after each chapter. In appendix III a short solution manual is provided.

A reader of this book is assumed to understand fundamental semiconductor physics as well as the principles of CMOS devices at an undergraduate level. Knowledge about noise, CMOS device fabrication or electrical circuits is useful but not necessary. We have mainly followed the conventional notations used in for example *Fundamentals of Modern VLSI Devices* by Y. Taur & T. K. Ning (Cambridge University Press, Cambridge, 1998). Note that the words low-frequency noise and $1/f$ noise are both frequently used throughout this book, but their meaning is interchangeable for the most part.

Martin von Haartman and Mikael Östling

January 2007

ACKNOWLEDGMENTS

The material of this book mainly stems from the PhD thesis work performed by Dr. Martin von Haartman under the supervision by Prof. Mikael Östling. The idea of the book was born in connection with the PhD defense, for this we would like to thank Prof. Mohammed Ismail. The actual writing of the book was made possible by faculty funds arranged by Prof. Östling during a 6 months assignment as research associate for Martin von Haartman. As always, the realization of this project would never have become possible without the direct and indirect help and contributions from a number of people. The researchers and engineers at the device technology laboratory at KTH (Royal institute of Technology), Kista, are greatly acknowledged for preparing the devices used in this work as well as enlightening research discussions. In this context, we specifically would like to thank Dr. Gunnar Malm for continuous support in form of comments and ideas about this research work as well as Dr. Per-Erik Hellström for the development of the CMOS device technology at KTH fuelling our research work with advanced devices to study. Dr. Malm and Prof. Carl-Mikael Zetterling also deserve many thanks for their help proofreading the manuscript. The authors are very grateful to the continuous project funding through the Swedish foundation for strategic research (SSF), the Swedish Governmental Agency for Innovation Systems (VINNOVA) and the graduate student fellowship award by IEEE EDS. Finally, Martin von Haartman would like to express his deep love and gratitude to his wife Anne for her support and encouragement during the course of this demanding project.