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## Glossary

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AC	Alternating current
ADC	Analog-to-Digital Converter
APLL	Analog phase-locked loop
BER	Bit Error Rate
BGA	Ball Grid Array
CODEC	COmpression/DECompression
CP	Charge pump
CPU	Central Processing Unit
DAC	Digital-to-Analog Converter
DC	Direct Current
DCO	Digital controlled oscillator
DDR	Dual Data Rate
DLL	Delay-Locked Loop
DNL	Differential Non-Linearity
DP	Display Port
DPLL	Digital phase-locked loop
DSP	Digital Signal Processing
DVI	Digital Video Interface
ENOB	Effective number of bits
EMC	Electromagnetic Compatibility
EMI	Electromagnetic Interference
EMIFF	External memory interface fast
EMIFS	External memory interface slow
ESL	Equivalent Series Inductance
ESR	Equivalent Series Resistance
FCC	Federal Communication Commission
GP	Gain peaking
GPS	Global Positioning System

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IEEE	Institute of Electrical and Electronics Engineers
HD	High Definition
HDMI	High Definition Media Interface
HSYNC	Horizontal Synchronization
INL	Integral Non-Linearity
IO	Input/Output
JTAG	Joint Test Action Group
LCD	Liquid Crystal Display
LDO	Low dropout regulator
LSB	Least significant bit
McBSP	Multi-Channel Buffered Serial Port
MPEG	Moving Picture Experts Group
PCI	Peripherals Component Interface
PCB	Printed Circuit Board
PFD	Phase frequency detector
PLL	Phase-Locked Loop
PSRR	Power Supply Rejection Ratio
PWM	Pulse Width Modulation
RF	Radio Frequency
SD	Standard Definition
SDRAM	Synchronous Dynamic Random Access Memory
SNR	Signal-to-noise
SoC	System-on-Chip
TL	Transmission Line
USB	Universal Serial Bus
UWB	Ultra-Wide Band
VCO	Voltage controlled oscillator
VSYNC	Vertical Synchronization
WLAN	Wireless local area network

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