COMPUTER TECHNOLOGY

The TMS 9918A VDP: A new device for generating moving displays on a microcomputer

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A procedure for creating smooth and continuous moving displays on a microcomputer using the TMS 9918A video display processor (VDP) is discussed. This processor shares all of the features of other VDPs and, in addition, enables the user to directly program smooth motion of specified graphics shapes (sprites) with multilevel pattern overlaying. The general principles for programming this video chip are described, and a machine language driver is presented. Three illustrative applications from the event perception literature—wheel-generated motion, kinetic disruption of optical texture, and biomechanical motions—are presented as demonstrations of how the unique features of this video chip can be implemented in the creation of dynamic displays.

The microcomputer is fast becoming a major tool of the visual perception laboratory (Cavanagh & Anstis, 1980). Included among its functions are the creation and rapid presentation of well-defined shapes, texture patterns, and scenes, the implementation of interactive displays, and the simple presentation of apparentmotion phenomena. One remaining function that has proved more elusive to implement involves smooth and continuous animation, especially when surfaces occlude and disocclude one another as they move. Yet, the availability of this last function is no less important than the others, since it is essential to the study of the kinetic specification of objects and their spatial layout, a topic beginning to receive increasing interest in the visual perception literature (Johansson, von Hofsten, & Jansson, 1980).

Machine language programs represent one solution to the animation problem (e.g., Jochumson, 1983), but the availability of this programming skill is fairly restricted and programming in machine language is certainly much more laborious than programming in higher level languages. Furthermore, the frequent need for hidden line and surface algorithms adds substantially to the complexity of the programming task and, more importantly, places additional demands on the relatively slow-operating microprocessor. The net result is that, even with a machine language program, the rate of movement is often slowed to a level that compromises the perception of smooth and continuous motion, especially when large shapes are involved.

In this paper, we introduce an alternative solution for

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producing animated displays that is more powerful than the standard machine language approach, and yet easier to implement. The major component of this solution is an integrated circuit by Texas Instruments, the TMS 9918A video display processor (VDP). This chip includes certain features that make it ideally suited for creating animation. In particular, it provides the opportunity for displaying and moving sprites.

SPRITES AND OTHER FEATURES OF THE TMS 9918A VDP

A sprite is a colored block shape that can be drawn on a video plane in a position given by a single pair of coordinates specifying a particular vertical and horizontal location on the screen. These graphic objects can be programmed in two sizes: 8 x 8 and 16 x 16 pixel (picture element) arrays. The size of the individual elements can be increased from 1 pixel to a 2 x 2 array of pixels through a magnification factor. User-defined shapes are created by programming a subset of the pixels in the array to appear as a particular color (15 possible colors) and setting the remaining pixels to transparent.

Animation of each shape is accomplished by simply changing the values of the x,y coordinates corresponding to the upper left-hand corner of the sprite. This procedure provides a method for quickly and smoothly moving patterns at a resolution of 1 pixel: The full screen resolution is 256 pixels horizontally and 196 vertically. In contrast, programming animation with other VDPs necessitates that a shape be drawn at one location, erased, and then redrawn at a new location. Even for those readers unfamiliar with the programming demands of the latter procedure, it should be apparent that the use of sprite graphics is the simpler of the two alternatives.

Programming with sprites also eliminates many of the difficulties encountered when overlaying a moving object on top of another surface. Each sprite occupies 1 of 32 separate video planes organized together as a set of vertically stacked display planes, not unlike 32 panes of glass sandwiched together (see Figure 1). Sprites are assigned priorities of visibility as a function of their position in depth relative to the observer. The closest plane is assigned a priority of 0, and the furthest plane is assigned a value of 31. Any time portions of two or more sprites occupy the same location on the screen, that portion of the sprite with the lower priority (i.e., higher number) will be occluded by the sprite with the higher priority. For example, notice how the front of the car is occluded by the tree in Figure 1. Also, as the car continues to move behind the tree, there will appear a progressive deletion and accretion of texture corresponding to the car. The point to be emphasized is that neither of these effects needs to be programmed; occlusion of the car occurs simply because it is composed of sprites with lower priorities than the sprites composing the tree. Thus, the unique graphics architecture of the TMS 9918A VDP enables the programmer to simulate both static and kinetic occlusion without any need for complex hidden-surface algorithms. This feature is especially useful for simulating threedimensional displays, since it is well known from the visual perception literature that occlusion information is perceived as specifying the depth-order relations among different surfaces, especially when one surface is moving relative to another (Gibson, Kaplan, Reynolds, & Wheeler, 1968).

In addition to the first 32 sprite planes, there exist 3 additional display planes (listed in their order relative to the observer)—pattern, backdrop, and external VDP input (see Figure 1). The pattern plane operates in any

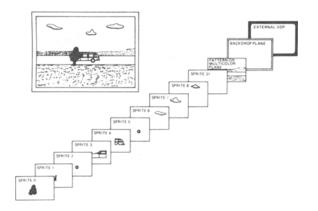


Figure 1. A computer-generated scene created through the combination of multiple display planes. Each video plane is assigned a priority of visibility as a function of its position in depth relative to the observer. [From *Texas Instruments TMS 9918A VDP Data Manual* (Texas Instruments, 1981), reprinted courtesy of Texas Instruments, Incorporated]

of three graphics modes and one text mode. In each of the graphics modes, the screen is divided into different-sized pattern blocks ranging from the equivalent of low-to high-resolution graphics. The backdrop plane is programmed to appear in 1 of 15 colors and provides a border around the sprites and pattern plane. Finally, it is possible to input an external video signal that will appear behind the other planes. (Note—some versions of this chip allow the external signal to emanate from the host microcomputer's VDP.) In all cases, the visibility of a particular location on any one display plane assumes that all the preceding planes are transparent in that location.

In sum, the TMS 9918A VDP incorporates all of the features of other VDPs, but its unique architecture enables the user to program smooth motion of certain graphics shapes (sprites) with multilevel pattern overlaying. These features make it possible for investigators interested in motion perception phenomena to program dynamic events with a modicum of programming skill.

GENERAL PRINCIPLES OF OPERATION FOR THE TMS 9918A VDP

The objective of this section is to provide a general overview of the procedure involved in programming the TMS 9918A VDP. [More specific information can be obtained from the *Texas Instruments TMS 9918A VDP Data Manual* (Texas Instruments, 1981).] There are two basic components of this task: (1) writing the data that define the patterns and their locations on the graphics screen to a buffer of dynamic memory (henceforth referred to as video random access memory—VRAM) attached directly to the VDP; and (2) initializing the eight registers of the VDP.

The data are organized into five discrete blocks in VRAM. Two define the sprite patterns, and the other three define the pattern appearing on the background plane. With regard to the sprites, one block contains the pattern definitions. They are arranged into subblocks of 8 bytes each and are bit-inapped such that each row of an 8 x 8 sprite corresponds to 1 byte of the block. Each bit assigned "1" causes the sprite to be defined at that point; each bit assigned "0" causes the sprite to be transparent at that point. A 16 x 16 sprite can be constructed from four 8-byte subblocks corresponding to four 8 x 8 sprites arranged as the quadrants of a square. (As many as 256 8 x 8 sprites or 64 16 x 16 sprites can be specified in the Pattern Definition Table at one time.) The second block specifies a Sprite Attribute Table containing the vertical and horizontal positions of the sprite, its video plane assignment corresponding to 1 of the 32 planes available, and its color. In order to move a sprite on the screen, it is necessary only to change the x,y coordinates in the Sprite Attribute Table; the color of the sprite can be changed in the same manner.

The pattern plane is defined by the three other blocks consisting of a Pattern Definition Table, a Pattern Name

Table, and a Pattern Color Table. These three tables provide a library of colored pattern blocks and their screen locations. Although these tables are similar to those created for displaying sprites, the logic governing how a background pattern is defined and displayed is somewhat more complex and will not be described further, since our primary emphasis is on the generation of sprites.

The eight registers of the VDP contain information concerning different modes of operation as well as the starting addresses for each of the tables stored in VRAM. For the purpose of displaying sprites, it should be noted that Register 5 contains the upper 7 bits of the 14-bit Sprite Attribute Table address and Register 6 contains the upper 3 bits of the 14-bit address of the Sprite Definition Table. The lower 4 bits of Register 7 contain the color code for the backdrop plane. Finally, Bits 6 and 7 of Register 1 specify sprite size ("0" selects 8 x 8 pixels; "1" selects 16 x 16 pixels) and magnitude ("0" selects 1 pixel per array element; "1" selects 2 x 2 pixels per array element), respectively.

Listing 1 presents a general-purpose machine language driver for the TMS 9918A VDP written in 6502 assembly language. It is designed so that separate routines can be

Listing 1
Machine Language Driver for Displaying Sprites

```
SPRITE DRIVER
BY BENNETT I. BERTENTHAL
                                                  * This driver performs the following functions:
                                                                  Initializes 8 registers of the Video
                                                           Display Processor (VDP)
2. Clears video RAH (VRAM)
                                                          3. Loads any number of sprites (G-32)
and their pattern definitions
                                                  * All blocks of data to be loaded into VRAM will
                                                  have their starting and ending address loaded
into 4 consecutive locations on Page zero.
Starting address: LEYTE = $06, HEYTE = $07
Ending address: LEYTE = $08, MEYTE = $09
                                      15678901223456789012345678901234567
                                                  * This program assumes that all blocks of data * begin at a page boundary.
                                                      This program also assumes that the sprite card is in slot 4.
                                                                        ORG $300
OBJ $300
                                                  **** DEFINE LABELS *****
                                                                       EQU $40 ;SLOT # FOR SP
EQU $1A ;SET-UP ADDRES
EQU $0081+SLOT ;VDP REGISTER
EQU $0080+SLOT ;VDP RAK
EQU $380 ;STARTING ADDR
                                                                                                              :SLOT # FOR SPRITE CARD
                                                                                                                SET-UP ADDRESS FOR TRANSFERRING DATA TO VEAM
                                                                                                               STARTING ADDRESS OF REGISTERS
                                                  **** INITIALIZE REGISTERS ****
0300: A0 80
0302: A2 00
0304: ED 80 03
0307: 20 44 03
0308: C8
0308: E8
0306: E0 08
0306: D0 F4
0310: 60
                                                                                   #$80
#$00
REGADR,X
SREG
                                                                                                               · SELECT REGISTER
                                                                                                              SELECT RECISTER
INITIALIZE COUNTER
ILOAD DATA FOR REGISTER
WRITE TO VDP
INORDMENT SELECTED COUNTER
INCREMENT SELECTED COUNTER
FALL REGISTERS LOADED'
IF HOT, BRANCH
                                                                        LDX
LDA
JSR
INY
INX
CPX
                                                                                   LOOP
                                        48
                                        49
50
51
53
54
55
55
56
57
56
61
62
                                                                 CLEAR VRAM MEMORY
                                                                                                               (SET-UP ADDRESS - BYTC2
(SET-UP ADDRESS - BYTC3
(WRITE TO VP)
(COUNTER - HIGH BYTE
(COUNTER - LOW BYTE
(WRITE O TO VP) RAM
(INCREMENT LOW COUNTER
(IS LOW COUNTER FULL?
(INCREMENT 10H COUNTER
(HIGH COUNTER FULL?
0311: A4 1A
0333: A9 00
0315: 20 44 03
0318: A2 00
031A: A0 00
031C: 8D C0 C0
031F: C8
032C: D0 FA
032C: D0 FA
032C: D0 F7
032S: 50
                                                                                   BYTE2
#$00
SREG
#$C0
#$00
VRAM
                                                                        LDY
LDA
JSR
LDX
LDY
STA
INY
BNE
INX
BNE
RTS
                                                                                   COUNT
                                                                                    COUNT
```

```
LOAD VRAM TABLES ****
0326: AN 1A
0326: AP 00
032A: 20 4W 03
032D: AO 00
032F: B1 06
0331: BD CO CO
0337: B6 07
0337: B6 07
0339: CW 09
0338: EW 09
                                                                                                                                        ;SET-UP ADDRESS - BYTE1;WRITE TO VDP
                                                                                        LDA
JSR
LDY
LDA
STA
                                                                                                      SREG
#$00
                                                                                                                                          INITIALIZE COUNTER
                                                              MORE
                                                                                                     ($06),Y
                                                                                                                                        : LOAD DATA
                                              70
71
72
73
74
75
76
77
78
80
81
82
83
84
85
                                                                                                                                       !LOAD DATA
!KNETE TO VOP RAM
!KNERMENT COUNTER
!FIRST PAGE FULL?
!YES, INCREMENT HETTE
!LOAD HEYTE
!COMPARE TO HEYTE FOR END OF TABLE
!LAST PAGE?
!LAST PAGE TO HEYTE FOR END OF TABLE
!LAST PAGE DONE?
                                                                                       INY
BNE SKIP
INC $07
LDX $07
CPX $09
BNE HORE
CPX $08
BNE MORE
RTS
                                                              SKIP
                                                                               WRITE TO VDP ****
0344: 8D C1
0347: 8C C1
034A: 60
                                                                                                                                        STORE BYTES
-- End ascembly--
75 bytes
Errors: 0
```

called to initialize the registers, clear VRAM, and load specific blocks of data to VRAM. Note that each block must be loaded into that portion of VRAM specified by the corresponding VDP register address for that particular function, for example, sprite attributes, sprite pattern definitions, etc. This goal is accomplished by loading a 2-byte address setup value into the VDP at the beginning of the data-transfer routine that is equivalent to the address specified by the corresponding VDP register. In addition, Bit 6 of the second byte of the address setup value must be set high to indicate that the CPU is transferring data to the VRAM through the VDP. This transfer uses a 14-bit autoincrementing address register; thus, once the starting address is set up, data are transferred 1 byte at a time to the VRAM. In a similar fashion, data are written to each of the eight registers in the VDP by transferring the data in the first byte and selecting the register in the second. The chip convention requires that this second byte have the most significant bit (MSB) set high, the next 4 bits set low, and the lowest 3 bits set to correspond to the destination register number.

In our lab, we have used this driver for creating a number of different dynamic displays on an Apple II microcomputer. One particular application displays wheel-generated motion and illustrates especially well how to use the machine language driver in conjunction with a program written in a higher level language.

AN ILLUSTRATIVE EXAMPLE: WHEEL-GENERATED MOTION

The study of wheel-generated motion has had a long and venerable history in the visual perception literature (Proffitt & Cutting, 1979). In essence, this event involves the presentation of two or more points of light moving as if placed on various locations of a rotating invisible wheel translating across a surface. Although observers typically report seeing the lights rotate around the centroid (center of rotation for any system of lights), an analysis of the motions of the individual lights reveals

that each moves along a path describing a cycloid or prolate cycloid (see Figure 2). The one exception is the point at the center of the wheel that moves along a linear path (Proffitt & Cutting, 1979). For example, the absolute motions of two lights located on the rim of a wheel 180 deg apart from each other both trace identical cycloidal paths corresponding to the same sinusoidal function differing only in phase. It follows therefore that programming wheel-generated motion on a computer involves displaying a series of lights that all undergo sinusoidal motions along a common path with the motion of each light out of phase with the others.

Our approach to creating wheel-generated motion was to keep the task as simple as possible by programming all initial procedures in Applesoft BASIC (see Listing 2). These procedures include loading the machine language driver and Sprite Pattern Table, initializing variables, and computing and storing in arrays the x and y coordinates for each sprite prior to the time that it is displayed on the screen. (An even more efficient procedure is to poke these values directly into RAM.) Thus, the processing demands during the display portion of the program are reduced to simply reading the next pair of x,y values from the array and writing them via the machine language driver into the appropriate location of the Sprite Attribute Table in VRAM. In addition, the program is compiled (Microsoft Tasc Compiler) not only to further reduce processing time during the display portion of the program, but also to reduce processing time during the earlier portion when the x and y coordinates are computed.

This program allows the user to display between one

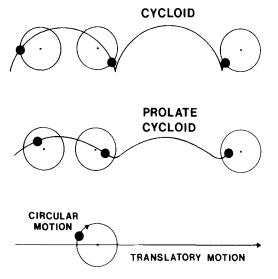


Figure 2. In the top panel is shown the motion of two points on a rolling wheel, with the center represented as a small dot and a perimeter point as a large dot. The middle panel likewise presents the motion of two points on a rolling wheel, one being the center and the other a point within the perimeter. The lower panel shows the circular and translatory components of motion. (From Proffitt & Cutting, 1979)

Listing 2
Demonstration Program for Displaying Wheel-Generated Motions

..............................

```
WHEEL-GENERATED MOTIONS
BY BENNETT I. BERTENTHAL
& STEVEN J. KRAMER
             REM
REM
REM
                             * MARCH 2, 1984 *
              HOME : VTAB 7: HTAB 8: INVERSE : PRINT SPC( 23)
            HOME: YTAB 6: HTAB 6: INVERSE: PRINT SPC( 23)

YTAB 6: HTAB 8: PRINT "WEEKL-GEMERATED MOTIONS"

YTAB 9: HTAB 8: PRINT SPC( 23): NORMAL

YTAB 12: HTAB 5: PRINT "FILIS PROGRAM DISELAIS AS MANY AS"

YTAB 13: HTAB 5: PRINT "FILIS FORTATING AND TRANSLATING"

YTAB 13: HTAB 5: PRINT "ARCOUND THE CENTROID OF A WHEEL"

YTAB 14: HTAB 5: PRINT "RECOUND THE CENTROID OF A WHEEL"

YTAB 12: HTAB 8: INVERSE: PRINT "PRESS ANY KEY TO CONTINUE": NORMAL: GET
             Z$: PRINT Z: HOME
REM
                                INITIALIZE VARIABLES AND
                         * LOAD MACHINE LANGUAGE PRGMS *
             DIM X(300.6),Y(300.6),COL(6),N(6)
             DIM X(300,6),Y(300,6),COL(6),N(6)

YTAB 10: HERB 8: INVERSE: PRINT "LOADING SPRITE DRIVER": YTAB 12: HTAB 9:

PRINT " ARD PATTERN TABLE": NORMAL

PRINT CHR (4); "BLOAD SPRITE PATTERN TABLE, A$8100": PRINT CHR$ (4);

"BLOAD SPRITE DRIVER, A$300": HOME

"VIB 10: HTAB 9: INVERSE: PRINT " PLEASE BE PATIENT ": VTAB 11: HTAB 9:

PRINT "INITIALIZING VARIABLES": NORMAL
 240
             REM DERINE CONSTANTS
                           DEFINE VARIABLE ADDRESSES
              NEM DEFINE VALUE ADDRESSES
ESTEZ 2 6: REM MEMORI LOCATION FOR VRAM ADDRESS SET-UP VALUE
80 = 896:R1 = R0 + 1:R2 = R1 + 1:R3 = R2 + 1:R4 = R3 + 1:R5 = R4 + 1:
80 = 896:R1 = R0 + 1:R7 = R6 + 1: REM REDISTER LOCATIONS
SPT = 32768:PT = SPT + 256: REM STARTING LOCATIONS FOR SPRITE ATTRIBUTE
             SRI = 32/80:F1 = 5F1 + 296: REM SIMILING LOCATIONS FOR SPRILE ATTRIBUTE
SL = 6:SR = 7:EL = 8:EH = 9: REM POINTERS FOR BEGINNING AND END OF BUFFERS
SL = 6:SR = 7:EL = 8:EH = 9: REM STARTING LOCATIONS FOR MACRIME LANGUAGE
              ROUTTNES
                        DEFINE FUNCTIONS
            DEF FN LO(X) = INT (X - INT
DEF FN HI(X) = INT (X / 256)
                                                                              INT (X / 256) # 256)
                        ************************
             REM
REM
             REM * USER-DEFINED VARIABLES *
            HOME: VTAB 3: HTAB 10: INVERSE: PRINT "USER-DEFINED VARIABLES": NORMAL VTAB 5: HTAB 5: PRINT "ENTER X(5-30), Y(10-150) "
          VTAB 5: HTAB 5: PRINT "ENTER X(5-30), Y(10-150)"

VTAB 6: HTAB 5: INPUT "FOR LIGHT #1: ";CI$,CI$:CI = .VAL (CI$):

CT = VAL (CI$): IF CI < 5 OR CI > 30 OR CI < 10 OR CI > 150 THEN

GOSUB 1030: GOTO #50

VTAB 8: HTAB 5: INPUT "WHEEL RADIUS (5-30) = ";R$!R = VAL (R$):

IF R < 5 OR R > 30 THEN GOSUB 1030: GOTO #60

VTAB 10: HTAB 5: INPUT "NOTATION INCREMENT (2-30) = ";RI$:RI = VAL (RI$):

IF RI < 2 OR RI > 30 THEN GOSUB 1030: GOTO 470

VTAB 12: HTAB 5: INPUT "TRANSLATION SPEED (0-5) = ";TS$:TS = VAL (TS$):

IF TS < 0 OR TS > 5 THEN GOSUB 1030: GOTO #0

VTAB 14: HTAB 5: PRINT "CORRECTION FACTOR FOR"

VTAB 15: HTAB 5: PRINT "SCREM (0.65-1.00) = ";CF$:CF = VAL (CF$):

IF CF < .65 OR CF > 1.0 THEM GOSUB 1030: GOTO 500

VTAB 17: HTAB 5: HTAB 5: PRINT "LIGHT # ";I; INPUT " COLOR (0-15) = ";COL$:

COL(I) = VAL (COL$): IF COL(I) < 0 OR COL(I) > 15 THEN GOSUB 1030: GOTO 520

POKE SPT + 3 + ((I - 1) * %), COL(I): REM POKE SPRITE COLOR VALUE

NEXT 1
550
560
570
            NEXT I
                       INITIALIZE REGISTERS *
            POKE R0,2: POKE R1,192: POKE R2,7: POKE R3,27: POKE R4,255: POKE R5,62: POKE R6,3: POKE R7,1
CALL IR: REM CALL MACHINE LANGUAGE ACUTINE FOR TRANSFERRING VALUES INTO VPD REGISTERS
 610 REM
           660
670
680
            690
700
710
720
            REM INITIALIZE COUNTERS
           RC = 0: REM INITIALIZE COUNTER FOR LOOP
TR = 0: REM INITIALIZE TRANSLATION DISTANCE
           REM COMPUTE LOCATION OF LIGHTS AROUND WHEEL (IN RADIANS)
          REM COMPUTE LOCATION OF LIGHTS AROUND WHEEL (IN RADIANS)

M(1) = M(1) + RI / DEG:M(2) = M(1) + PI / 2:M(3) = M(1) + PI

M(4) = M(3) + PI / 2:M(5) = M(1) + PI / 4:M(6) = M(1) + PI / 8

FOR I = 1 TO 6: IF I = 6 THEW RR = R:R = R * .6

X(MC,I) = CCT + R * SIN (M(I)) + TR

I(NC,I) = (CY - R * COS (M(I))) * CF

IF X(NC,I) = 2555 THEM X(NC,I) = 2555: GOTO 860

MEXT I

PRINT * .*;
840
           REM DISPLAY LIGHTS PREM
           HOME: YTAE 10: HTAE 9: PRINT "PRESS SPACE BAR TO BEGIN": YTAE 11: RTAE 12: PRINT "DISPLAYING LIGHTS": GET A$: POKE - 16368,0: IF A$ < > CHR$ (32) THEN 890
900 YTAB 16: HTAB 10: INVERSE : PRINT "PRESS ANY KEY TO STOP": NORMAL
```

and six sprites rotating around the center of an invisible wheel translating from left to right across the screen. The sprites are designed to appear as small circles of light moving on a dark background. Each sprite contains a separate pattern definition stored in the Sprite Pattern Table. (A binary dump of this table is presented in Listing 3.) The initial locations of the sprites around the circle are presented in Figure 3. These locations are

Listing 3
Binary Dump of Sprite Pattern Table

#8100.812F								
8100-	00	30	78	78	30	00	00	00
8108-	00	30	78	78	30	00	00	00
8110-	00	30	78	78	30	00	00	00
8118-	00	30	78	78	30	00	00	00
8120-	00	30	78	78	30	00	00	00
8128-	00	30	78	78	30	00	00	00

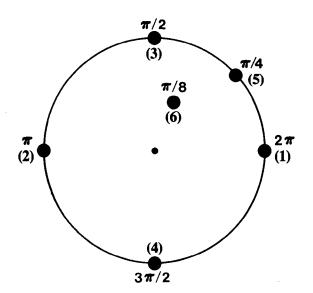


Figure 3. The initial locations (in radians) of the six sprites around the wheel.

presented in radians rather than degrees because the trigonometric functions in Applesoft expect the argument to correspond to radians.

Some of the parameters of the display must be specified by the user in Lines 300-430. These userdefined parameters include: (1) initial location for sprite #1 $(20.85)^2$; (2) wheel radius (measured in pixels displayed on the screen) (20); (3) rotation increment (5)-this value corresponds to the number of degrees incremented around the circle each time the positions of the sprites are updated; (4) translation speed (1.7)—this value corresponds to the number of pixels incremented on the horizontal axis each time the location of the sprites is updated; (5) correction factor for screen (.80)-this value is designed to correct for screens for which the aspect ratio between horizontal and vertical pixels is not equal to 1.00; (6) color for each of the six sprites—the value 15 corresponds to white, and the value 0 corresponds to transparent. It is thus possible to control the number of sprites to be displayed by setting the color of unwanted sprites to 0.

ADDITIONAL APPLICATIONS IN THE CREATION OF DYNAMIC DISPLAYS

Although the above program is a good illustration of how sprites can be used in dynamic displays, it by no means exhausts the possible applications or makes use of all of the special features provided by the TMS 9918A VDP. In this last section, we will illustrate some of the other features available to the user by discussing the creation of two additional displays: segregation of two randomly textured surfaces through kinetic occlusion, and specification of a human form through biomechanical motions.

Kinetic Occlusion of Randomly Textured Surfaces

Two randomly textured surfaces appear as a flat continuous surface as long as both remain stationary. Yet, as soon as one begins to move relative to the other, there occurs an accretion of texture at the trailing edge of the nearer surface and a deletion of texture at the leading edge. Observers report seeing one surface going behind another at an occluding edge and use this information for specifying which of the two surfaces is nearer (Gibson et al., 1968). Kaplan (1969) was the first to test this phenomenon empirically by creating a movie film of two moving randomly textured surfaces. This approach must have been painstakingly slow, since each frame had to be filmed separately. In contrast, we created this demonstration in a relatively short period of time using both sprites and the pattern plane of the TMS 9918A VDP.

The first step was to create a random texture pattern using a dart-throwing technique to determine which pixels should be colored (the probability of lighting any pixel was .3). This pattern was programmed in Apple II high-resolution graphics and then converted to

a pattern that could be displayed by the TMS 9918A VDP using a commercially available conversion program (E-Z Color Graphics Editor, by Micromint). The nearer occluding surface was created from sprites designed with the same dart-throwing algorithm used for creating the larger pattern plane. Note that the size of the occluding surface need not be constrained by the size of a single sprite, since a moving unitary shape can be composed of more than one sprite. For example, the car displayed in Figure 1 was composed of two sprites that occupied different screen locations and were assigned to two successive video planes. For the purpose of this demonstration, we arranged four sprites together in such a way that each sprite represented one quadrant of an array twice as large as a single sprite. When the sprites are programmed to move back and forth over the pattern plane, they produce an accretion and deletion of texture of the more distant surface because the sprites automatically occlude the texture on the more distant pattern plane.

Although the above procedure was quite successful in creating a demonstration of the kinetic disruption of optical texture, a small caveat is in order. Recall that each sprite is assigned only one color; thus, all pixels not corresponding to the pattern remain transparent. Accordingly, this particular demonstration of surface segregation through texture occlusion differs from Kaplan's (1969) original demonstration in one unusual way. Whereas Kaplan's demonstration involved two opaque surfaces, this new version created with sprites depicts a moving transparent surface occluding a more distant opaque surface. Phenomenally, the nearer surface appears to be like a thin pane of glass with a texture pattern randomly distributed over its area. A simple procedure for modifying the present demonstration to depict an opaque moving surface is to add four additional sprites directly behind the four composing the moving occluding surface. As long as these sprites are programmed to represent a solid pattern sharing the same color as the background, and all lie in video planes that will be occluded by the four sprites defining the texture of the moving surface, then the nearer moving surface will appear opaque.

In sum, the creation of this demonstration highlights two points about using sprites in the generation of dynamic displays: (1) The creation of kinetic occlusion is reduced from an extremely complex programming task to one that is relatively simple when the TMS 9918A VDP is used; and (2) the size of any one moving shape is not limited by the size of a sprite, but rather is defined by the number of contiguously arranged sprites that move together.

Biomechanical Motions

Johansson (1973) showed that adults can almost immediately recognize the form of a person walking when they view a two-dimensional projection of 10 to 12 point-lights attached to the head and major joints of an unseen walker (see Figure 4A). This same event has more recently been simulated on a computer by Cutting (1978). In essence, Cutting has written a program that displays points of lights mimicking the movements of a human walker. These lights appear to be mounted on the major joints (near shoulder and hip, and both wrists, elbows, knees, and ankles) and head of a person walking along a path normal to the observer's line of sight. There exists, however, one significant difference between this computer-synthesized display and those created using real people. In versions of this event involving the filming of real people, there appears a gradual deletion and accretion of the more distant point-lights as they are hidden and unhidden by the invisible form. Specifically, the more distant wrist, elbow, knee, and ankle, as well as the hip light, undergo some degree of kinetic occlusion twice during each gait cycle. In contrast, the computer simulation has each of the relevant point-lights disappear and appear all at once as if the point-lights were blinking on and off. When one considers how much complexity is added to the programming task by trying to achieve gradual accretion and deletion of the pointlights, it is not surprising that this effect was omitted.

An implementation of the point-light walker program with the TMS 9918A VDP overcomes this programming limitation by using sprites to represent both the point-lights and the invisible occluding forms. In essence, we used the basic logic of Cutting's (1978) FORTRAN program, but rewrote it in Applesoft BASIC and also modified it to write the x,y coordinates to the sprite driver rather than to an oscilloscope driver. In this sprite-oriented version, Sprites 0-6 and 11-14 are used to

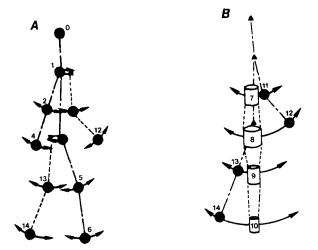


Figure 4. Depicted in A is the array of 11 point-lights attached to the head and joints of a walking person: The head and near side of the body are numbered 0 through 6, and the more distant joints are numbered 11 through 14. These numbers correspond to the priority of visibility possessed by each sprite used to display that particular point-light. Depicted in B is the shape and relative location of those sprites (7 through 10) used to occlude the far joints (11 through 14) of the walker as they pass behind the invisible form of the body.

represent the point-lights in the display and are arranged so that the higher numbered (lower priority, or more distant) sprites correspond to the point-lights depicting the more distant joints (see Figure 4A). Sandwiched between the more distant point-lights and those appearing closer are four additional sprites (Sprites 7-10) shaped to correspond to the human form at those relative positions (see Figure 4B). These four sprites are not visible to the observer because they are of the same color as the background; still, they serve to gradually hide and unhide the sprites corresponding to the far joints of the walker, since these background-colored sprites have higher priorities of visibility.

There is one additional point-light, corresponding to the hip, that should also undergo gradual accretion and deletion, but does not in the present scheme. The reason for this omission highlights the only significant limitation we have found in using the TMS 9918A VDP. As currently designed, this VDP can display only four sprites on any one horizontal line. If more than four are programmed to appear on the same line, then the four highest priority (or closest) sprites on the line are displayed normally and the fifth and subsequent sprites are not displayed at all. In order to occlude the hip light as the implicit arm of the walker passes in front of it, an additional sprite would have to be sandwiched between the near wrist and hip. This manipulation is not possible, however, because there are already four sprites overlapping in the same location; these sprites correspond to the hip, the two wrists, and the invisible occluder sandwiched between the hip and the more distant wrist light. Although there are procedures for getting around this limitation (such as computing the period during the gait cycle when the hip light should be occluded and then changing the priority value of the occluding sprite for that portion of the gait cycle), it must be recognized that these procedures can be implemented only by adding to the complexity of the programming task.

In spite of the one limitation described above, the ease of depicting the gradual hiding and unhiding of the point-lights remains an important advantage in using sprites for programming point-light displays of biomechanical motions. Furthermore, these displays appear almost as smooth and continuous as those created by Cutting (1978) using an HP 1350A display system driven by an HP 1000L computer; thus, they provide a further endorsement for the effectiveness of sprite-oriented graphics in the creation of moving displays.

CONCLUDING REMARKS

As recently as 3 years ago, we were convinced that it was impractical to create dynamic displays on a micro-

computer. Since that time, we have begun to create moving displays with a microcomputer using sprites and other features provided by the TMS 9918A VDP. Many of our applications have involved moving pointlight displays, for which sprites are especially well suited. Still other types of dynamic displays are also possible, as demonstrated by our discussion of the kinetic disruption of optical texture. Most importantly, we are finding that many of our moving displays are comparable to those created with much more expensive minicomputers and oscilloscopes.

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NOTES

- 1. This chip is designed to interface with a host of microprocessors via an 8-bit bidirectional data bus and three control lines. Its output is a composite color video signal defined by the contents of dynamic VRAM (ranging between 4K and 16K) attached directly to the VDP. The VRAM is automatically refreshed by the VDP and needs no direct connections to the host computer. Video output is asynchronous with other functions of the VDP, and thus the host processor and the VDP can communicate at any time without interfering with the video output.
- 2. A prototypical display is created with the values enclosed in parentheses following the listing of each user-defined variable.

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