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Investigation of LRS dependence on the retention of HRS in CBRAM

Xiaoxin Xu, Hangbing Lv^{*}, Hongtao Liu, Qing Luo, Tiancheng Gong, Ming Wang, Guoming Wang, Meiyun Zhang, Yang Li, Qi Liu, Shibing Long and Ming Liu

Abstract

The insufficient retention prevents the resistive random access memory from intended application, such as code storage, FPGA, encryption, and others. The retention characteristics of high resistance state (HRS) switching from different low resistance state (LRS) were investigated in a 1-kb array with one transistor and one resistor configuration. The HRS degradation was found strongly dependent on the LRS: the lower the resistance of the LRS (R_{LRS}) is, the worse HRS retention will be. According to the quantum point contact model, the HRS corresponds to a tiny tunnel gap or neck bridge with atomic size in the filament. The degradation of HRS is due to the filling or widening of the neck point by the diffusion of copper species from the residual filament. As the residual filament is stronger in case of the lower R_{LRS} , the active area around the neck point for copper species diffusion is larger, resulting in higher diffusion probability and faster degradation of HRS during the temperature-accelerated retention measurement.

Keywords: Resistive random access memory (RRAM); High resistance state (HRS); Retention; Quantum point contact (QPC) model

Introduction

Resistive random access memory (RRAM), relying on the reversely switching between high resistance state (HRS) and low resistance state (LRS), has been widely explored as one of the most promising candidates due to its key advantages, such as simple structure, scalability potential, and complementary metal-oxide-semiconductor (CMOS) compatibility [1-6]. Nevertheless, the deficient switching mechanisms and ungenerous reliability still present a significant barrier to implement RRAM into large-scale commercial applications. Cation-based electrochemical metallization cell (ECM) and anion-based valence change memory (VCM) are two main categories of RRAM [1]. The ECM device also called as conductive bridge random access memory (CBRAM), whose switching mechanism is governed by creation (SET operation) and dissolution (RESET operation) of the metallic conductive filament (CF), likely related with the active metal species such as Ag or Cu [5,6]. On the other hand, the switching behavior of VCM is triggered by the drift of oxygen anions [7,8]. The copper-based CBRAM, which takes the advantage of the copper plug or copper line as the electrode, is a favorable choice with respect to costeffective fabrication [9-11]. According to the CF model of CBRAM, the data retention is related with the diffusion of metal species [12,13]. In the case of LRS, the lateral diffusion of metal ions out of the CF causes a decrease of CF diameter or metal concentration in the CF, resulting in the degradation of LRS retention, whereas the degradation of HRS retention stems from the reconstruction of CF due to the vertical ions diffusion from the residual CF to the rupture region [12-14]. It has been reported that the retention of HRS or LRS was highly dependent on the resistance value of HRS or LRS, respectively [14-16]. The activation energy required for metal species diffusing in the electrolyte also affected the HRS and LRS retention greatly [16-22]. However, none of the prior arts has addressed the topic of LRS-dependent HRS retention yet.

In this work, we investigated the influence of LRS on HRS retention of CBRAM in 1-kb one transistor and one resistor (1T1R) array. The HRS cells, programmed from different LRS, were baked on vacuum oven under elevated temperature for retention measurement. We found the HRS retention was strongly dependent on the



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^{*} Correspondence: lvhangbing@ime.ac.cn

Laboratory of Nano-Fabrication and Novel Devices Integrated Technology, Institute of Microelectronics, Chinese Academy of Sciences, #3 Beitucheng West RoadChaoyang District, Beijing 100029, China

LRS: The lower R_{LRS} was, the worse HRS degradation would be. This behavior was explained by a comprehensive physical model based on metal diffusion mechanism.

Method

A 1-kb array of N-MOS transistor was fabricated by standard 0.13- μ m CMOS technology, followed by integrating Cu/HfO₂/Pt RRAM stack on the copper plug on the drain terminal of the transistors, as shown in Figure 1. The HfO₂ and Pt were about 4 and 70 nm, respectively, deposited by ion beam spurting. The size of RRAM cell was about 300 nm × 400 nm, defined by the area of copper plug. Finally, the 1T1R array was covered by a SiO₂ passivation layer, leaving the test pads exposed. Electrical measurements were performed using Keithley 4200 analyzer. HRS retention characteristics were measured by baking the samples in a vacuum oven at an elevated temperature of 150°C, whose resistances were then checked periodically by a self-made array test system after device cooling down to room temperature.

Results and discussion

During the FORMING/SET operation, the wordline (WL) was forced by a positive bias to open the access transistor and provide compliance current. Positive sweeping voltage was applied on the sourceline (SL) to switch the memory cells from HRS to LRS with bitline (BL) kept on ground and vice versa for the RESET operation. Typical current-voltage (I-V) switching characteristics (FORMING, SET, and RESET) in DC modes are illustrated in Figure 2. The FORMING voltage was about 1.2 V, the SET and RESET voltages were about +0.6 and -0.75 V, respectively. Figure 3a shows the global statistical distribution of R_{LRS} and R_{HRS} . R_{LRS} is distributed in a range of 300 Ω to 1,000 Ω , whereas R_{HRS} concentrates around 10 k Ω approximately. To elucidate the influence of LRS on HRS retention properties, the R_{LRS} was divided into four groups (marked as 'G-n' below) as follows: G-1: 300 Ω to 400 Ω , G-2: 400 Ω to 500 Ω , G-3:



500 Ω to 600 Ω , G-4: >600 Ω . The distributions of four groups screened LRS (solid-symbol lines) and corresponding HRS (open-symbol lines) were plotted in Figure 3b. It should be noted that although the LRS were different, the consequent HRS distributions were nearly the same. The reason for this result will be discussed later.

The programmed HRS cells were then baked in a vacuum oven at 150°C. The resistance of each cell was periodically checked by a self-made array testing system after cooling down to room temperature. The criterion for judging whether the HRS failed or not was defined by $R_{\text{HRS}} \leq 5 k \Omega$. Figure 4 shows the dependence of the HRS on the baking time. As the baking time increases, the resistance of the HRS decreases gradually. Figure 5a-d shows the cumulative probabilities of HRS for G-1, G-2, G-3, and G-4, after baking for 0, 10, 50, and 100 h, respectively. Similar evolution trends (shift towards the left) of HRS were found in the four groups as the baking time increased. For the sake of presenting the difference of





HRS retention in the four groups clearly, the cumulative probabilities of HRS after a 100-h baking were plotted in one coordinate, as shown in Figure 6. It can be clearly found that the degradation of the HRS programmed from the lowest LRS is worse than that from higher LRS, obeying a relation of G-1 < G-2 < G-3 < G-4. That is to say, the $R_{\rm HRS}$ retention is highly dependent on LRS. This result is further supported by summarizing the failure percentage of the HRS at different baking times in the insert of Figure 6.

Taking the failure mechanism of retention into account, more attention should be paid on the reconstruction of the conductive filament, which is responsible for the HRS failure behavior (switching to LRS). Based on our previous work [5], the reversible switching between HRS and LRS of Cu/HfO₂/Pt device is in accordance with the filling and extraction of the copper species from the filament constriction, as sketched in Figure 7. Considering the HRS is in the range from 9 k Ω to 15 k Ω , which is on the level of the quantum conductance, the assumption of the HRS corresponding to a tiny tunnel gap or neck bridge with atomic size in the filament is reasonable according to the quantum point contact (QPC) model [23-25]. Due to the existence of concentration gradient, metal diffusion from the metal-rich filament to the neck point possibly happened, resulting in a gradual decrease of the HRS by the filling or widening of the neck point. In the environment with elevated temperature, the diffusion process was enhanced, leading to the acceleration of the HRS retention failure.

According to the filament theory, the LRS resistance is related closely with the filament size [26,27]. The lower LRS corresponds to the stronger filament. The HRS resistance is mainly determined by the tiny





tunnel gap or neck bridge formed during the RESET process. Although the residual filament or the contact resistance may also contribute to HRS resistance, they are much less than the component from the tunnel junction. That is the reason why the HRS of the four groups is at the same level. Figure 8a and b shows the schematic views of HRS programmed from low LRS and high LRS, respectively. Considering both of the cells having a similar HRS, the lengths of tunnel gap or the widths of neck bridge are the same. The main difference for the two cases is the size of the residual filament. Since the metal diffusion from the residual filament to the tunnel gap or neck bridge results in the HRS failure, the case of a smaller residual filament has less probability of gap filling because of the lower copper diffusion, as shown in Figure 8a. In contrast, the HRS with a stronger residual filament gets a higher probability of gap filling, corresponding to worse data retention (Figure 8b). Although the stronger filament can improve the LRS retention, based on the above results, it is not helpful to HRS retention. As a result, we can come to such a conclusion that









there exists a balance between the HRS and LRS retention.

Conclusion

In this letter, we have investigated the influence of LRS on the HRS retention by taking the Cu/HfO₂/Pt-based CBRAM in a 1-kb 1T1R array as the object. For the retention measurement, the HRS cells which were programmed from different LRS were baked on a vacuum oven under elevated temperature. The results clearly show that the HRS degraded faster as the lowering of the R_{LRS} . The HRS corresponds to a tiny tunnel gap or neck bridge with atomic size in the filament and would fail to LRS when the gap is filled by the metal diffusion from the residual filament. The lower LRS corresponding to the thicker residual filament, which makes higher probability of gap filling by metal diffusion, results in the worse HRS retention.

Abbreviations

RRAM: resistive random access memory; HRS: high resistance state; LRS: low resistance state; Cu: copper; Pt: platinum; HfO₂: hafnium oxide; 1 T-1R: one-transistor-one-RRAM; QPC: quantum point contact; R_{LRS} : resistance of the LRS; ECM: electrochemical metallization mechanism; CBRAM: conductive bridge random access memory; CF: conductive filament; VCM: valence change mechanism; MOS: metal oxide semiconductor; CMP: chemical mechanical polishing; SiO₂: silicon dioxide; WL: word line; SL: source line; BL: bite line; R_{HRS} : resistance of the HRS.

Competing interests

The authors declare that they have no competing interests.

Authors' contributions

XXX and HTL prepared the samples and electrical measurements. XXX and HBL contributed to the design and experimental result analysis and wrote the manuscript. MW, GMW, and MYZ provided technical support to the study. All authors read and approved the final manuscript.

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