ERRATA

**Erratum: “Dynamic Reconfiguration of the FPGA with the Use
of Compressed Bit Streams”
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RETRACTION NOTE

The article “Dynamic Reconfiguration of the FPGA with the Use of Compressed Bit Streams” (DOI: 10.1134/S1063739711070171) by *V. A. Shaltyrev, K. A. Shaltyrev, and I. I. Shagurin*, which appeared in *Russian Microelectronics*, 2011, vol. 40, no. 7, pp. 502–508, has been retracted because one of the co-authors submitted the article without other co-authors’ consent. The article should not be cited.