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Field Effect Transistor Based on Layered NiPS₃

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Layered metal phosphochalcogenides of molecular formula, MPX₃ (M = Mn, Fe, Co, Ni, etc and X = S, Se) have been emerging as new class of semiconductors towards various catalytic and optoelectronic applications. The low cleavage energy associated with these layered chalcogenides may lead to devices with very thin semiconductor channels. Herein, we report the first successful fabrication of field effect transistor (FET) using layered NiPS₃ that reveals n-type semiconducting behavior. Devices using bulk and few-layer NiPS₃ with gold contacts show on/off ratios of ~10³–10⁵ at 25 °C. The device characteristics reveal an increase in on-state current with decrease in threshold voltage and the Schottky barrier height is extracted to be 112 meV. Density functional theory calculations reveal various parameters that affect electron/hole doping in the layered phosphochalcogenide material.

Among the 2-dimensional layered materials, graphene has received considerable attention due to its ultrahigh mobility and tunability of layer thickness^{1,2}. However, the zero band gap of pristine graphene limits its use in optoelectronics and other applications. Transition metal dichalcogenides (TMDs) such as MoS₂ form the next class of well-studied compounds with certain band gap tunability^{3–12}. Bulk MoS₂ is semiconducting in nature with an indirect band gap of ~1.2–1.4 eV¹³, while mono-layer MoS₂ possesses direct band gap of 1.8 eV¹⁴. This has led to various fundamental studies in the areas of electronic, optoelectronic and ultrasensitive sensors with atomically thin MoS₂ membranes^{15–23}. Though the TMDs (MoS₂, WSe₂) have shown high on/off ratios with tunable band gap in the visible wavelength range^{24,25}, the low carrier mobility is of concern. Recently, phosphorene has been looked at, as a potential candidate for optoelectronic, electronic devices and sensors^{26–28}, owing to its high on/off ratio coupled with high carrier mobility^{29,30}. The disadvantage though is its stability that is still being tackled. Both TMDs and black phosphorus possess small band gaps thus restricting their applications in optoelectronics using light of short wavelength field effect transistors (FETs)^{20,26}.

Continuous search for new 2D-materials has recently led to a well-studied class of bulk layered semiconducting metal phosphotrichalcogenides with formula MPX₃ (M = Ni, Fe, Mn, Co, V, Zn etc; X = S and Se). This class of layered compounds have been well-explored in the latter half of 20th century towards understanding their crystal structure and intercalation properties^{31–39}. However, little is known as for few layer MPX₃ materials are concerned and recently, this area has been attracting considerable attention^{40–42} particularly towards catalysis^{43–47} and UV photodetector⁴⁸. A recent review⁴⁹ highlights the importance of this class of materials and their multifunctionality. The MPX₃ family of materials possesses wide variation of band gap values from 1.3 eV to 3.5 eV. The cleavage energy of MPX₃ is reported to be similar and in certain cases, lower than that of graphene and TMDs^{41,50}. Depending on the nature of metal ion, MPX₃ family of compounds may open up ways to fabricate field effect transistors which is still a missing link in the current literature. A recent report on the first principle calculations on MnPSe₃ reveals transformation from anti-ferromagnetic semiconductor to ferromagnetic half-metal by carrier doping³⁷. Raman spectroscopic studies on certain stable MPX₃ compounds has been recently reported^{40,41}. However, electronic devices based on these classes of materials such as field effect transistors have not been explored in the literature so far.

Among the MPX₃ family of semiconductors, NiPS₃ is a layered ternary metal thiophosphate with monoclinic crystal system containing two molecular formula units per unit cell (Ni₂P₂S₆), with point group, C2/m. NiPS₃ is comprised of layers of covalently bonded units of (P₂S₆)⁴⁻ bipyramids with honeycomb arrangement of divalent nickel ions. The sulfur atoms are hexagonally arranged along the c-axis in ABCABC sequence. The inter layers are connected through weak van der Waals forces similar to other layered 2D-materials such as TMDs. Hence, it is possible to separate the layers by mechanical exfoliation. Bulk NiPS₃ is anti-ferromagnetic with magnetic moment of 3.9 BM⁵¹. The reported band gap of bulk NiPS₃ is ~1.6 eV⁴³.

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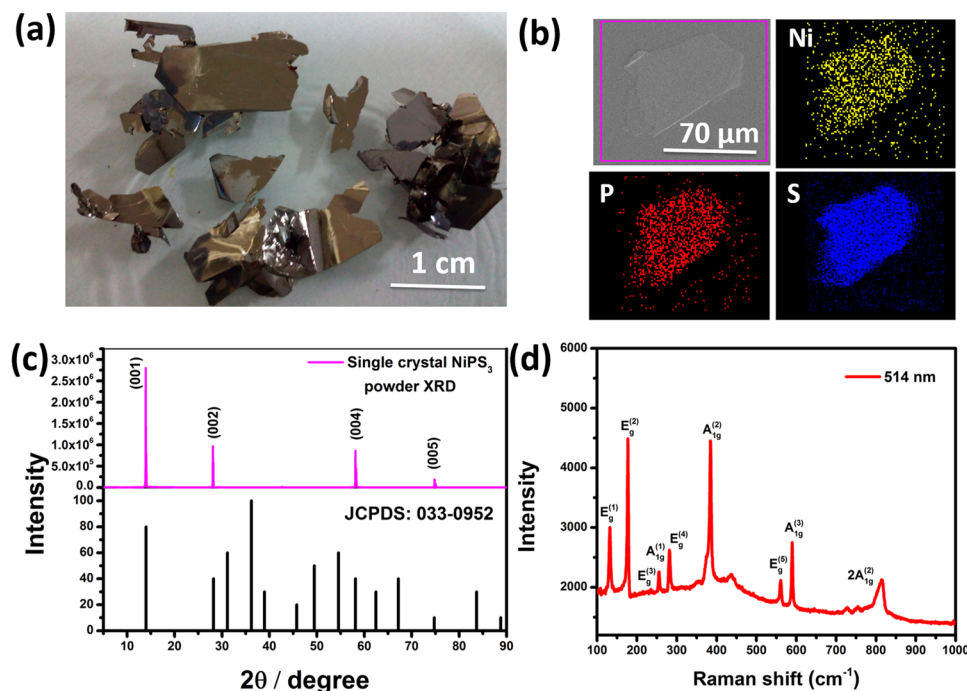


Figure 1. (a) Photographic images of NiPS₃ crystals. (b) Scanning electron microscopic image with elemental mapping of mechanically exfoliated few layer NiPS₃ on Si/SiO₂ along with (c). XRD pattern of the bulk crystals and the corresponding standard pattern. (d) Raman spectrum of NiPS₃ crystals obtained with 514 nm excitation laser.

In the present study, we have synthesized fairly large sized, oriented crystals of NiPS₃ using a high temperature solid state method and explored its use in FETs. The material is thoroughly characterized using X-ray diffraction (XRD), Raman spectroscopy, transmission electron microscopy (TEM) and atomic force microscopy (AFM). Field effect transistors (FETs) have been fabricated and temperature dependent electrical transport measurements have been carried out. Density functional theory (DFT) calculations reveal the possible parameters that help understand the carrier-type observed in the electrical transport studies.

Result and Discussion

Highly oriented crystals of NiPS₃ obtained in the present study are shiny black in colour with high crystallinity and the X-ray diffraction pattern shows very high orientation in the (001) direction. Figure 1(a–c) shows the optical images, scanning electron microscopic image with elemental mapping and the XRD pattern of large sized crystals. The Raman spectrum of the bulk material (Fig. 1d) shows a band at 253 cm⁻¹ which is assigned to the A_{1g}⁽¹⁾ mode. The high intense peak at 176 cm⁻¹ is due E_g⁽²⁾ vibration and the one observed at 384 cm⁻¹ is due to symmetric stretching vibration of P-S bond in the P₂S₆ units (assigned as A_{1g}⁽²⁾). The bands at 236 cm⁻¹, 280 cm⁻¹, 560 cm⁻¹ and 588 cm⁻¹ are assigned to E_g⁽³⁾, E_g⁽⁴⁾, E_g⁽⁵⁾ and A_{1g}⁽³⁾ modes^{40,51,52}. The bulk electrical conductivity has been measured to be 1.64 × 10⁻⁷ S/cm at 25 °C. TMDs such as 2H MoS₂, MoSe₂, WSe₂ and MoTe₂ show values of 3 × 10⁻², 2 × 10⁻¹, 6 and 1.8 S/cm respectively at 25 °C⁵³.

The trans conductance (g_m) value of NiPS₃ obtained from the fabricated FET devices is 2.5 μS/cm. The TMDs, on the other hand show values of 0.5–3 μS/μm for back-gated devices¹⁶. The morphology and microstructure of exfoliated NiPS₃ nanosheets are given in Fig. 2. Spatially resolved EDS elemental mapping of Ni, P and S elements reveals a ratio of 1:1:3 as expected with uniform distribution of the elements obtained over the entire surface. The HRTEM image shows well-resolved lattice fringes along (002) plane, confirming the quality of the crystalline nanosheet (Fig. 2b). The selected area electron diffraction (SAED) pattern displays a single set of diffraction spots, further confirming the oriented nature of the exfoliated nanosheets (Fig. 2c).

The low cleavage energy⁴¹ reported for NiPS₃ makes it possible to fabricate devices containing few to several layers of the material. The bulk crystals are exfoliated by mechanical means (Fig. S2) as reported for graphene-based materials⁵⁴. The devices used in the present study are prepared by transferring the material onto pre-fabricated contact pads on highly doped silicon as the back gate with 230 nm SiO₂ dielectric by standard mechanical exfoliation method using scotch tape. The source-drain metal contacts are given using 10/60 nm ITO/Au. The flakes in the devices have been characterized using optical microscopy, Raman spectroscopy (with excitation wave length of 514 nm) and the thickness is measured using atomic force microscopy (AFM). Several micron-sized, large flakes with thicknesses ranging from 1.5–10 nm have been achieved in the devices. The characteristics discussed in the present study are for devices with varying thicknesses of 1.5–60 nm consisting of 2–75 layers.

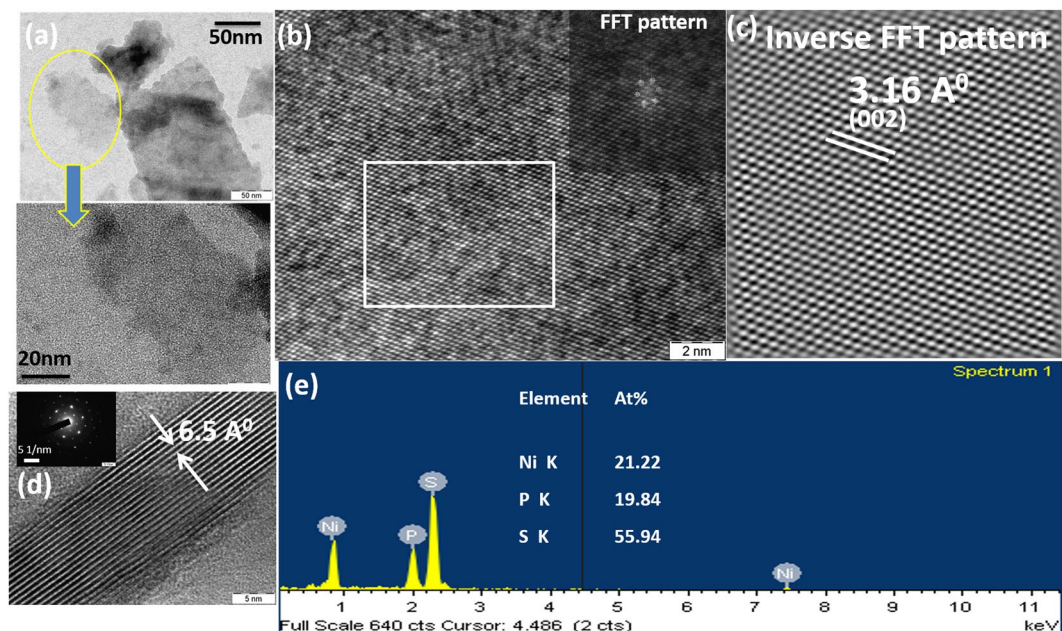


Figure 2. (a,b,d) High resolution TEM images of mechanically exfoliated few-layer NiPS₃ showing an interlayer spacing of 6.5 Å and the corresponding (c) FFT and (e) EDS mapping show the elemental ratio of Ni, P and S to be ~1:1:3.

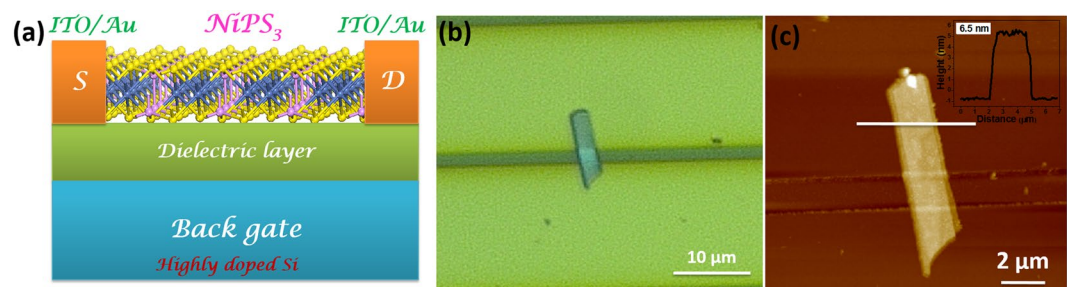


Figure 3. (a) Schematic of few-layer NiPS₃ field effect transistor. (b) Optical micrograph of a typical NiPS₃ device. (c) Atomic force microscopic image of 6.5 nm NiPS₃ flake on Si/SiO₂ with ITO/Au contact pads and the corresponding height profile measured across NiPS₃ flake.

The back gated field effect transistors (FETs) have been fabricated and the characteristics followed. The data given below (Fig. 3) is for a channel length of 2.5 μm. The schematic of the FET along with the optical image, AFM picture and the height profile are shown in Fig. 3. It is clear that a large size single flake is present within the channel and the height is measured to be ~6.5 nm. This corresponds to 6 to 8 layers based on the single layer thickness of around 8 to 11 Å⁴⁰. Standard transistor measurements have been carried out in vacuum (~10⁻⁵ mbar) and to ensure reproducibility, we have carried out measurements using several tens to hundreds of different devices.

The on-off ratio, threshold voltage, carrier type and mobility have been evaluated. The output characteristics of the devices, source-drain voltage (V_{ds}) vs. source-drain current (I_{ds}) at different gate voltages are shown in Fig. 4. The non-linear behavior possibly arises due to the schottky barrier contact with Au metal. The current increases with increasing positive gate voltage, suggesting n-type semiconducting behavior. The on-off ratio estimated based on the transfer characteristics is ~10³–10⁵ for most of the devices (Fig. 5) at 25 °C. The field effect mobility is extracted from the I_{ds} - V_{bg} curve using the following expression⁵.

$$\mu = \left[\frac{dI_{ds}}{dV_{ds}} \right] \times \left[\frac{L}{WC_i V_{ds}} \right] \quad (1)$$

where μ is the mobility, W (3 μm) is the channel width, L (2.5 μm) is the channel length and C_i (1.5×10^{-4} F/m²) is the capacitance between the channel and the back gate per unit area ($C_i = \epsilon_0 \epsilon_r / d$; $\epsilon_0 = 8.85 \times 10^{-12}$ F·m⁻¹; $\epsilon_r = 3.9$; $d = 230$ nm). The mobility values are determined to be ~0.5–1 cm²/Vs. It is low as compared to several devices known in the literature. However, it is possible that poor contact with the metal electrode may be responsible as reported for MoS₂ and WSe₂^{55,56}. The mobility may be improved further by using high-K dielectric materials in top gated devices and is being presently studied.

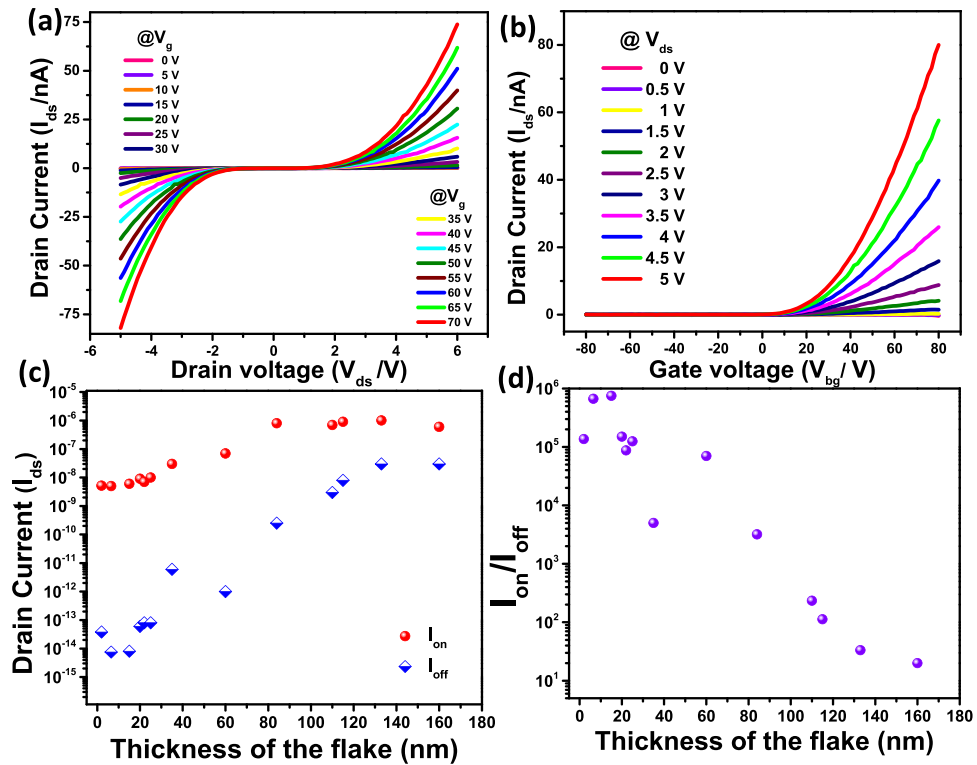


Figure 4. Electronic properties of few-layer NiPS₃. (a) Drain current versus drain voltage for various V_g values as given in the figure, at 25 °C. (b) Drain current versus back gate voltage for V_{ds} ranging from 0 to 5 V. The FET characteristics obtained for a flake thickness of 6.5 nm NiPS₃. (c) Thickness dependent I_{on} (red) and I_{off} (blue) currents. (d) Thickness dependence of I_{on}/I_{off} ratio.

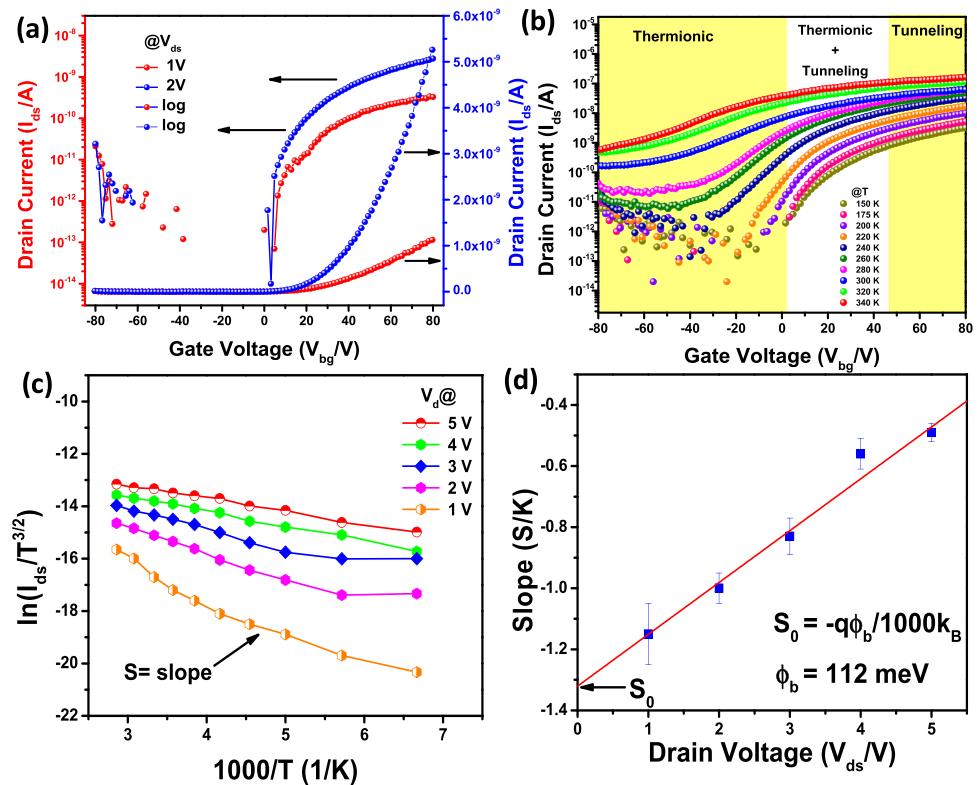


Figure 5. (a) Source-drain current as a function of gate voltage. (b) Temperature dependent transfer characteristics of the device. (c) Arrhenius plots of $\ln(I_{ds}/T^{3/2})$ vs $1000/T$. (d) Slope extracted from Fig. 5c as a function of V_{ds} . Φ_B is derived from the y-intercept, S_0 .

A threshold voltage of 10 V is estimated by extrapolating the transfer characteristics. The $I_{\text{on}}/I_{\text{off}}$ ratio as a function of thickness of the material is shown in Fig. 4d and it is observed that for thicknesses less than 60 nm, the $I_{\text{on}}/I_{\text{off}} \sim 10^5$ and it is of the order of 10^4 – 10^2 when the thickness is in the range 60–100 nm. Further increase in thickness leads to small $I_{\text{on}}/I_{\text{off}}$ ratio. This indirectly points to depletion length of ~ 100 nm for the present device. The $I_{\text{on}}/I_{\text{off}}$ ratio for MoS₂ has been reported to be in the range of 10^7 – 10^4 for thicknesses varying from few nm to 250 nm⁵⁷. The relationship between the depletion length with various parameters of the material is given below (Equation 2),

$$W_{\text{max}} = \sqrt{\frac{4kT\epsilon_s\epsilon_r\ln\frac{N_d}{n_i}}{q^2N_d}} \quad (2)$$

where, k is the Boltzmann constant, T is absolute temperature, q is elementary charge, ϵ_r is the vacuum permittivity, ϵ_s is the relative dielectric constant of NiPS₃ (~ 9.2)⁵⁸, n_i is intrinsic carrier concentration $6 \times 10^5 \text{ cm}^{-3}$, and N_d is unintentional doping level. NiPS₃ is known to be an intrinsic semiconductor with low conductivity⁵⁹. For a depletion length of ~ 100 nm, the N_d works out to be $1.5 \times 10^{17} \text{ cm}^{-3}$ that points to certain level of doping in the material. It should be noted that NiPS₃ possesses $[\text{P}_2\text{S}_6]^{4-}$ clusters that leads to 'ionic' interactions with Ni²⁺ in the material⁶⁰. The contributions of thermionic emission current component ($I_{\text{thermionic}}$) and the thermally assisted tunneling current component ($I_{\text{tunneling}}$) under different gate voltages indicate band bending at the metal - semiconductor interface. The Schottky barrier height (SBH) at the interface has been determined based on thermionic emission model using temperature dependent transport behavior. Figure 5b shows the device characteristics at different temperatures and the change in current with the gate voltages is plotted (Fig. 5c) to extract the SBH using the following relationship (Equation 3).

$$I_{\text{ds}} = AA^*T^{3/2}e^{-\frac{q}{kBT}\left(\Phi_B - \frac{V_{\text{ds}}}{n}\right)} \quad (3)$$

where A is contact area of the junction, A^* is the Richardson constant, q is magnitude of electron charge, Φ_B is the Schottky barrier height, k_B is Boltzmann constant, n is ideality factor, V_{ds} is drain-source voltage and T is the temperature. For the device shown in Fig. 3, the values of $\ln(I_{\text{ds}}/T^{3/2})$ are plotted against $1000/T$ at different V_{ds} as shown in Fig. 5c. The slope at each bias is determined and plotted as a function of source-drain bias as shown in Fig. 5d. The intercept observed in Fig. 5b is determined (S_0). Using the equation 4,

$$S_0 = -\frac{q\Phi_B}{1000K_B} \quad (4)$$

the Schottky barrier height is calculated to be 112 meV for the 8 layer device of NiPS₃. This value is similar to that observed for transition metal chalcogenide based devices^{21,61}. Based on the small SBH determined from the transfer behavior, it may be expected that FET would show n-type unipolar behaviour and consequently the barrier height for holes is high [(band gap – SBH) for electrons].

FET devices based on bi-layer and bulk NiPS₃. The output characteristics of the bi-layer NiPS₃ device, source-drain voltage (V_{ds}) vs. source-drain current (I_{ds}) at different gate voltages are shown in Fig. S3. The current amplitude increases with increasing positive gate voltage as observed for the n-type behavior of eight-layer device. However, the current values for the bi-layer NiPS₃ device are quite low as compared to the multilayer device. It has been reported by Kim and co-workers⁶² that density of states for multilayer MoS₂ is several times larger than that of single layer MoS₂ thus making multilayers attractive for device applications with considerable drain currents. The on-off ratio estimated based on the transfer characteristics is $\sim 10^5$ for the bi-layer devices and the mobility has been estimated to be $\sim 0.5 \text{ cm}^2/\text{Vs}$. The variation of mobility as a function of thickness of the flake (Fig. S4) is similar to the observations on reported TMD-based devices⁶³. This is possibly due to scattering caused by extrinsic charge impurity that decreases as the number of layers increases, as reported for MoS₂⁶³. The transport behavior for the device with 60 nm thick flake (Fig. S5) is similar to that observed for other thicknesses. The current values are large with on-off ratio and mobility of $\sim 10^2$ – 10^3 and $\sim 3.5 \text{ cm}^2/\text{Vs}$ respectively.

DFT studies. Density functional theory (DFT) calculations have been performed to decipher the electrical transport behavior of NiPS₃. The crystal structure of NiPS₃ (Fig. 6a) illustrates that the $(\text{P}_2\text{S}_6)^{4-}$ units are in staggered configuration where all sulphur atoms are coordinated to Ni in distorted octahedral environment and the van der Waals interlayer distance between S...S is 4.422 Å. The optimized geometry illustrates that the $(\text{P}_2\text{S}_6)^{4-}$ units show dihedral (angle involving S-P-P-S structure) angle of 180°. The six S-P-P angles in the $(\text{P}_2\text{S}_6)^{4-}$ unit being almost equal (Table S1), reveals that there is no strain present in the P_2S_6 clusters even when Ni is present in the lattice (Fig. 6b). On the contrary, the NiS₆¹⁰⁻ units clearly reveal distortions in the structure. The distortion parameters (distortion angle, denoted as $\sigma^2\text{oct}$)^{64–66} are measured as proposed by Robinson *et al.*⁶⁷. Based on the structure (Fig. 6c), it is seen that there are twelve different bond angles that can be measured and the values are given in the Supporting Information (Table S1). The average $\sigma^2\text{oct}$ determined for neutral NiPS₃ is 26.02°.

The DOS calculations (Fig. S6a) confirm the presence of up and down spins in the frontier region. The atom projected DOS given in Figure S7 indicates that the 3d bands of Ni (spin-up) are well-mixed with the 3p bands of S confirming strong hybridization as shown in SI (Fig. S6b,d). The upper valence bands are comprised of 3d orbital of Ni and 3p orbital of S. Sulphur dominates the valence band region whereas nickel contribution is maximum at the conduction band region. The orbital projected DOS of individual atoms reveals the presence of large population of 3p orbital of S at the Fermi whereas the contribution by phosphorus is very minimum. This may be due to the strong P-P covalent bond that appears in the lower valence band (-5 to -7 eV) energy region (Fig. S6c).

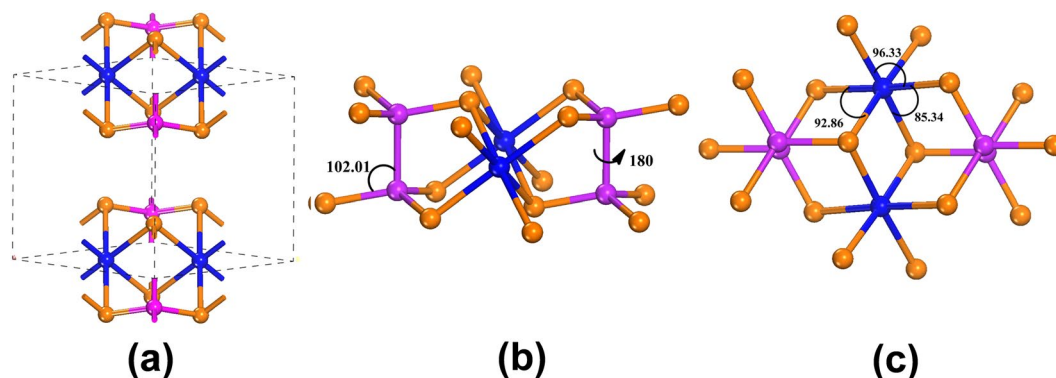


Figure 6. (a) NiPS₃ crystal structure showing various atom positions and van der Waals gap. (b) P₂S₆⁴⁻ units showing the dihedral angle of S-P-P-S and the angle of S-P-P. (c) NiS₆¹⁰⁻ unit with different bond angles. Blue, pink and brown colours represent nickel, phosphorus and sulphur atoms respectively.

The FET characteristics of transistors and the type of conduction (p- or n-type) will depend on the relative position of the frontier region with respect to the metal contacts. To understand this aspect, the structural modulation of NiPS₃ by electron/hole doping has been studied. The parameters obtained for the optimized geometries of neutral and hole/electron doped (0.1e⁻ per atom) NiPS₃ are given in the supporting information (Table S2 and S3 in SI). Same level of theory has been used to perform calculations in all the cases. Doping an electron (0.1e⁻ per atom) to NiPS₃ results in increased (8.01 eV) van der Waals gap and while for hole doping, the gap is found to be reduced (3.20 eV).

Addition of an electron elongates the Ni-S bond length and the opposite effect is observed by hole doping. Very little changes are observed in P-S and P-P bond lengths during electron and hole doping. Further, it is seen that the addition of electron decreases the bond angle distortion in the NiS₆ octahedra quite considerably to nearly zero (1.5°) while the hole doping increases the distortion angle by 12° from the neutral value of 26.02°. This suggests that electron doping leads to stable octahedral geometry around Ni. The changes observed in NiS₆ units confirm the earlier prediction based on DOS calculations that the doping affects the environment around Ni and S. In the case of electron doping, spin up bands illustrate that the conduction band minimum is observed at G k point and the valance band maximum is found in between L and M k point with a band gap of 1.53 eV while the spin down structure becomes metallic (Fig. 7a). Similarly, the hole doping makes it metallic in nature for spin-up configuration and semiconducting for spin-down configuration (Fig. 7b) with an indirect band gap of 0.47 eV. The results show that NiPS₃ changes from semiconducting to metallic nature tuned by electron/hole doping. Magnitude of band gap may also indirectly indicate the stability of materials upon doping. This can be related to the spin flip-gap related stability on doping (Fig. S1).

The atom projected DOS for individual atoms with and without electron/hole doping are shown in Fig. S7. Ni dominates at the Fermi for electron doping while S dominates for hole doping. Interestingly, for electron doped structure, significant increase in the population of Ni is observed near Fermi as compared to neutral NiPS₃. It has moved to lower energy region indicating that the structure stabilizes with electron doping. The Ni population decreases near Fermi and minor reduction at the valance band region is observed for the hole doped material. In the case of S, major changes occur only during hole doping. The above results indicate that the electron doping affects Ni and hole doping affects sulphur and consequently the NiS₆ unit. This is supported by the lowered bond angle distortions during electron doping thus leading to stable NiS₆¹⁰⁻ octahedral environment.

Summary. The present study has shown the possibilities of fabricating field effect transistors using layered phosphochalcogenides, NiPS₃. The FET characteristics show n-type behavior with on/off ratio of 10³–10⁵. The DFT studies have predicted the transport characteristics and are experimentally verified. The phosphochalcogenides with magnetically active centers such as Ni, Co and Mn open ways to flip the spin behavior under magnetic field.

Experimental Section

Synthesis of NiPS₃ crystals. Single crystals of NiPS₃ have been synthesized by chemical vapor transport (CVT) technique using iodine as the transporting agent. Pure elements (99.99%) of nickel, phosphorous and sulphur (Aldrich) in stoichiometric proportions with I₂ (2 mg/cc) were sealed in an evacuated quartz ampoule. After several attempts, the optimum reaction conditions were arrived at, with hot zone temperature of 950 °C and cold zone of 850 °C that result in high quality, large sized crystals of NiPS₃. The schematics of CVT growth setup and the parameters used are given in the supporting information (Fig. S8, S9).

Characterization. The physicochemical nature of the crystals were identified using X-ray diffraction (XRD) (Philips, PAN analytical, with Cu-Kα radiation), transmission electron microscopy (TEM, JEOL 2100 F operating at 200 kV), Raman spectroscopy (LabRAM, Horiba, France, with excitation wavelength of 514.5 nm and 50x long working distance objective) and atomic force microscopy (AFM, Veeco, NanoscopeIVa Multimode AFM, with silicon nitride, Si₃N₄ probes of length 130 μm, width 35 μm, resonance frequency, 270 kHz and force

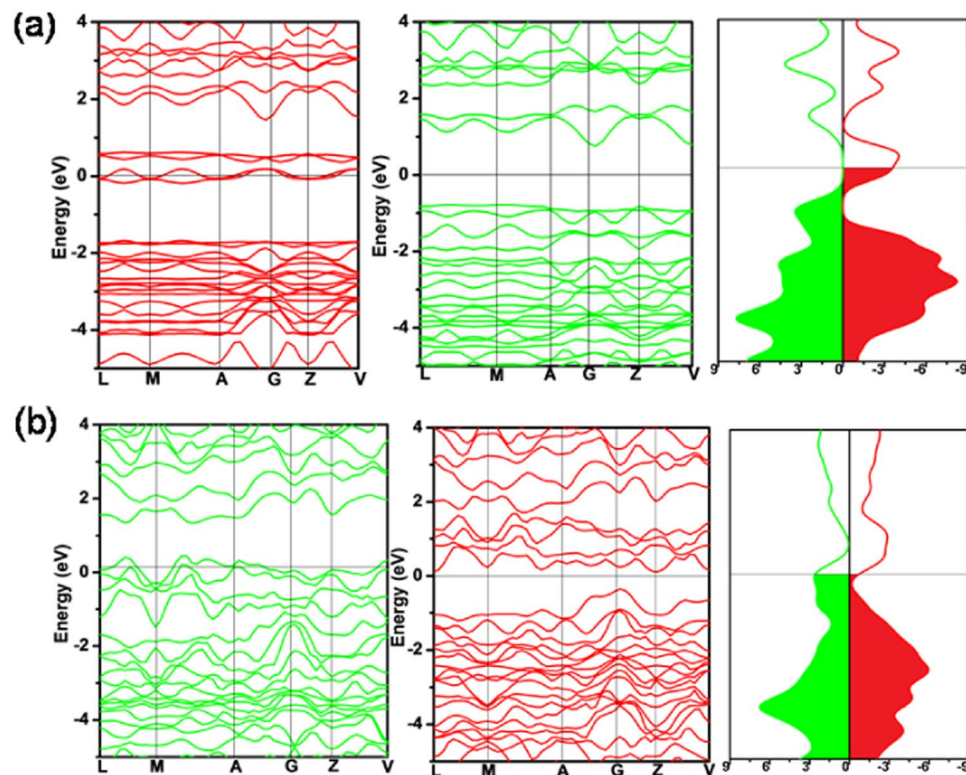


Figure 7. Band structure and total DOS of spin-up (green) and spin-down (red) configurations for (a) NiPS₃ doped with 0.1 electron per atom, (b) NiPS₃ doped with 0.1 hole per atom. The Fermi energy is set to zero.

constant, 4.5 N/m). The samples for TEM measurements were prepared by dispersing few-layer NiPS₃ colloids onto carbon-coated copper grid and dried under vacuum. The electrical measurements were performed under high vacuum ($<5 \times 10^{-5}$) using Agilent B1500 semiconductor parametric analyzer. Devices were prepared by transferring the material onto pre-fabricated contact pads (Fraunhofer IPMS, Germany) on highly doped silicon with 230 nm SiO₂ dielectric using standard mechanical exfoliation method. Source-drain metal contacts were given using 10/60 nm ITO/Au. Highly doped silicon act as the back gate. It was observed that the lithography procedure wherein the sample was exfoliated on Si/SiO₂ surface and subsequently depositing the contact pads on the pre-marked areas led to sample deterioration due to the use of solvents during the process.

Computational methodology. Geometrical optimization calculations for all extended structures has been performed using VASP code^{68,69} with plane wave basis truncated at a kinetic energy of 500 eV. The projector augmented wave (PAW) scheme as incorporated in the Vienna ab initio simulation package (VASP) is used in the study (Supporting Information). Density of states and band structure calculations have been performed followed by optimization using CASTEP package⁷⁰ including LDA + U approximation. Calculations have been performed for neutral NiPS₃ and electron/hole doping separately, and the values are tabulated in Table S1 (Supporting Information).

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Author Contributions

Ramesh Naidu Jenjeti synthesized the material and carried out FET studies. Rajat Kumar helped in carrying out certain transport measurements while Muthu P. Austeria carried out computational studies. S. Sampath conceived the research problem and all authors contributed in the interpretation of data and writing the manuscript.

Additional Information

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