Erratum

The Publisher apologizes for a misprint that appeared in *Analog Integrated Circuits and Signal Processing: An International Journal*, volume 33, number 2, November 2002, “Generation of Technology-Independent

Retargetable Analog Blocks” by R. Castro-López, F. V. Fernández, F. Medeiro and A. Rodríguez-Vázquez, page 160. Figure 3 appears below in the correct form.

Fig. 3. Common-centroid structure for a current mirror.