An alternative approach to the tribological analysis of Si-doped DLC coatings deposited with different bias voltages using Raman spectroscopy mapping

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Abstract

A series of diamond-like carbon (DLC) coatings were deposited with increasing bias voltage using magnetron sputtering techniques. Structural changes were observed in the sp^2 -configuration across the films which were accompanied by a slight increase in the sp^3 fraction. With an increasing bias voltage, the thermal stability of the coatings increased from 300 to 450 °C. Oxygen diffusion was observed through the coating as a result of the high-temperature annealing and found to slow down with increasing bias voltage. Coefficients of friction (COF) remained stable with temperature for the individual coatings, with the softer films reporting the lowest COF. Our approach employed Raman spectroscopy to map the wear tracks at different temperatures, providing a deeper understanding of the coating performance and suggested maximum flash temperatures endured during testing.

Keywords Diamond-like · High-temperature · Raman · X-ray photoelectron · Tribology

1 Introduction

Diamond-like carbon (DLC) coatings have been widely researched during the past decade. These coatings are currently used for automotive, aerospace, biomedical, or manufacturing purposes, amongst others [1–4]. The wide range of applications of these films arises from their remarkable properties, such as low friction and high wear resistance, chemical inertness, nano-smoothness, or even anti-bacterial properties [5, 6].

These coatings have also some limitations complicating their wider application, such as the low thermal stability, dependence on the sliding environment, or the adhesion strength [7–10]. As the environmental temperature is

increased, these coatings undergo a transformation in their sp^2 configuration, where the existing olefinic bonds become aromatic [11, 12]. Simultaneously, a reduction in the hardness and sp^3 fraction has also been reported, as well as an increasing wear rate with temperature [13–16]. The sliding environment has been reported to affect greatly the surface chemistry of these films, reporting large differences in COF in dry environments and humid, as well as between those coatings deposited with hydrogenated precursors and without them [7, 17, 18].

Doping the films has increasingly appeared as an opportunity to overcome the existing limitations and even provide new properties. Silicon doping, as studied in this work, has been reported to increase the thermal stability of the films, while having positive effects on the tribological properties due to the formation of silicon-oxides on the surface of the films [19–21]. Although the mechanical properties have been observed to drop with silicon doping up to a defined content threshold [22], alternative dopants can be employed to reduce the compressive stress of the film or provide new properties, such as antibacterial [15, 23].

Many plasma deposition techniques rely on bias voltages applied to the substrates to infer changes in the DLC structure through the modification in the ion bombardment energy. Bias voltages have been reported to affect different

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DLC properties, such as the optical properties [24], mechanical properties [25, 26], or topography [25]. There are also reports about their impact in other parameters, such as growth rate, density, compressive stress, or even hydrogen content [25–27]. Nonetheless, improvements in the aforementioned parameters are only obtained within a threshold [28], as too-high bias voltage may result in intense ion bombardments [29], causing increasing deposition temperatures and thermal spikes upon arrival to the coating, which may induce relaxation and changes in the sp^2 configuration [26, 29, 30].

In this work, we investigated the structural changes in the sp^2 and sp^3 configuration with increasing bias voltage, by Raman spectroscopy and x-ray photoelectron spectroscopy (XPS). The evolution of the tribological properties with temperature for each of the studied films was examined, as well as their increased thermal stability. The effect of the bias voltage on the oxygen diffusion with increasing temperature was also studied. Finally, an alternative analysis of the sp^2 -configuration after tribological testing was presented, providing new insights that could help probing experimentally the maximum flash temperatures reached during contact.

2 Experimental methodology

Silicon-doped DLC coatings (1.4 ± 0.2 at. %) were deposited at Teer Coatings Ltd. (Worcestershire, UK) on M42 high-speed steel specimens of 30 mm diameter using increasing bias voltages, following the procedures described previously [16, 31].

Raman spectroscopy measurements ($\lambda = 532$ nm) were performed with minimised power using an inVia reflex Raman microscope (Renishaw PLC., Wotton-under-Edge, UK). The spectra were recorded for 10 s during 10 accumulations, from 300 to 2400 cm^{-1} . The central area of the wear track was mapped using StreamLine™ Raman mapping. The area mapped was slightly modified as the temperature was increased due to the increasing wear track width (Y-axis), while the wear track length mapped (X-axis) was kept constant at $30\text{ }\mu\text{m}$. The fitting process was automated through a custom-built script where +8000 spectra were fitted using a double Gaussian model.

Both hardness (H) and reduced elastic modulus (E') were acquired using NanoTest Xtreme (Micromaterials Ltd., Wrexham, UK) with a diamond Berkovich indenter. The films were indented under depth-controlled conditions, ensuring that the depth remained below 200 nm, which was below a tenth of the coating thickness to minimise the influence of surface roughness and substrate.

XPS was employed to measure the silicon content within each of the films, as well as any existing difference in terms

of carbide formation, and C-C sp^3 content induced by the different bias voltages applied. These spectra were acquired using a Kratos Axis Ultra DLD (Kratos Analytical, Manchester, UK). The work function and binding energy scale were calibrated before any experiment against the Fermi edge and $3d_{5/2}$ peak of a polycrystalline Ag sample. Clean metallic foils were used to determine the analyser transmission function and the detection efficiency for the compositional analysis, using Al $K\alpha$ ($h\nu = 1486.7\text{ eV}$) x-ray beams were used in both cases. The CasaXPS software was used for peak fitting with Gaussian–Lorentzian line shapes. The background of the analysed photoelectron emission band was subtracted using the Shirley method [31].

Tribological testing was carried out using a Multi-Function Tribometer MFT-5000 (Rtec Instruments, Inc., San Jose, CA, USA). A linear speed of 1 cm s^{-1} and a frequency of 1 Hz were defined for the experiments. Additionally, a load of 10 N was applied over 6.3 mm diameter Al_2O_3 balls that were used as counterparts. These experiments were carried out at different temperatures until the coating failure, which ranged from room temperature to $450\text{ }^\circ\text{C}$. The temperature was calibrated to ensure that the defined values were reached on the surface of the coating, and prior to the tribological contact, both elements were annealed for 40 min, accounting for temperature homogenization. Specific wear rates (SWR) were estimated by the relationship between both the volume of material removed and the relationship between the load applied and sliding distance. The volume removed from each of the wear tracks was estimated using an Alicona InfiniteFocus instrument (Alicona Imagine GmbH, Graz, Austria).

3 Results and discussion

3.1 Raman spectroscopy

Raman spectroscopy was employed to analyse the bonding structure of the Si-doped DLC coatings. These coatings present two main modes under visible excitation, the D and G modes (Fig. 1) [32]. The former presents A_{1g} symmetry and covers the breathing mode of the sp^2 sites in rings, while the latter, with E_{2g} symmetry, covers the in-plane stretching vibrations of all pairs of sp^2 atoms in either rings or chains [12, 33]. These two are the main modes under visible excitation due to the larger cross-section of the sp^2 sites [32]. The fitting process of the spectra led to the extraction of three major parameters, the G peak position, full-width half-maximum of the G peak, FWHM (G), and the intensity ratio between both D and G peaks, I_D/I_G (Table 1).

Increasing bias voltages are often linked to more energetic ions accelerating towards the substrate, which may allow deeper penetration [26, 30]. The G-peak position

Fig. 1 Raman spectra of the studied carbon films: a-C:Si (65 V), a-C:Si (75 V), and a-C:Si (85 V)

was observed to increase with the bias voltage applied during deposition, soaring from 1556 cm^{-1} in a-C:Si (65 V) to 1570 cm^{-1} in a-C:Si (85 V). This could be aligned with an increase in the residual stresses and defects of the films, as a result of such energetic bombardment [26]. The FWHM (G) has been reported to increase with the disorder of the films, with ordered materials dominated by aromatic rings, such as graphite, having reduced values [33]. Nonetheless, no major changes in the FWHM (G) were observed across the films. Such changes were found in the I_D/I_G ratio, where the D-peak was observed to extend its presence [11]. This effect was likely induced by increasing bias voltage modifications that transformed the existing aromatic bonds into olefinic bonds, leading to the observed decrease in the I_D/I_G ratio [34, 35]. The film with the lower bias, a-C:Si (65 V), presented a value of 1.4 that was found to continuously decrease until a value of 1.12 for a-C:Si (85 V).

3.2 X-ray photoelectron spectroscopy

X-ray photoelectron spectroscopy (XPS) was employed to obtain further information on the bonding states through the measurement of both C 1s and Si 2p orbitals. These

films were observed to present similar doping levels, [Si] = 1.4 ± 0.2 at. %, while the [Ar] increased slightly from a-C:Si (65 V), 3.65 at. %, to a-C:Si (85 V), 3.85 at. %, allegedly due to the intense bombardment [36, 37]. The C 1s spectrum was decomposed into four main features: carbon sp^2 contribution, carbon sp^3 contribution, silicon carbide, and residual surface contaminants (Fig. 2). The sp^2 -bonded carbon was centred at 284.3 eV, while the sp^3 -bonded carbon was fixed at a separation of 0.95 eV, following prior results in carbon films without hydrogenated precursors [38–40]. The film with the lower bias voltage, a-C:Si (65 V), presented a C–C/C–H content of 21.9 ± 0.6 , value that was found to progressively increase until the value of a-C:Si (85 V), 23.1 ± 0.4 . Such a trend confirmed a slight change in the coating configuration, as observed previously using Raman spectroscopy. The silicon-carbon bond was found at 283.5 eV, and its relative area was also found to present a correlation with bias voltage. This content ranged from 1.1 % in a-C:Si (65 V) to 2.5 % in a-C:Si (85 V), as presented in Fig. 3. Similar results were observed for Cr-doped films with an increase in the relative area of Cr–C, as the bias voltage was increased [25]. Additionally, surface contaminants in the form of both ether and carbonyl groups (C–O, C=O) were found at 286 and 288 eV, respectively.

The Si 2p orbital, of the films under study, displayed changes as a result of the increasing bias voltage employed during deposition (Fig. 3). The Si 2p orbital was deconvoluted into three well-defined doublets, with their maxima in the $2p_{3/2}$ component, corresponding with the existing silicon bonds within the surface of the films. The major doublet in a-C:Si (65 V) represented Si–O–C and was centred at 101.6 eV. Such a peak has previously been observed to decrease with increasing silicon content [31], although a similar effect was observed in this work as a result of increasing bias voltage. This species has been attributed positive effects on the friction of the films, resulting in a coefficient of friction reductions [41]. Its reduction with bias voltage could therefore play a role in the tribological properties of the films. At the same time, the relative fraction of Si–C, centred at 100.5 eV, and SiO_x , centred at 103.4 eV, soared with the former growing from 3.5% in a-C:Si (65 V) to 10.2% in the a-C:Si (75 V) and 52% in a-C:Si (85 V).

Table 1 Silicon content of each of the films as well as Raman parameters extracted after fitting of the spectra, sp^3 fraction obtained from XPS, and mechanical properties

	Si content (at. %)	G-peak (cm^{-1})	FWHM (cm^{-1})	I_D/I_G	sp^3 C–C content (%)	H (GPa)	E' (GPa)
a-C:Si (65 V)	1.3 ± 0.1	1556 ± 1	167 ± 1	1.4 ± 0.02	21.9 ± 0.4	13.6 ± 1.0	157.7 ± 15.9
a-C:Si (75 V) [16, 31]	1.4 ± 0.1	1557 ± 2	169 ± 2	1.23 ± 0.10	22.5 ± 0.8	16.1 ± 1.0	182.7 ± 7.0
a-C:Si (85 V)	1.5 ± 0.2	1570 ± 2	168 ± 1	1.12 ± 0.02	23.1 ± 0.6	27.2 ± 1.5	243.5 ± 6.5

Fig. 2 C 1s orbital of the films, obtained by XPS, after 1 h of exposure to Ar^+ sputtering to remove contaminants

3.3 Mechanical properties

The H and E' of the films were acquired through depth-controlled indentations, ensuring that the depth remained below a tenth of the total film thickness and limiting any influence from either substrate or surface roughness. The modification of the bias voltage applied to the substrates during deposition had a direct effect on the mechanical properties of the films. As presented in Table 1, both parameters increased from 13.6 ± 1.0 and 157.7 ± 15.9 respectively in the case of the film with the lower bias voltage applied, a-C:Si (65 V), to 27.2 ± 1.5 and 243.5 ± 6.5 in the higher bias voltage film, a-C:Si (85 V). An increase of H due to the application of larger bias voltages has previously been related to the increase in compressive stress due to the enhanced bombardment of ions [26]. The coating with the lowest depositing bias reported the lowest H/E' , usually related to higher plasticity, while a-C:Si

Fig. 3 Si 2p core-level spectra of the as-deposited Si-doped films obtained by XPS

(85 V) could display an improved wear resistance due to its higher H/E' .

3.4 Tribological properties

The tribological performance of the different films was assessed through the testing of the films from room temperature (RT) up to temperatures required for the film to display failure, defined as contact between Al_2O_3 counterpart and the steel substrate. The friction curves for each condition and temperature can be found in Fig. 4, while a summary of both the coefficient of friction (COF) and the specific wear rate are presented in Fig. 5. The sp^2 -configuration modifications, induced by the higher contact temperatures, exhibited at the end of the test are presented in Fig. 6 for a-C:Si (85 V), where areas inside and outside the wear track were analysed through Raman mapping.

For samples tested at room temperature, relatively high flash temperatures were reached in the contact during the

Fig. 4 Evolution of the coefficient of friction with temperature for all tested coatings using a 6.3 mm diameter Al_2O_3 balls as a counterpart, 10 N load, and 1 cm s^{-1} linear speed for **a** a-C:Si (65 V); **b** a-C:Si (75 V); **c** a-C:Si (85 V)

Fig. 5 Summary of the results obtained from ball-on-disk friction tests using Al_2O_3 as counterpart. **a** Averaged steady-state values of the coefficient of friction during the final 1000 strokes, and roughness evolution for each coating. **b** Specific wear rates obtained after tribological testing

running-in of the films, due to the frictional energy generated [42, 43]. Once this period was overcome, graphitised transfer layers are expected to be created on the counterpart due to the low shear strength, providing a reduction of such

friction and stable friction behaviour [44]. The two films with lower bias voltages, a-C:Si (65 V) and a-C:Si (75 V), reported lower COFs than a-C:Si (85 V), as presented in Figs. 4 and 5. Such difference is thought to be related to the increasing Si–C fraction reported earlier, whereby increasing bias voltage changes in the surface chemistry of the film were identified. A reduction in the relative area of Si–O–C within the Si $2p$ orbital was also observed with increasing bias voltage. The former could have driven the COF increase observed at this temperature for a-C:Si (85 V), as it is more abrasive [45], while the reduction in Si–O–C may also affect the tribological properties, since it has been attributed lubricious properties [19, 20]. The graphitization of the coatings induced by the friction was monitored through the change of sp^2 -configuration within these films (Fig. 6, Fig. S1, Fig. S2). These maps allowed the study of the condition of the coating inside the wear track at the end of the tribological test. For instance, a-C:Si (85 V) was found to present a reduction in the G-peak position and a FWHM (G) increase, with no significant changes observed in I_D/I_G (Fig. 6a). Earlier, this has been attributed to a reduction of defects for annealing temperatures below 200°C [46], which may indicate that the contact temperature reached at the end of the contact was below such value. A similar trend was observed for a-C:Si (65 V) (see Fig. S1a) although with smaller values characteristic for a film deposited with lower bias voltage. Both in this specimen and a-C:Si (75 V) (Fig. S2a), the presence of a groove within the wear track was identified as a progressive graphitization localized in a line due to abrasive wear. The latter film presented a clearer impact of the temperature-induced graphitization within the wear track than its peers, with both areas clearly differentiated.

At 150°C , the coefficient of friction presented a twofold decrease (Fig. 4), potentially due to the progressive graphitisation of the transfer layers. The temperature reached at the contact was now compounded by the contact and the environmental temperature [47]. The running-in length was

Fig. 6 Raman mapping of the wear tracks of a-C:Si (85 V) at **a** 25 °C, **b** 150 °C, **c** 200 °C, **d** 250 °C, **e** 300 °C, **f** 350 °C, and **g** 400 °C. Note that the wear track length mapped (X-axis) was kept constant at 30 μm , while the wear track width (Y-axis) was changed to account for the width increase with temperature

found to be directly correlated to the bias voltage employed to deposit the specimens for all temperatures. The mapping at this temperature reported an extended graphitisation inside the wear track (Fig. 6b). The specimens were found to present a progressive transition from olefinic to aromatic bonding which was found to be induced by the contact temperatures reached. This was probed through the G-peak position displacements towards the red, FWHM (G) reductions, and the I_D/I_G ratio increase confirming the rise of the D peak. The changes in the Raman parameters observed within the wear track at RT were now found in the areas outside the wear track. The film deposited with the lower bias voltage, a-C:Si (65 V), was severely affected (Fig. S1), while a-C:Si (75 V) reported intermediate values to a-C:Si (65 V) and a-C:Si (85 V), as observed in Fig. S2., which correlates with the impact of bias voltage on the thermal stability mentioned earlier. The changes in the Raman parameters observed within the wear track at RT were now found in the areas outside the wear track.

As the temperature was increased, a-C (65 V) and a-C (75 V) were found to have a stable COF. At 200 °C, a-C:Si (65 V) presented extended changes within the wear track with an accelerated graphitisation within the wear track that kept progressing, while a-C:Si (75 V) and a-C:Si (85 V) presented limited changes with the lower temperature. At 250 °C, the wear track in a-C:Si (65 V) seemed to have reached stage 1 defined by Ferrari et al. [11]. This was clearly observed in the graphitisation trajectory of FWHM (G) and G-peak position, which decreased from 115 to 100 cm^{-1} and increased from 1580 to 1585 cm^{-1} respectively, between 200 and 250 °C. Additionally, the I_D/I_G ratio displayed an increasing trend from RT up to 200 °C, where it changed and dropped from 1.7 to 1.5 at 250 °C. Nevertheless, the transition started in the areas outside the wear track at 200 °C, as can be seen in Fig. S1c. This is significant, as it confirmed that the coating presents different graphitised states throughout its thickness. Additionally, it also confirmed that the frictional heat generated during the contact was superficial, due to the linear speed, as the coating exposed inside the wear track was not as graphitised as that outside. Similarly, a-C:Si (75 V) presented the same trend, although due to the higher thermal stability, the film had a lower ordering trend, which delayed such behaviour by 50 °C until 300 °C (Fig. S2e). These coatings reached their failure at 300 °C and 350 °C, respectively. At such temperatures, the films were found to display a minimal running-in due to the minimised shear strength [14, 48]. Meanwhile,

their SWR progressively increased with temperature, which is associated with the largest graphitization expected within the wear tracks with increasing temperature. The lowest bias voltage specimen, a-C (65 V), presented larger SWR than any of the other coatings under most temperatures. This can be attributed to its initial state, which was already more graphitized, and would have underpinned a lower COF. This also caused its failure at lower temperatures than its peers.

The film with the highest bias voltage, a-C:Si (85V), was found to present larger signs of graphitisation within the wear track as the annealing temperature was increased. This led to an increasing I_D/I_G inside the wear track, accompanied by an increase in COF up to 300 °C, when the wear track almost reached complete graphitisation. Similar trends were observed with the G-peak position and the FWHM (G), which continued their accelerated aromatic transition within the wear track, induced by the additional frictional energy generated at the contact. This led to I_D/I_G values of 1.8 inside the wear track, while outside they remained at 1.6.

At the end of the 350 °C test, a-C:Si (85V) presented G-peak positions of 1590 cm^{-1} and 1580 cm^{-1} , and FWHM (G) of 110 cm^{-1} and 120 cm^{-1} , inside and outside the wear track respectively, which confirmed the accelerated amorphization trajectory within the track [11]. Meanwhile, the I_D/I_G analysis presented larger values outside the wear track than inside, but still higher than those observed at 300 °C. This indicated that the coating surface was close to a transition into stage 1 from the amorphization trajectory defined by Ferrari et al. [11]. As mentioned earlier, the temperature reached inside the wear track was significantly higher than the annealing, but I_D/I_G was below one expected at this temperature. It is thought that it was caused by the lower shear strength of the films in stage 1, which allowed the removal of the graphitised part of the coating shortly after reaching such state. This could be confirmed by the groove observed in the same figure, caused by material entrapment, which is in the frontier with stage 1, and found in the wear track solely due to its larger depth (Fig. 6f). It was also confirmed that the film displayed a heterogeneous outcome of the annealing. The inner coating was less ordered than the superficial film, and the flash temperatures that originated during the friction test were limited to the surface due to the relatively large tribological velocity [47]. This was also observed at 400 °C, where the area outside the track completely transitioned into stage 1, with drops in FWHM (G) and I_D/I_G beyond those found in the areas inside the track (Fig. 6g). Simultaneously, the G-peak position also increased beyond those values found inside the track. This could assist in the relatively more stable COF found at 350 °C and 400 °C, due to the formation of transfer layers with increased stability. The coating was found to fail at 450 °C, as it reached complete graphitization at the end of the 400 °C test. Nevertheless, a-C:Si (85 V) increased the operative window by

100 °C from that of a-C:Si (75 V), and 150 °C from that of a-C:Si (65 V).

3.5 High-temperature integrity of the coatings

Cross-sections of the coatings outside the wear track after their tribological test at 300 °C were studied by TEM using a high-angle annular dark-field detector (HAADF).

Figure 7 shows the cross-section with the coating sandwiched between a protective Pt layer on the top and the Cr-C inner layer at the bottom, along the EDS analysis of the coatings. These films have been previously reported to display the diffusion of oxygen from the top layer, dark features in Fig. 7, with their ingress point at the joints of the cauliflower-like structures, characteristic for the surface of these films, indicated by A in Fig. 7a [49]. These features were reported to diffuse deeper with increasing temperature, which impacted their adhesion and tribological behaviour, although increasing Si-doping was reported to minimise the depth of such features [31].

It is believed that oxygen diffusion is linked to the low density of the films. As shown in Fig. 7a, both in the HAADF and EDS figures, the dark features reached the interlayer of a-C:Si (65 V) at 300 °C, concurrently with its tribological failure. These features covered the entire coating and were found to form oxides in the interlayer, dark features identified as B. Figure 7b shows a-C:Si (75 V) at the same temperature, while a-C:Si (85 V) displays a reduction in the penetration depth (Fig. 7c). Diamond-like carbon coatings deposited with increasing bias voltages have been often associated with larger densities [12], which could be aiding to minimise the oxygen diffusion. These depositions could also render larger sp^3 fractions, but that is not believed to be

driving the different performance at high temperatures, as were found to remain within error, as presented in Table 1.

4 Conclusions

Si-doped DLC coatings deposited with increasing bias voltages ranging from 65 to 85 V have been studied. The film structure was found to become olefinic with increasing bias voltage, which agreed with an increase in the sp^3 carbon content from 21.9 ± 0.4 in a-C (65 V) to 23.1 ± 0.6 in a-C (85 V). Simultaneously, the hardness doubled from 13.6 ± 1.0 to 27.2 ± 1.5 GPa in the same coatings.

The film deposited with the largest bias voltage, a-C (85 V), was found to have the highest thermal stability of the films, increasing by 100 °C that of a-C (75 V), and by 150 °C that of a-C (65 V). The tribological performance was found to be also affected by the bias voltage, with the softest coating, a-C (65 V) reporting consistently the lowest COF, although not the lowest specific wear rate.

An alternative approach for the sp^2 configuration evaluation of the coatings at the end of the tribological contact was provided. This methodology experimentally confirmed the coating does not present a homogeneous behaviour during annealing. Additionally, it was experimentally measured that the flash temperatures reached inside the contact were superficial and only affected those layers in contact. It is thought that this technique could be developed further and employed to experimentally estimate the final contact temperature reached within the wear track through the understanding of the coating changes with temperature.

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Fig. 7 TEM image of **a** a-C:Si (85 V), **b** a-C:Si (75 V) [31], and **c** a-C:Si (85 V) after tribological testing at 300 °C. TEM foils were extracted from pristine regions outside the wear track

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Declarations

Statement of originality The authors confirm the following:

- This work was done wholly by the cited authors.
- Where published work was consulted, it was clearly attributed, and the source was always provided.
- This work has not been published by another journal and to the best of our knowledge is original work.

Conflict of interest The authors declare no competing interests.

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