

# RFSoC Gen3-Based Software-Defined Radio Characterization for the Readout System of Low-Temperature Bolometers

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# Abstract

This work reports the performance evaluation of an SDR readout system based on the latest generation (Gen3) of AMD's Radio-Frequency System-on-Chip (RFSoC) processing platform, which integrates a full-stack processing system and a powerful FPGA with up to 32 high-speed and high-resolution 14-bit Digital-to-Analog Converters and 14-bit Analog-to-Digital Converters. The proposed readout system uses a previously developed multi-band, double-conversion IQ RF-mixing board targeting a multiplexing factor of approximately 1000 bolometers in a bandwidth between 4 and 8 GHz, in line with state-of-the-art microwave SQUID multiplexers. The characterization of the system was performed in two stages, under the conditions typically imposed by the multiplexer and the cold readout circuit: first, in transmission, showing that noise and spurious levels of the generated tones are close to the values imposed by the cold readout, and second, in RF loopback, presenting noise values better than -100 dBc/Hz totally in agreement with the state-of-the-art readout systems. It was demonstrated that the RFSoC Gen3 device is a suitable enabling technology for the next generation of superconducting detector readout systems, reducing system complexity, increasing system integration, and achieving these goals without performance degradation.

**Keywords** Low-temperature detectors  $\cdot$  Microwave SQUID multiplexing  $\cdot$  Softwaredefined radio readout electronics  $\cdot$  FPGA-based readout electronics  $\cdot$  RFSoC

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#### 1 Introduction

The next generation of Cosmic Microwave Background (CMB) B-mode polarization telescopes requires densely populated focal planes with thousands of ultra-sensitive bolometers operating at cryogenic temperatures to achieve the required sensitivity [1]. However, reading out a large number of detectors such as Transition Edge Sensors (TES) [2] or the recently proposed Magnetic Microbolometers (MMBs) [3] at temperatures below one Kelvin imposes significant technical challenges to the cryogenic readout systems. In the last decade, a technique called Microwave Superconducting Quantum Interference Device (SQUID) Multiplexing (µMUXing) has become predominant because it is able to achieve multiplexing factors in excess of 1000 while maintaining the readout noise subdominant to the intrinsic detector noise [4]. This scheme encodes the detector signals in the resonance frequencies of multiple GHz-frequency superconducting resonators coupled to a common feed line. Therefore, recovering the signals from each detector is straightforward, requiring only monitoring the resonant frequencies. Despite the aforementioned benefits, it imposes stringent requirements for generating and acquiring high-purity broadband microwave signals on the warm electronics and real-time processing without degrading the performance imposed by the cold multiplexing circuit [5].

This task is traditionally tackled using FPGA-based Software-Defined Radio (SDR) systems offering high customizability, large I/O bandwidths, and realtime processing. The FPGA drives a set of high-speed data converters along with analog mixers and other radio-frequency (RF) components to translate these signals to the required operating band [6]. Nowadays, technological advances in telecommunications have led to developments such as the AMD's Radio-Frequency System-on-Chip (RFSoC), which combines a high-performance heterogeneous processing platform with several embedded high-speed digitizers (>1 GSPS) into a System-on-Chip (SoC) [7]. This kind of SoC significantly increases the total bandwidth capabilities while simultaneously reducing system size, weight, and power consumption in comparison with traditional systems.

We seek to improve the state of the art of the frequency-multiplexed SDR detector readout systems by migrating to an RFSoC platform and making use of the extended characteristics of the newest Gen3 devices, such as an improved number of data converters with faster sampling rates and an augmented number of bits [8]. We present the noise performance characterization of a custom RFSoC Gen3-based SDR readout system for low-temperature bolometers multiplexed in the frequency domain by means of the Microwave SQUID Multiplexer ( $\mu$ MUX).

### 2 System Overview

The proposed system shown in Fig. 1 is built around the Zynq<sup>TM</sup> UltraScale+<sup>TM</sup> RFSoC ZCU216 Evaluation Kit [9]. It adapts hardware and firmware architecture developed for The Electron Capture in <sup>163</sup>Ho experiment (ECHo) [10] to the



Fig. 1 RFSoC SDR readout system built around the ZCU216 Evaluation Kit, including an external CLK104 RF clock add-on board and a flux-ramp generation boards (left). Multi-band IQ RF-mixing board (center). Cold multiplexing system and bolometer array (right)

RFSoC Gen3 family of devices. The system is targeting the multiplexed readout of 1000 low-temperature bolometers in the bandwidth between 4 and 8 GHz, in line with state-of-the-art  $\mu$ MUXing systems [4].

The hardware architecture is based on a multi-band, double-conversion, zerointermediate frequency RF-mixing board, which divides the total bandwidth into five 800-MHz complex base-bands [10]. As a consequence, ten DACs and ten ADCs are required to cover the total bandwidth. The adopted sampling rate is 1 GSPS due to the excellent performance in output power, Spurious-Free Dynamic Range (SFDR) and Signal-to-Noise Ratio (SNR) in agreement with the datasheet and previously used converters [8, 11]. Ultra-stable low-noise clock generation is achieved using the CLK104 RF clock add-on card [12] connected to an external 10-MHz frequency reference fanned out through the different boards. Because the multi-band RF-mixing board is still under development, an available single-complex base-band prototype version connected to the RFSoC converters through the XM655 add-on card was used for the characterization [11]. This prototype RF-mixing board comprises the blocks highlighted in red in Fig. 1 and allows us to test a single sub-band using one DAC pair and one ADC pair. Due to the RF-mixing board modular structure, the characterization of a single sub-band is representative of the final multi-band system performance.

Regarding the firmware, the RFSoC Gen3 was integrated into our custom build system based on Yocto and most of the blocks previously developed were reused [13, 14]. The main contribution of this work was the integration and configuration of the new data converters into the firmware design by means of the Radio-Frequency Data Converter RFDC<sup>TM</sup> LogiCORE<sup>TM</sup> IP [15]. Since the same Zynq<sup>TM</sup> UltraScale+<sup>TM</sup> device family is used for the reported development, it also allowed to keep the same Processing System (PS) software, a huge advantage for fast development.

#### **3** System Performance

The cold multiplexing system depicted in Fig. 1 encodes the detector signals into phase and amplitude of the probing tones. Therefore, the noise present at these coordinates is indistinguishable from the detected signals [16]. The scope of this work is to quantify the noise degradation due to the SDR system with respect to the cryogenic low-noise amplifier (LNA) noise within the range of possible frequencies adopted by the flux-ramp modulation for bolometric applications and assuming a phase domain readout [17, 18].

First, the signal generation performance was characterized seeking to ensure the quality of the tones required for monitoring the  $\mu$ MUX channels. For this, a number of N = 200 tones centered at 7.5 GHz were generated with a Gaussian frequency distribution with  $\mu = 4$  MHz spacing and  $\sigma = 200$  kHz deviation. This emulates the resonance frequency distribution of a real multiplexer given by variations in the manufacturing process and allows for the identification of inter-modulation products generated within the readout bandwidth. Comb generation was performed by continuously reproducing the complex waveform stored in Block RAM (BRAM) at a rate of 1 GSPS by the DACs. It has a memory depth of 2<sup>17</sup> complex samples resulting in a frequency resolution of  $\Delta f \approx 7.6$  kHz which is sufficient for frequency placement in bolometric applications where the resonator bandwidth is BW $\approx$ 200 kHz [4]. Then, the base-band signal was up-converted to 7.5 GHz by the RF-mixing board. Figure 2 shows the generated frequency comb at the RF-mixing board transmitter output (Tx). The selected tone power at Tx port is -40 dBm considering an optimum readout power of -75 dBm at the  $\mu$ MUX input port and 35 dB of cold attenuation [19]. In order to utilize the DACs dynamic range efficiently, several Peak-to-Average Power Ratio (PAPR) minimization methods were evaluated [20], but none of them performed significantly better than the random phases method with an average PAPR  $\approx 12$  dB in case of nonuniformly spaced tones.

The transmitter output (Tx) was connected to a R &S FSWP50 phase noise analyzer, where six reference tones were measured regarding their phase noise. This selected subset is representative of the quality of all generated tones. The results



**Fig. 2** Power spectrum of 200 fixed tones generated by the RFSoC-based SDR system proposed in this work using random frequency spacing ( $\mu = 4$  MHz spacing and  $\sigma = 200$  kHz deviation) centered at 7.5 GHz with -40 dBm power per tone. The inset shows a close-in-look at one tone taken as example to see the close-in spurious signals produced by the SDR system

of these measurements are presented in Fig. 3. The shaded green region represents the frequency band where the detector signal flux-ramp modulation will be typically between 10 and 100 kHz for bolometric applications [4, 6]. The dashed cyan line represents the phase noise present in the local oscillator of the RF-mixing board without tone generation. It can be seen that this local oscillator imposes the shape of the stimulation phase noise. The phase noise profile in the band of interest is dominated by Voltage-Controlled Oscillator (VCO) white phase noise and spurious signals, while the quantization noise is negligible. This value is compared with the estimated cryogenic LNA noise level represented in the dashed black line in Fig. 3. LNA Single-Sideband (SSB) phase noise  $\mathcal{L}(f)$  can be calculated according to [21]

$$\mathscr{L}(f) = \frac{k_B T_n}{2|S_{21}^{min}|^2 P_r} \tag{1}$$

where  $k_B$  is the Boltzmann constant,  $T_n$  is the LNA equivalent noise temperature,  $S_{21}^{min}$  is the resonance depth and  $P_r$  the tone power at  $\mu$ MUX input. Assuming an LNA equivalent noise temperature of  $T_n \approx 4$  K, resonance depth of  $S_{21}^{min} \equiv -15$  dB and  $P_r \approx -75$  dBm as mentioned earlier, the minimum Single-Sideband (SSB) phase noise density at the multiplexer output is  $\mathscr{L}(f) \approx -106$  dBc/Hz. Therefore, a maximum degradation of 3 dB with respect to the contribution of the cryogenic LNA is expected due to the phase noise present in the probing tones.

Later, once the spectral purity of the generated tones had been characterized, we evaluated the degradation of the tones due to the receiver path. A loop between the transmitter (Tx) and receiver (Rx) ports of the RF-mixing board was created through  $a \approx 10$ -m-long cable with around 15 dB attenuation, emulating the typical input–output attenuation of the cryogenic RF circuit at resonance [5, 22]. This is consistent with 15 dB of net attenuation resulting from the combination of 35 dB of cold attenuation, a typical LNA gain of 35 dB and -15 dB resonance depth. Considering that



**Fig. 3** SSB phase noise for selected tones at transmitter output (Tx). Green and black dashed lines are the up-conversion local oscillator and cryogenic low-noise amplifier phase noise profiles, respectively. The shaded green region represents the possible region for flux-ramp modulation (FRM) frequencies

the tones were generated with random phases and the fact that in a real measurement both the RF components and the detector signals will lead to a pseudo-randomization of the phases, a PARP  $\approx 12$  dB at the input of the ADCs was considered. Hence, the attenuation of the Rx path was adjusted to satisfy  $P_{tone} \approx -40$  dBFS for optimum ADC SFDR.

On the receiver side, the frequency comb was amplified, down-converted to baseband, and filtered by the RF-mixing board. Then, in the digital domain, the tones were channelized, down-converted, and filtered again. The IQ data streams of each channel were acquired, and the SSB phase noise density  $\mathcal{L}(f)$  was calculated. Figure 4 shows the phase noise of the same six reference tones measured in the previous step, but after being processed by the receiving chain. A detailed analysis shows that the 1/f phase noise component was almost completely removed during down-conversion and sampling process, except at frequencies below 10 Hz. This is consistent with the fact that all oscillators and clocks are locked to the same frequency reference maintaining strong coherence between transmitter and receiver for frequencies below 10 kHz. Beyond our application, which is not sensitive to low-frequency noise, the noise below 10 Hz is being studied because of its importance for the readout of other types of detectors such as MKIDs [22]. In the case of frequencies above 1 MHz the roll-off on the noise profile is due to the combination of the channelizer and a 1,6-MHz Digital Down-Converter (DDC) low-pass filter, while the phase noise plateau above 5 MHz around -136 dBc/Hz is consistent with the theoretical predictions for quantization noise. Within the band of interest, the noise profiles are dominated by the white-phase noise component and several spurious signals. The black dashed line in Fig. 4 corresponds to the average white phase noise of -102 dBc/Hz and represents a degradation of less than 6 dB with respect to the noise level present in the injected tones. The white phase noise component is a combination of the phase noise present in the injected tones and the thermal noise added by the RF-mixing board, while the spurious signals are mostly inter-modulation



Fig. 4 SSB phase noise for selected tones after channelization. The shaded green region represents the possible region for flux-ramp modulation (FRM) frequencies. The black dashed line represents the averaged noise level

products produced by the nonlinearities of the RF components in the receiver chain. Despite the intensity of the spurious signals, their impact can be reduced by narrowing the filtering stages and choosing the flux-ramp frequency carefully.

# 4 Conclusion

A novel SDR readout system has been developed and characterized, utilizing the third generation of RFSoC devices. A characterization methodology has been proposed that allows for evaluating the degradation in the readout performance under real operating conditions. The system was characterized within two scenarios: in transmission mode and in loopback mode. The readout noise was found to be lower than -100 dBc/Hz for a multiplexing factor of about 1000 channels, being in good agreement with state-of-the-art readout systems. The equivalent noise represents an end-to-end degradation of less than 4 dB with respect to the noise level imposed by the cryogenic amplifier. Finally, it was demonstrated that the RFSoC Gen3 devices are suitable enabling technologies for the next generation of readout systems for superconducting detectors. They will lower the costs, reduce the system complexity, and increase the system integration significantly.

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#### Declarations

Conflict of interest The authors declare no competing interests.

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