

Correction to: A subthreshold low-power CMOS LC-VCO with high immunity to PVT variations

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The original publication of the article contains an error in the author Dr. Loulou's biography. The correct version of the biography is given below

Mourad Loulou was born in Sfax, Tunisia in 1968. He received the Engineering Diploma from the University of Sfax in 1993 and his Ph.D. degree in 1998 in electronics from the University of Bordeaux

France. Since October 2009 he became a Full Professor and he currently supervises the Analogue and RF IC Design Group at the National School of Engineers in Sfax. He is also a consulting expert in IC design. He is IEEE senior member. He is the organizer of the IEEE Tunisia Section and section chair from 2008 to 2012. He is also the organizer and he chaired the Tunisia section Circuits And Systems Society chapter from 2009 to 2016. He served as general co-chair, TPC co-chair and committee member of several IEEE conferences. Professor Mourad Loulou, is advisor of several Ph.D. thesis in the field of the design and the design automation of Analogue, mixed mode and RF circuits. He is author and co-author of several journal papers, conference papers, book chapters and books.

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