Preface

Jyotsna Kumar Mandal¹ · Angsuman Sarkar²

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We have great pleasure in presenting this special issue of Microsystem Technologies (MST) on “More than Moore Microsystems”, which includes 18 extended version of peer reviewed selected best papers presented at the International Conference on “2018 IEEE Electron Device Kolkata Conference (2018 IEEE EDKCON)” held in the Kolkata, India during November 24–25, 2018.

The purposes of EDKCON 2018, Organized by IEEE EDS Kolkata Chapter were principally to bring together the people active in the area and to provide new results. It provides an opportunity for exchange of the researchers and insights from scientists and scholars in order to discuss current developments and future trends in device technology and Materials Science for application in circuits and microsystems.

The aim of this special issue is to bring together recent advances in Microsystems with “More than Moore” Nanoscale MOSFET Devices. In this special issue, we focus our attention to research activities in the field of simulation and modeling of nanoscale electronic devices for application in microsystems. It is fervently hoped that by the assembly of the deliberations of eminent academicians, scientists and researchers on this theme topic, this Special Issue will be fruitful, productive and will serve to broaden the perspective for the researchers, scientists, and students in order to unveil new horizons of knowledge in the areas related to the semiconductor devices, Integrated Circuits and microsystems.

A committee consisting of experts from eminent academicians and leading scientists was formed to shortlist and selection of the best interesting articles from the conference. The guest editors asked the authors of the selected papers to submit an extended version for publication in this Special Issue. These papers have been fully refereed as per

the norms and high standards of the Microsystem Technologies. The acceptance of this special issue was strictly based on the reviewers’ recommendations, mandatory revisions, and final examinations conducted and supervised by the guest editors.

We thank and express our sincere gratitude to all the reviewers for their comment, expert advice, opinions and hard work. We would like to appreciate the contributors to this special issue for submitting and patiently revising manuscripts to meet the comments / suggestions of the referees and responding to meet all the requirements of the journal.

Our special thanks are given to Prof. Bernd Michel for his encouragement, help and guidance to the publication of this issue. Finally, we wish to express our sincere thanks and deepest gratitude to Dr. Bharat Bhushan, Dr. Michael Maskos and the members of the Editorial Office of the journal for their assistance in preparation and publication of this issue.

It is hoped that this special issue will make a good reference material and will make a significant contribution in the domain of micro and nano-electronic technologies.

Guest editors.

Prof. (Dr.) Jyotsna Kumar Mandal, University of Kalyani,
India.

Prof. (Dr.) Angsuman Sarkar, Kalyani Government Engineering College, India.

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✉ Jyotsna Kumar Mandal
jkm.cse@gmail.com

¹ University of Kalyani, Kalyani, India

² Kalyani Government Engineering College, Kalyani, India