



# Si beam-assisted graphitization of SiC (0001)

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## Abstract

We have performed a study of thermal graphitization of SiC (0001) surface in the high-purity molecular beam of Si atoms obtained from a controllable source. With the aid of Si beam, we have been able to achieve the semi-equilibrium reaction conditions and to control the growth rate of graphene film freely until complete stop of the graphitization, at the test temperature of 1350 °C. Samples treated in Si beam are characterized by larger and homogeneous areas of graphene having uniform thickness, and much improved crystallographic ordering. This is attributed mainly to the improved buffer layer structure. The buffer layer shows neither disordered regions, nor point defects, which are prevalent in UHV-annealed samples. Significantly smaller concentrations of other surface defects are also observed. The used approach is a promising alternative to more commonly used buffer gas graphitization methods. Apart from the precise control for process parameters and very high process purity, it also allows for a co-deposition of other atoms at the growth stage.

## 1 Introduction

Graphitization of silicon carbide is one of the most widely used methods of the synthesis of graphene. It is particularly promising for electronic applications, because it yields the wafer-scale graphene readily on an insulating substrate, requiring no further transfer. As shown in the graphene roadmap [1], the graphitization technique is of particular interest for high-value applications, because of the large cost of the substrate and expensive processing (high processing temperature).

The process has been studied as early as 1961 [2] and is based on the high-temperature vacuum decomposition of the (0001) surface of SiC and the sublimation of more volatile silicon atoms [3]. Excess carbon atoms left on the surface undergo spontaneous transformation into a graphene film, separated from the substrate by a  $(6\sqrt{3} \times 6\sqrt{3})R30^\circ$  reconstructed buffer layer. The buffer layer contains atoms ordered similarly to the graphene lattice, but still covalently bound to the silicon atoms of the surface [4]. It is also responsible for the n-type doping of the synthesized graphene.

Because the process is not self-limiting in UHV conditions, it has to be conducted at relatively low temperatures of around 1200 °C and during properly limited time, to avoid the formation of multi-layer graphene or graphite. However, in these temperatures, the mobility of carbon atoms on SiC surface is insufficient, resulting in the synthesis of the low-quality, inhomogeneous graphene film, having high concentration of defects, small area of monocrystalline domains and variable thickness of 1–6 ML [5, 6].

The most successful approach to overcome this limitation, so far, is conducting graphitization in the inert gases, e.g., argon, at atmospheric pressure which slows down the sublimation rate of Si atoms from the surface [7–9]. At these conditions, it is possible to use much higher process temperatures of around 1500 °C, which result in higher mobility of carbon atoms and consequently in the higher-quality graphene layers, characterized by more uniform thickness of 1–2 ML, lower concentration of defects and large monocrystalline domains in the micrometer range.

In this paper, we propose an alternative approach to increase the graphitization process temperature, which relies on exposing SiC surface during the process to the molecular beam of silicon atoms from an external, high-purity Si sublimation source. This approach also reduces the effective sublimation rate of silicon from SiC surface, by resupplying Si atoms, but has several advantages over the buffer-gas method.

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Firstly, the use of even the purest available buffer gases at the atmospheric pressure results in the very large exposition of the silicon carbide surface to unknown impurities during graphitization (of the order of 1000 Langmuir/s, in case of the buffer gas of 6N purity in atmospheric pressure). In contrast, the use of high-purity (MBE compatible) sublimation source (utilizing sublimation from the monocrystalline Si filament) results in the beam equivalent pressure (BEP) in the range of  $10^{-7}$  mbar and the BEP of the impurities in the range of  $10^{-11}$  mbar in the worst-case scenario. This seems possibly a very significant improvement, especially in case of samples annealed for a prolonged period of time. Secondly, the areal density of the silicon atom flux can be adjusted freely, with high precision, and within a wide range of values, which creates the possibility to execute semi-equilibrium graphitization at high temperatures (for the context). This approach also allows for the co-deposition of other types of atoms on the graphitized surface, alongside the Si beam, which could lead to new strategies for doping and intercalating graphene on SiC. The flux of Si atoms incoming to the surface can also be spatially varied, by tilting the sample or placing a patterned aperture in front of it, creating a potentially useful tool for different applications.

Unexpectedly, while the preparation of ordered SiC (0001) surfaces through annealing in the Si flux has been common and thoroughly described, we have not found papers reporting such attitude adopted for SiC graphitization. Somewhat similar ideas have been used only in course of few graphitization studies using disilane background gas [10, 11] and in the approach based on the controlled confinement of Si vapor near the surface of SiC during graphitization. In the latter method, Si confinement was achieved in several different ways, such as: conducting the process in a graphite enclosure with an aperture providing controllable leak of Si to the atmosphere [12], capping the SiC surface with another SiC substrate, which has been used to produce high-quality graphene on the C-face of the compound [13], or by capping the SiC with molybdenum plate during annealing, shown to induce heat accumulation at the surface, by reflecting thermal radiation, in addition to providing Si confinement [14]. Other approaches to this process include: flash annealing in UHV [15] or in Pb atmosphere [16].

Here, we have performed a study of the Si beam-assisted SiC (0001) graphitization process, systematically varying its parameters, such as temperature, time and the Si flux density in search for a reliable technology capable of growing electronic-grade graphene films on a semiconducting substrate.

## 2 Materials and methods

Nominally undoped, semi-insulating, epi-ready 4H SiC (0001) wafers obtained from University Wafer were used in the experiment. The samples were rinsed with isopropanol,

blown dry with an inert gas and introduced into an ultrahigh vacuum (UHV) system (base pressure  $< 1 \times 10^{-10}$  mbar). They were degassed at the temperature up to 600 °C and subsequently annealed using an electron beam heater (acting from the backside of the SiC wafer) in the temperature range of 1100–1650 °C for 1–45 min. The temperature was controlled using an infrared pyrometer, calibrated with the observation of melting point of silicon.

Most samples were annealed under silicon flux, with the control samples annealed in UHV conditions. An external sublimation source, SUSI 40 (MBE Komponenten), was used to produce the Si beam. The source is characterized by very high purity: all hot parts exposed to vacuum are made of silicon and the sublimation takes place from the monocrystalline filament (purity  $> 99.9999\%$ ). The source is calibrated with the growth rate of silicon on a quartz crystal microbalance (QCM). This growth rate is then converted to Si flux density, using the known atomic density of Si [17]. The flux densities used in the experiments were within the range of  $0.08\text{--}1.8 \times 10^{14}$  cm $^{-2}$  s $^{-1}$ , or the BEPs (beam equivalent pressures) in the range of  $10^{-8}\text{--}10^{-7}$  mbar (derived from the QCM growth rate 1–20 Å/min).

The process parameters (annealing temperature, time and silicon flux density), creating a three-dimensional parameter space, were varied systematically. The series of samples were characterized with a few complementary methods: low-energy electron diffraction (LEED), X-ray photoelectron spectroscopy (XPS), UV angle-resolved photoelectron spectroscopy (ARPES), and scanning tunneling microscopy (STM).

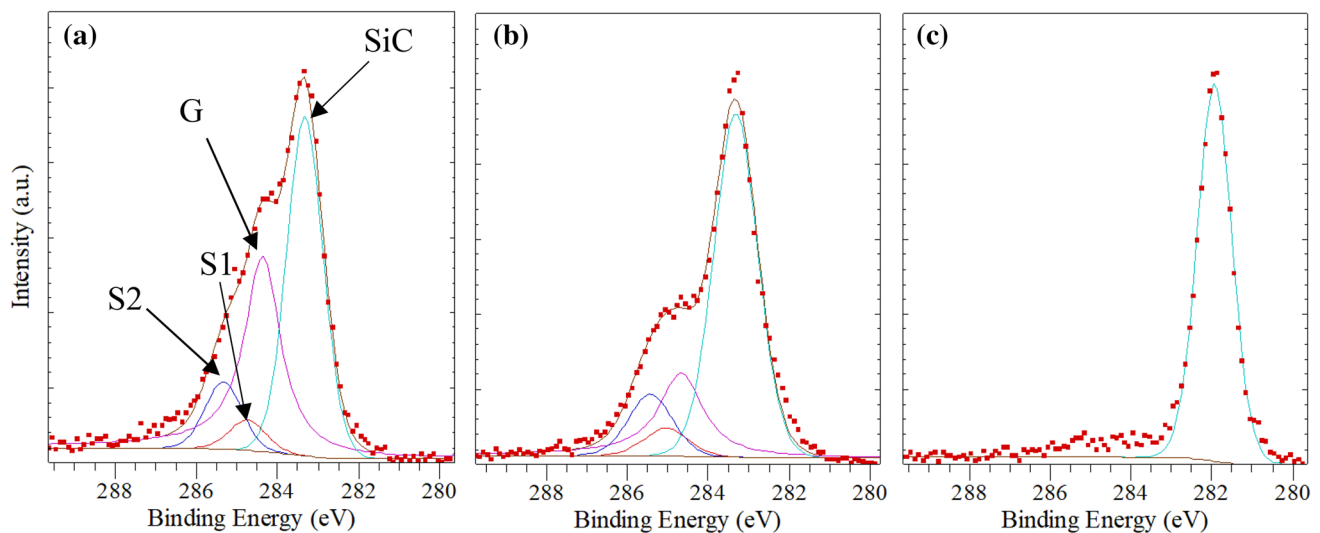
VG-SCIENTA R4000 hemispherical electron energy analyzer with a VUV5k UV lamp or a PREVAC RS 40B1 X-ray source using Mg anode source was used for photoelectron spectroscopy measurements, and OCI MCP LPS300-D diffractometer was used for LEED studies. The STM measurements were performed in a different UHV system (base pressure  $< 2 \times 10^{-10}$  mbar) equipped with an Omicron RT-STM/AFM microscope, using etched tungsten tips as probes. The samples were transferred between UHV systems in the medium vacuum suitcase ( $p < 1 \times 10^{-6}$  mbar).

Analysis of XPS data was performed using CasaXPS 2.3.16 [18], ARPES data using IgorPro 6.1.2.1 software [19] and STM images were processed using Gwyddion version 2.42 [20] and Fiji software [21].

## 3 Results and discussion

### 3.1 Graphene growth kinetics

To confirm the decrease in the growth rate of graphene under the flux of Si atoms and to assess the graphene growth



**Fig. 1** C1s region of an XPS spectrum of the sample of SiC (0001) annealed at  $T=1350\text{ }^{\circ}\text{C}$  for 5 min **a** in UHV, **b** in the Si flux of  $1.0\times 10^{14}\text{ cm}^2\text{ s}^{-1}$ , **c** in the Si flux of  $1.8\times 10^{14}\text{ cm}^2\text{ s}^{-1}$

kinetics in different conditions, we used X-ray photoelectron spectroscopy.

Figure 1 shows the spectra of SiC (0001) samples annealed at the temperature of  $1350\text{ }^{\circ}\text{C}$  for 5 min in UHV (1a) and in the increasing flux of Si atoms (1b, 1c). The C1s peak consists of four main components, described in Fig. 1a, which can be associated with: bulk SiC (located at  $E_b \approx 283.4\text{ eV}$ ), graphene (located at the binding energy:  $E_b \approx 284.2\text{ eV}$ ) and with the interface atoms: S1 ( $E_b \approx 285\text{ eV}$ ) and S2 ( $E_b \approx 285.6\text{ eV}$ ) [17, 22]. The graphene component is fitted using Doniach–Sunjic profile (numerically convoluted with a Gaussian function), because of its metallic behavior, while all the other peaks have a Gaussian–Lorentzian profile. The surface coverage of SiC with graphene is proportional to the intensity of the G component and can be used to quantitatively assess the growth rate of graphene.

As seen in Fig. 1, annealing in UHV resulted in the average coverage of one monolayer of graphene, while utilizing the moderate Si flux of  $\Phi = 1.0\times 10^{14}\text{ cm}^2\text{ s}^{-1}$  considerably slowed down the graphitization—deconvolution of the spectrum showed the presence of interface components (associated with the buffer layer and the top SiC layer) and the G component, with its intensity reduced by around 45%. The presence of the buffer layer and relatively small regions of graphene on the surface was also confirmed by LEED and ARPES studies. Applying larger Si flux of  $1.8\times 10^{14}\text{ cm}^2\text{ s}^{-1}$  completely inhibited the graphitization and the C1s spectrum could be fitted with a single SiC peak. We also observed a significant shift of its position to the lower binding energies (by 1.4 eV), most likely due to band bending effects.

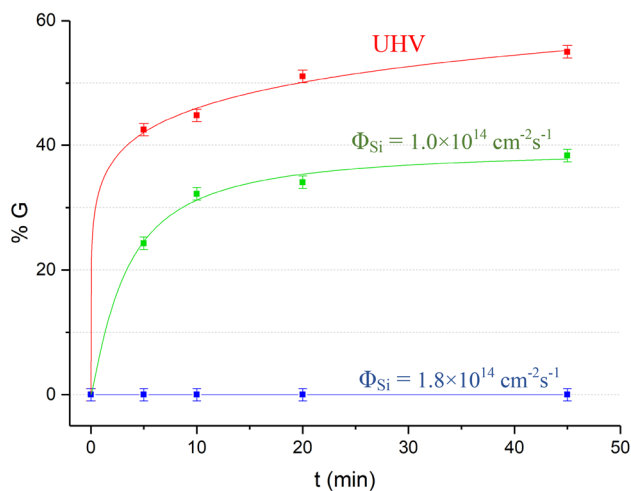
To study the growth kinetics, we have performed studies of the samples annealed at constant temperature, in varying incoming flux of Si atoms. Figure 2 shows the nominal atomic concentration (in this case equal to the percentage of the area of the whole peak) of the G component in the C1s spectrum of graphene, grown at the temperature of  $1350\text{ }^{\circ}\text{C}$ <sup>1</sup> in: UHV (red) Si flux of  $\Phi_1 = 1.0\times 10^{14}\text{ cm}^2\text{ s}^{-1}$  (green) and  $\Phi_2 = 1.8\times 10^{14}\text{ cm}^2\text{ s}^{-1}$  (blue).

The chart of the sample annealed in the highest Si flux confirms that the graphitization is completely inhibited and the graphene does not appear on the surface even after prolonged annealing for  $t = 45\text{ min}$ . The other two charts can be fitted rather accurately with the logistic-type function, according to the equation:

$$y = \frac{A_1 - A_2}{1 + (x/x_0)^p} + A_2. \quad (1)$$

This equation does not have immediate, physical meaning, but qualitatively well describes the observed phenomena. The growth kinetics appear to happen in two phases. In the beginning, the rate of reaction is inversely proportional to the amount of non-graphitized carbon present on the surface (kinetics resembling first-order, self-limiting chemical reaction) and its change over time is approximately proportional to  $\frac{1}{1+\exp(-t)}$ . However, the reaction is not completely self-limiting. When the coverage of the sample with

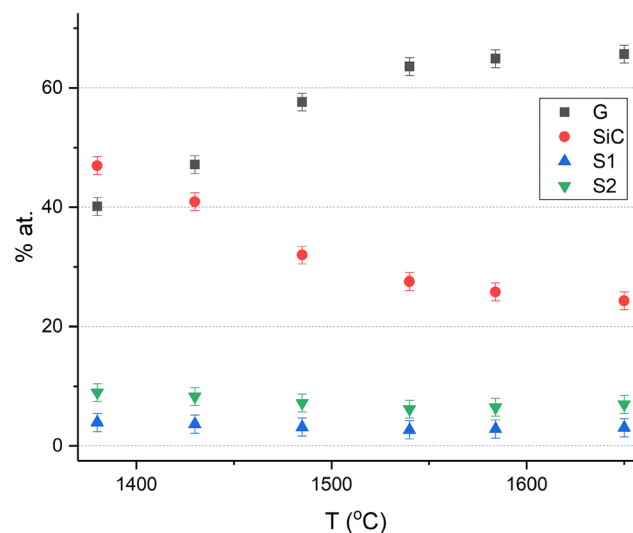
<sup>1</sup>  $1350\text{ }^{\circ}\text{C}$  was found to be the highest temperature, at which graphene growth could be completely inhibited with the maximal Si flux achievable from our source and it is of no meaning beside this.



**Fig. 2** Atomic concentration (%) of the G component in the C1s spectrum of the SiC samples annealed at  $T=1350$  °C in UHV (red), in the Si flux of  $\Phi_1=1.0\times 10^{14}$  cm $^{-2}$  s $^{-1}$  (green) and  $\Phi_2=1.8\times 10^{14}$  cm $^{-2}$  s $^{-1}$  (blue)

graphene increases, the reaction progresses to the second phase, during which the reaction rate becomes approximately independent of the amount of non-graphitized carbon. It is instead limited by the ability of silicon atoms from the subsurface layers to diffuse through the defects present in the continuous graphene film, covering the surface. This leads to the approximately linear growth, with significantly decreased growth rate (resembling zero-order chemical reaction). This type of reaction kinetics, for both UHV and moderate Si-flux conditions is consistent with the current understanding of the growth of epitaxial graphene on SiC (0001). Applying flux of Si atoms during graphitization decreases both the initial reaction rate (in the fast-growth phase) and the linear slope describing graphene growth in the slow-growth phase.

These results show that Si beam-assisted graphitization allows us to freely and precisely adjust the growth rate of graphene. This opens a possibility of semi-equilibrium graphitization (in the case discussed above, at  $T=1350$  °C, it took around 75 min to grow a single monolayer of graphene when it was graphitized in Si flux, compared to 5 min in UHV conditions; the resulting approximate growth rate is 0.015 ML/min) and studying phenomena related to this very slow growth. Semi-equilibrium graphitization can also be extremely useful to study the atomistic mechanism of graphene growth, which is still not fully understood, as well as the kinetics of this inherently non-equilibrium process. We emphasize here, that, in such conditions, using the flux of Si atoms has two significant advantages over other methods: (1) exposition of the surface to the impurities is minimal, (which is particularly important during long exposition times, as impurities may affect the observed reactions at the atomic



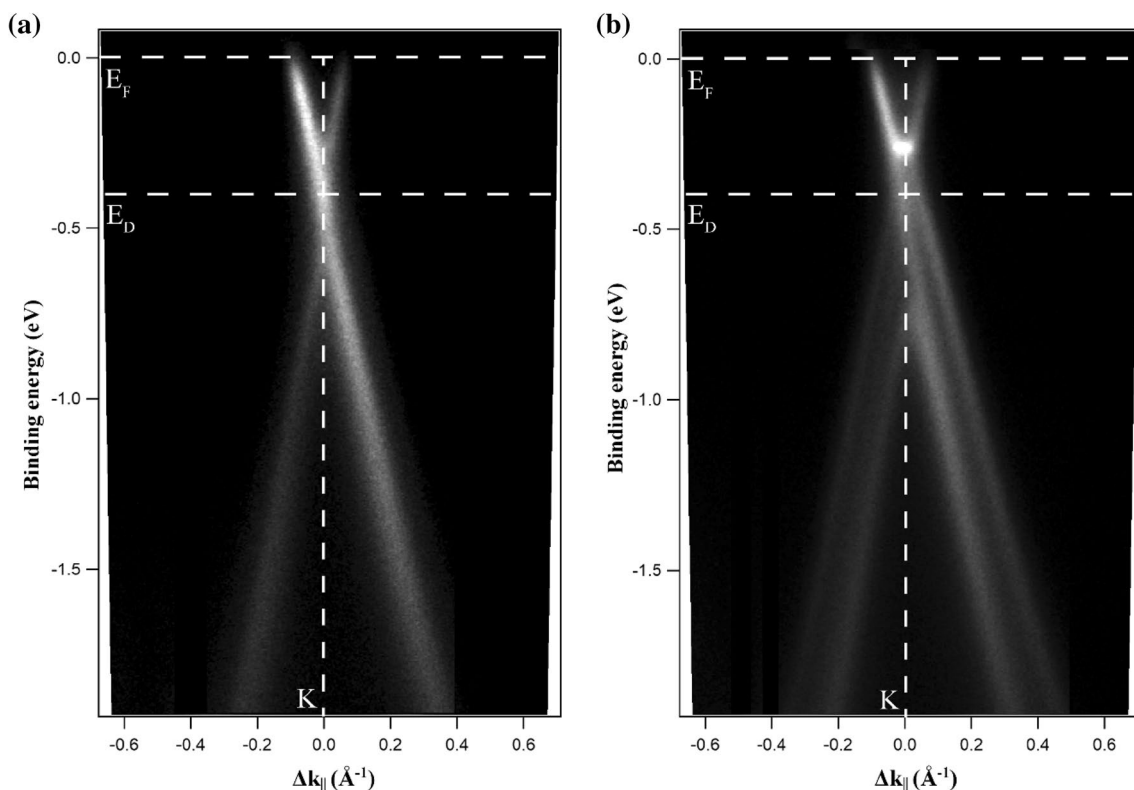
**Fig. 3** Atomic concentration (%) of the different components of C1s peak of SiC samples annealed for a constant time (5 min) and with constant Si flux of  $1.8\times 10^{14}$  cm $^{-2}$  s $^{-1}$ , at varied temperature

scale) and (2) it allows for a very precise control over the effective reaction rate.

Graphene growth can be also performed in the “far from the equilibrium” conditions, with the assistance of the flux of Si atoms. This might be motivated by practical reasons (shorter annealing time needed to grow graphene samples) or by the intention to avoid some of the obstacles associated with prolonged annealing, such as step bunching. To investigate these non-equilibrium processes, we have prepared a series of samples annealed in systematically varied conditions (temperature, time, incoming Si flux). The results, showing the nominal atomic concentration of the different components of C1s spectrum for such series of samples, prepared in constant process time (5 min) and constant Si flux of  $\Phi=1.8\times 10^{14}$  cm $^{-2}$  s $^{-1}$ , but at varied temperature (in the range of 1385–1650 °C), are shown in Fig. 3.

Graphene coverage, proportional to the XPS nominal atomic concentration of the G component, increases with increasing temperature and stabilizes in the given conditions (time, Si flux) at temperatures exceeding 1600 °C, at the nominal concentration of around 65%. The nominal concentration of S1 and S2 components exhibits a slight decreasing trend with increasing annealing temperature, which is expected, given that the signal from the interface layer is attenuated by the increasing thickness of graphene. However, the magnitude of this effect is too small for accurate observation.

Coverage of the surface corresponding to a complete mono- and bilayer graphene, at the G values of 42.5% and 62.5% respectively, was found using angle-resolved photoelectron spectroscopy (See Fig. 4a. for the relevant ARPES



**Fig. 4** ARPES spectra around the K point in the reciprocal lattice of graphene for samples annealed for 5 min, in the Si flux equal to  $1.8 \times 10^{14} \text{ cm}^{-2} \text{ s}^{-1}$  **a** at 1430 °C, showing a single dispersion relation characteristic for the monolayer graphene, and **b** at 1580 °C, showing

a two-band dispersion relation characteristic for the bilayer graphene; Fermi energy ( $E_F$ ), Dirac energy ( $E_D$ ) and K point in the reciprocal lattice are marked in the figure

spectra). ARPES measurements have also shown that the graphene is n-doped, with Dirac point located around 0.4 eV below the Fermi level, which is consistent with the graphene prepared on silicon carbide with other methods [23], and shows that there is no intercalation of graphene with silicon atoms (the process of the intercalation of the graphene grown on SiC (0001) with Si is reported in Ref. [24]).

### 3.2 Surface structure—STM studies

Figure 5a, b shows the STM images of two different areas on the SiC sample annealed at  $T = 1400$  °C for 5 min in UHV conditions, while Fig. 5c, d show images of SiC annealed in the same conditions, but with an Si flux of  $\Phi = 0.4 \times 10^{14} \text{ cm}^{-2} \text{ s}^{-1}$ . The areas of monolayer graphene are marked as ML, bilayer graphene as BL, and amorphous areas as A. The assignment is based on the observed step heights and characteristic STM patterns.

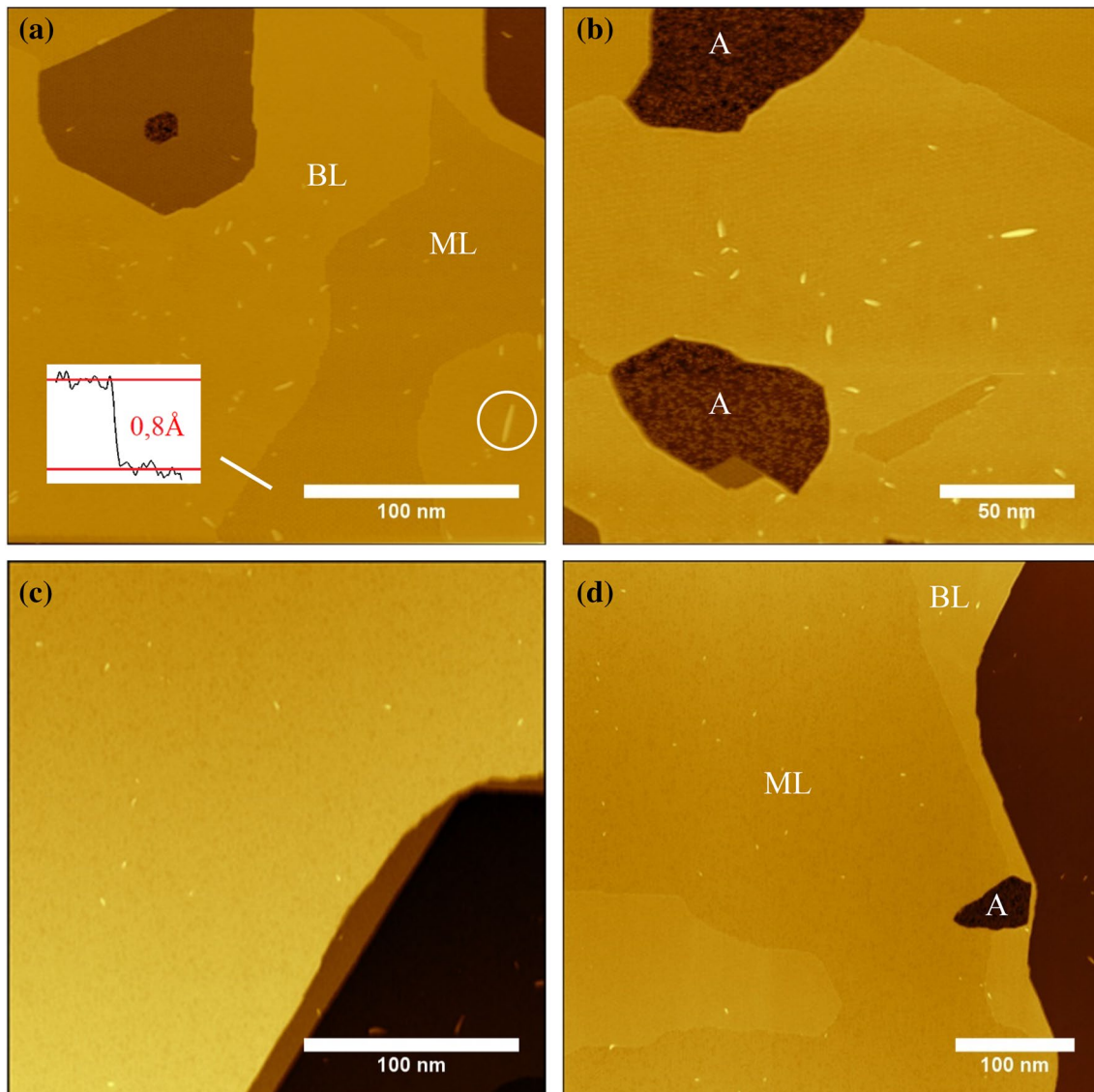
Earlier STM studies reported that the apparent STM step height between the monolayer and bilayer graphene was 0.6–0.9 Å [25, 26]. This is much lesser than the interlayer spacing observed for the graphite (3.35 Å) or SiC bilayer height (2.5 Å) and, therefore, reliably identifies the

coverages under discussion. This small apparent step height has been attributed to electronic effects interfering with the topography during STM imaging [24] or to the bottom-up growth of epitaxial graphene [25]. According to these lines, the monolayer and bilayer graphene areas are identified as those which are separated with the STM step having a height of around 0.8 Å (see an example of a line profile across the bi/monolayer boundary, shown in the inset in Fig. 5a).

The amorphous areas, observed for the sample annealed in UHV, have the form of pits, with apparent (STM) depth of around 7.8 Å, equal to around three bilayers of SiC (7.5 Å). We also observed few amorphous pits for the surfaces annealed in Si flux, but they were usually shallower, with the depth of around 5–6 Å (around 2 bilayers of SiC). Surface pits have been observed for the graphene/SiC(0001) system, but they were usually somewhat deeper [23]. The double step observed in Fig. 5c) has an apparent height of around 10 Å (each step has a height of around 5 Å), while the step observed in Fig. 5d) has a height of around 5 Å. These can be attributed to steps with a height of two SiC bilayers (2.5 Å) [27].

Overall, the morphology of the samples appears to be significantly improved by applying the flux of Si atoms during





**Fig. 5** STM images of SiC samples annealed in  $T=1400$  °C for 5 min: **a, b** without Si flux, **c, d** in Si flux of  $0.4 \times 10^{14} \text{ cm}^{-2} \text{ s}^{-1}$ . Imaging parameters are: **a**  $I=12$  pA,  $U=400$  mV; **b**  $I=15$  pA,

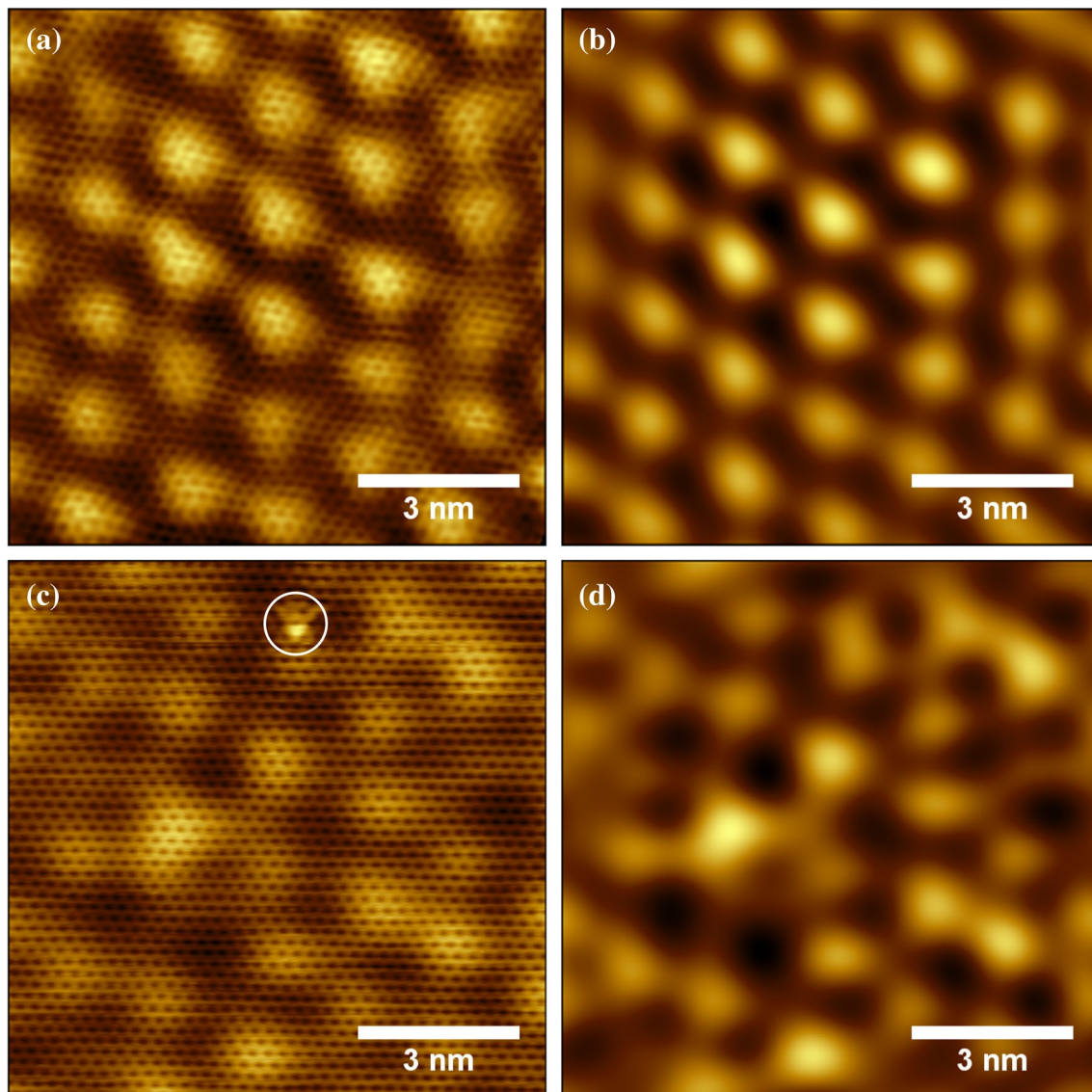
$U=400$  mV; **c, d**  $I=20$  pA,  $U=400$  mV; inlet in Fig. 5a shows the height profile along the white line marked in the figure

graphitization. The samples annealed in a moderately low Si flux of  $\Phi=0.4 \times 10^{14} \text{ cm}^{-2} \text{ s}^{-1}$  (far from the semi-equilibrium conditions at this temperature) exhibit a larger degree of surface order and larger areas of continuous, monolayer graphene. We observed some amorphous pits on the surface, but their number was reduced compared to UHV conditions and they were usually shallower. Interestingly, the amorphous pits appear to be positioned near the step edges for the Si beam-assisted growth, while their distribution for the UHV-grown samples is rather uniform across the surface.

We also observed a large reduction of the amount and size of point defects present on the surface (example marked by circle in Fig. 5a). These defects are commonly observed for

the graphene/SiC (0001) system and they were attributed either to carbon nanotube-type defects [28] or to the other off-plane defects in epitaxial graphene. Regardless of their origin, it is evidenced that Si flux-assisted growth is effective in reducing their density.

To gain more insight into the properties of graphene grown in different conditions, we performed atomically resolved STM studies: Fig. 6a, b shows the SiC sample annealed in the flux of Si atoms, while Fig. 6c, d shows the sample annealed in vacuum. An atomically resolved graphene structure is visible as a fine grid with a lattice constant of around 0.25 nm, overlaid on the grid with a much larger lattice constant of around 3.1 nm, consistent



**Fig. 6** Atomically resolved STM images of graphitized SiC(0001) samples: **a, b** annealed in Si flux of  $0.8 \times 10^{14} \text{ cm}^{-2} \text{ s}^{-1}$ ; at  $T = 1400 \text{ }^\circ\text{C}$  and  $t = 5 \text{ min}$ , **c, d** annealed in UHV at  $T = 1400 \text{ }^\circ\text{C}$  and

$t = 5 \text{ min}$ . Maps **b, d** are obtained from the maps **a, c** correspondingly, using 2D FFT filtering. Imaging parameters: **a, b**  $I = 25 \text{ pA}$ ,  $U = 20 \text{ mV}$ ; **c, d**  $I = 102 \text{ pA}$ ,  $U = 20 \text{ mV}$

with earlier studies [17]. The details of the STM imaging of graphene on SiC (0001) have been described elsewhere [29] and the large-scale pattern has been assigned to the “quasi  $6 \times 6$  corrugation”, composed of the hexagons of two slightly different sizes, which combined to create the  $(6\sqrt{3} \times 6\sqrt{3})R30^\circ$  surface lattice [30–32].

In both STM images of Fig. 6a, c, the graphene layer itself appears to be defectless regardless of the graphitization conditions. The single graphene grain is larger than the scan size and no vacancies are observed. However, for the UHV-grown sample, we observed the presence of a point defect, located most likely in the buffer layer, marked by the white circle in Fig. 6c. The presence of the defect does

not strongly perturb the image of the local graphene lattice, making it most likely a type A defect, identified in Ref [33]. We have observed no such defects for the Si flux-assisted growth; moreover, a very pronounced difference is visible concerning the regularity of the  $(6\sqrt{3} \times 6\sqrt{3})R30^\circ$  sub-surface lattice. We have performed 2D fast Fourier transform filtering to expose these patterns clearly—see Fig. 6b (for the graphitization in Si flux) and Fig. 6d (for graphitization without Si flux). The quasi  $(6 \times 6)$  pattern reminiscent of the  $(6\sqrt{3} \times 6\sqrt{3})R30^\circ$  surface lattice is different, depending on the preparation method. The sample annealed in UHV is characterized by a large degree of disorder—in fact, the quasi  $(6 \times 6)$  pattern is barely visible. In contrast,

graphitization in the flux of Si atoms results in a very regular ( $6 \times 6$ ) pattern. Moreover, while the regions with the disordered quasi ( $6 \times 6$ ) pattern are prevalent in UHV-grown samples, we found no such regions for samples annealed in Si flux. A similar type of disorder was observed in other studies, in which the method of flash annealing in UHV conditions was used to produce large-scale bilayer graphene on 6H-SiC (0001) (for example, see Fig. 1b. in Ref. [34]). To a lesser extent, the disorder in the buffer layer is also visible for the samples prepared by atmospheric-pressure graphitization (for example, see Fig. 2a, b in Ref. [30]).

It appears that the most striking effect due to resupplying Si atoms to SiC surface during graphitization in non-equilibrium conditions is an elimination of disorder in the interfacial buffer layer. This could be very important for the electron transport in graphene on SiC, since any irregularity in the periodic, electric potential perpendicular to the surface will contribute strongly to the scattering processes and decrease electron mobility in this material.

## 4 Conclusions

We have performed a systematic study of the graphitization of (0001) surface of silicon carbide in the flux of Si atoms from an external, high-purity sublimation source. Investigations of the process rate using X-ray photoelectron spectroscopy resulted in the identification of the semi-equilibrium graphitization conditions in Si flux, for which the graphene growth is characterized by a very slow rate, even as low as 0.015 ML/min. The samples annealed in Si flux are characterized by a high degree of crystallographic ordering, as evidenced by STM measurements. Atomically resolved STM measurements have revealed, in particular, lower concentration of carbon nanotube or off-plane types of surface defects and large improvement in crystallographic order in the sub-surface buffer layer (or the interface reconstruction) as compared to samples annealed with no Si flux.

The graphene growth method described in this article appears to be an attractive alternative to the more popular method of graphitization of SiC in the presence of inert buffer gases. We assign the observed improved ordering in the graphene/SiC(0001) surface system not only to the higher diffusivity of carbon atoms on the SiC surface and longer process time, but also to the very clean UHV process environment. We emphasize here that extending the graphitization time must cause increased exposition of the reacting system to interfering impurities, but this does not constitute a problem for the method presented here.

Further studies, including electrical characteristics of the obtained graphene films, are needed to describe the influence of the increased crystallographic ordering on the electrical parameters of the material. The proposed method

allows to vary and freely adjust all process parameters (temperature, time and Si flux) and the absence of buffer gases makes it possible to introduce beams of other atoms acting on the surface simultaneously with silicon. This also opens up new possibilities of doping and intercalation of graphene layers already during their growth.

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