

# Graph Grammar Modeling and Verification of Ad Hoc Routing Protocols

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**Abstract.** We present a technique for modeling and automatic verification of network protocols, based on graph transformation. It is suitable for protocols with a potentially unbounded number of nodes, in which the structure and topology of the network is a central aspect, such as routing protocols for ad hoc networks. Safety properties are specified as a set of undesirable global configurations. We verify that there is no undesirable configuration which is reachable from an initial configuration, by means of symbolic backward reachability analysis.

In general, the reachability problem is undecidable. We implement the technique in a graph grammar analysis tool, and automatically verify several interesting nontrivial examples. Notably, we prove loop freedom for the DYMO ad hoc routing protocol. DYMO is currently on the IETF standards track, to potentially become an Internet standard.

## 1 Introduction

The verification of network protocols has been one of the most important driving forces for the development of model checking technology. Most approaches (e.g., [15]) analyze finite-state models of protocols, but an increasing number of techniques are developed for analyzing parameterized or infinite-state models (e.g., [2]). In this paper, we consider verification of protocols for networks with a potentially unbounded number of nodes, possibly with a dynamically changing topology. This is a large class of protocols, including protocols for wireless ad hoc networks, many distributed algorithms, security protocols, telephone system services, etc. Global configurations of such protocols are naturally modeled using graphs, that are transformed by the protocol's dynamic behavior, and therefore various forms of graph transformation systems have been used to model and analyze them [19,7].

In this paper, we present a technique for modeling and verification of protocols using a variant of *graph transformation systems* (GTSs) [19,7]. We use a general mechanism for expressing conditions on the applicability of a rule, in the form of *negative application conditions* (NACs). Sets of global configurations are symbolically represented by *graph patterns* [7], which are graphs extended with NACs. Intuitively, a graph pattern represents the set of configurations that contain it as a subgraph, but none of the NACs. A safety property of a protocol is represented by a set of graph patterns that represent undesirable global configurations.

We consider the problem of verifying safety properties. This can be reduced to the problem whether an undesirable configuration can be reached, by a sequence of graph transformation steps, from some initial global configuration. We present a method for automatically checking such a reachability problem by backward reachability analysis. Backward reachability analysis is a powerful verification technique, which has generated decidability results for many classes of parameterized and infinite-state systems (e.g., [3,2,13]) and proven to be highly useful also for undecidable verification problems (e.g., [1]). By fixed point computation, we compute an over-approximation of the set of configurations from which a bad configuration can be reached, and check that this set contains no initial configuration. The central part of the backward reachability procedure is to compute the predecessors of a set of configurations in this symbolic representation. Since the reachability problem is undecidable in general, the fixed point computation is not guaranteed to terminate. However, we show that the techniques are powerful enough for verifying several interesting nontrivial examples, indicating that the approach is useful for network protocols where the dynamically changing topology of the network is a central aspect.

A main motivation for our work is to analyze protocols for wireless ad hoc networks, including the important class of *routing protocols*. We have implemented our technique, and successfully verified that the DYMO protocol [10] will never generate routing loops. Verifying loop freedom for ad hoc routing protocols has been the subject of much work [8,12]; several previous protocol proposals have been incorrect in this respect [9,4]. Our verification method handles a detailed ad hoc routing protocol model, with relatively little effort. In our work, we have also found GTSSs to be an intuitive and visually clear form of modeling.

For space limitations, proofs are not included in this paper; instead see the extended version [22].

*Related work.* There have been several efforts to verify loop freedom of routing protocols for ad hoc networks. Bhargavan et al. [8] verified AODV [21] to be loop free, using a combination of SPIN for model checking a finite network model, and HOL theorem proving for generalizing the proof. In contrast, we prove the same property automatically for an arbitrary number of nodes. Our experience is that modeling using GTSSs is more intuitive than to separately construct SPIN models and HOL proofs. Das and Dill [12] developed automatic predicate discovery for use in predicate abstraction, and proved loop freedom for a simplified version of AODV, excluding timeouts. The construction of an abstract system and discovery of relevant abstraction predicates require many calls to a theorem prover; our method does not need to interact with a theorem prover. We check the graphs directly for inconsistencies.

There have been several other approaches to modeling and analysis using variants of GTSSs. König and Kozioura [19] over-approximate graph transformation systems using Petri nets, successively constructed using forward counterexample guided abstraction refinement. Their technique does not support the use of NACs. We have found NACs to be an advantage during modeling and verification. For example, our first approach at verifying the DYMO protocol was

without NACs, resulting in a more complex model with features not directly related to the central protocol function.

Kastenberg and Rensink [18] translate GTSs to finite-state models in the GROOVE tool by putting an upper bound on the number of nodes in a network. Becker et al. [7] verified safety properties of mechatronic systems, modeled by GTSs that are similar to ours. However, they only check that the set of non-bad configurations is an inductive invariant. That worked for their application, but for verifying safety properties in general it requires the user to supply an inductive invariant. Bauer and Wilhelm [6,5] use *partner abstraction* to verify dynamic communication systems; two nodes are not distinguished if they have the same labels and the sets of labels of their adjacent nodes are equal, respecting edge directions. That abstraction is not suited for ad hoc protocols, because nodes do not have dedicated roles.

Backward reachability analysis has also been used to verify safety properties in many parameterized and infinite-state system models, with less general connection patterns than those possible in GTSs. Examples include totally homogeneous topologies in which nodes can not identify different partners, resulting in Petri nets with variants (e.g., [13]), systems with linear structure and some extensions (e.g., [1]), and systems with binary connections between nodes, tailored for modeling telephone services [17].

*Organization of paper.* We give a brief outline of the DYMO protocol in Section 2. The graph transformation system formalism and the backward reachability procedure are presented in Sections 3 and 4. In Section 5 we describe how we modeled DYMO, and present our verification results in Section 6. Finally, Section 7 concludes the paper.

## 2 DYMO

We are interested in modeling and verification of ad hoc routing protocols. These protocols are used in networks that vary dynamically in size and topology. Every network node that participates in an ad hoc routing protocol acts as a router, using forwarding information stored in a routing table. The purpose of the ad hoc routing protocol is to dynamically update the routing tables so that they reflect the current network topology. DYMO [10] is one of two ad hoc routing protocols currently considered for standardization by the IETF MANET group [23]. The latest DYMO version at the time of writing is specified in version 10 of the DYMO Internet draft [11]. This is the version we have used as basis for our modeling. The following is a simplified description of the main properties of DYMO. The reader is referred to the Internet draft for the details.

In our protocol model, each DYMO network node  $A$  has an address, a routing table and a sequence number. The routing table of  $A$  contains the following fields for each destination node  $D$ .

- `RouteNextHopAddressA(D)` is the node to which  $A$  currently forwards packets, destined for node  $D$ .

- $\text{RouteSeqNo}_A(D)$  is the sequence number that node  $A$  has recorded for the route to destination  $D$ . It is a measure of the freshness of a route; a higher number means more recent routing information. Note that this sequence number concerns the route to  $D$  from  $A$ , and is not related to the sequence number of  $A$ .
- $\text{RouteHopCnt}_A(D)$  is the recorded distance between nodes  $A$  and  $D$ , in terms of number of hops.
- $\text{Broken}_A(D)$  is an indicator of whether or not the route from  $A$  to  $D$  can be used. The protocol has a mechanism to detect when a link on a route is broken [11]. Information regarding broken links is propagated through route error messages (**RERR**).

When a network node  $A$  wants to send a packet to another network node  $D$ , it first checks its routing table to see if it has an entry with  $\text{Broken}_A(D) = \text{false}$ . If that is the case, it forwards the packet to node  $\text{RouteNextHopAddress}_A(D)$ . Otherwise, node  $A$  needs to find a route to  $D$ , which it does by issuing a route request (**RREQ**) message. The route request is flooded through the network. It contains the addresses of nodes  $A$  and  $D$ , the sequence number of  $A$ , and a hop counter. The hop counter contains the value 1 when the **RREQ** is issued; each re-transmitting node then increases it by one. Node  $A$  increases its own sequence number after each issued route request.

When the destination of a route request,  $D$ , receives it, it generates a route reply message (**RREP**). The route reply contains the same fields as the request. Route replies are not flooded, but instead routed through the network using available routing table entries. **RREPs** and **RREQs** are collectively referred to as routing messages (**RMs**).

Whenever a network node  $A$  receives an **RM**, the routing table of  $A$  is compared to the **RM**. If  $A$  does not have an entry pertaining to the originator of the **RM**, then the information in the **RM** is inserted into the routing table of  $A$ . Otherwise, the information in the **RM** replaces that of the routing table if the information is more recent, or equally recent but better, in terms of distance to the originator. The routing table update rules are detailed in Section 5.

### 3 Modeling Using Graph Transformation Systems

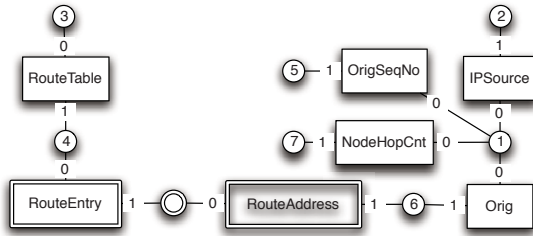
We model systems as transition systems of a particular form, in which configurations are hypergraphs, and transitions between configurations are specified by graph rewriting rules. Constraints on configurations are represented by so-called *patterns*, which are hypergraphs extended with a mechanism to describe the absence of certain hyperedges: *negative application conditions (NACs)*. Our definitions are similar to the ones used by, e.g., Becker et al. [7], but with a more general facility for expressing NACs.

Assume a finite set  $\Lambda$  of labels. A *hypergraph* is a pair  $\langle N, E \rangle$ , where  $N$  is a finite set of *nodes*, and  $E \subseteq \Lambda \times N^*$  is a finite set of *hyperedges*. A hyperedge is a pair  $(\lambda, \vec{n})$ , where  $\lambda \in \Lambda$  is its *label* and  $\vec{n} \in N^*$ . The length of  $\vec{n}$  is called

the *arity* of the hyperedge. A hyperedge is essentially a relation on nodes, and can be visualized as a box labeled  $\lambda$ , with connections to each node  $n \in \vec{n}$ .

A *pattern* is a tuple  $\varphi = \langle N_\varphi, E_\varphi, \mathcal{G}_\varphi^- \rangle$ , where  $\langle N_\varphi, E_\varphi \rangle$  is a hypergraph, and  $\mathcal{G}_\varphi^-$  is a set of NACs. Each NAC is a hypergraph  $G^- = \langle N^-, E^- \rangle$ , where  $N^-$  is a finite set of *negative nodes* disjoint from  $N_\varphi$ , and  $E^- \subseteq \Lambda \times (N_\varphi \cup N^-)^*$  is a finite set of *negative hyperedges*. We refer to  $N_\varphi$  and  $E_\varphi$  as *positive nodes* and edges of  $\varphi$ . We define  $\text{NODES}(E) = \{n \in \vec{n} \mid (\lambda, \vec{n}) \in E\}$ .

*Example.* Figure 1 shows a pattern — the left-hand side of one of the DYMO model routing table update rules. The pattern models a network node receiving routing information for a node to which it currently has no route. In the pattern, positive nodes are drawn as circles and negative nodes as double circles. Nodes have numeric names for identification. Positive and negative edges are drawn as boxes and double boxes. Edge connections are numbered, to indicate their order. The pattern contains a single NAC, consisting of the negative edges labeled **RouteEntry** and **RouteAddress** along with their connected nodes. Without the possibility to express non-existence, we would need to model traversal through the entries to conclude the absence of an entry. In more detail, the pattern consists of a network node *A* (node 3) and a routing message (node 1). *A* has a routing table (node 4) that contains no routing table entry pointing to network node *D* (node 6). The message has originator *D*, a hop count (node 7), a sequence number (node 5) and an IP source (node 2).



**Fig. 1.** A pattern containing a NAC

A hypergraph  $g = \langle N_g, E_g \rangle$  is *subsumed by* a pattern  $\varphi = \langle N_\varphi, E_\varphi, \mathcal{G}_\varphi^- \rangle$ , written  $g \preceq \varphi$ , if there exists an injection  $h : N_\varphi \rightarrow N_g$  satisfying:

1. for each  $(\lambda, \vec{n}) \in E_\varphi$  we have  $(\lambda, h(\vec{n})) \in E_g$  and
2. there exists no  $\langle N^-, E^- \rangle \in \mathcal{G}_\varphi^-$  and no injection  $k : N^- \rightarrow N_g$  such that  $(\lambda, (h \cup k)(\vec{n})) \in E_g$  for each  $(\lambda, \vec{n}) \in E^-$ , where  $(h \cup k)$  is defined as  $h$  on  $N_\varphi$  and as  $k$  on  $N^-$ .

Intuitively, a pattern  $\varphi = \langle N_\varphi, E_\varphi, \mathcal{G}_\varphi^- \rangle$  is a constraint, saying that a hypergraph must contain  $\langle N_\varphi, E_\varphi \rangle$  as a subgraph, which does not have a “match” for any NAC in  $\mathcal{G}_\varphi^-$ .

Above we let  $f((n_1, \dots, n_k)) = (f(n_1), \dots, f(n_k))$  for a function on nodes applied to a vector of nodes. If an injection  $h$  satisfying the above conditions exists, we say that  $g \preceq \varphi$  is *witnessed by  $h$* , written  $g \preceq_h \varphi$ .

For a pattern  $\varphi$  we use  $\llbracket \varphi \rrbracket$  to denote the set of hypergraphs  $g$  such that  $g \preceq \varphi$ . For a set of patterns  $\Phi$ , we let  $\llbracket \Phi \rrbracket = \cup \{ \llbracket \varphi \rrbracket \mid \varphi \in \Phi \}$ . We call  $\varphi$  *consistent* if there is no  $\langle N^-, E^- \rangle \in \mathcal{G}_\varphi^-$  and no injection  $k : N^- \rightarrow N_\varphi$  such that  $(\lambda, k'(\vec{n})) \in E_\varphi$  for each  $(\lambda, \vec{n}) \in E^-$ , where  $k'$  extends  $k$  by the identity on  $N_\varphi$ . Informally,  $\varphi$  is consistent if none of its NACs contradicts its positive nodes and edges. An inconsistent pattern  $\psi$  represents an empty set, as  $g \preceq \psi$  is not satisfied by any  $g$ .

A pattern  $\varphi$  is subsumed by the pattern  $\psi$ , denoted  $\varphi \preceq \psi$ , if  $\llbracket \varphi \rrbracket \subseteq \llbracket \psi \rrbracket$ . The relation  $\preceq$  on patterns can be checked according to the following Proposition.

**Proposition 1.** *Given patterns  $\varphi = \langle N_\varphi, E_\varphi, \mathcal{G}_\varphi^- \rangle$  and  $\psi = \langle N_\psi, E_\psi, \mathcal{G}_\psi^- \rangle$  which are consistent, we have that  $\varphi \preceq \psi$  iff there exists an injection  $h : N_\psi \rightarrow N_\varphi$ , such that  $\langle N_\varphi, E_\varphi \rangle \preceq_h \langle N_\psi, E_\psi, \emptyset \rangle$  and for each NAC  $\langle M^-, F^- \rangle \in \mathcal{G}_\psi^-$  there is a NAC  $\langle N^-, E^- \rangle \in \mathcal{G}_\varphi^-$  and an injection  $k : N^- \rightarrow M^-$  such that*

- $(\text{NODES}(E^-) \setminus N^-) \subseteq h(N_\psi)$ , and
- for each  $(\lambda, \vec{n}) \in E^-$ , we have  $(\lambda, (h^{-1} \cup k)(\vec{n})) \in F^-$ . □

Intuitively,  $\varphi \preceq \psi$  if and only if the positive part of  $\psi$  is a subgraph of the positive part of  $\varphi$ , and for each NAC in  $\mathcal{G}_\psi^-$ , there is a corresponding NAC in  $\mathcal{G}_\varphi^-$  which is a subgraph of the former NAC.

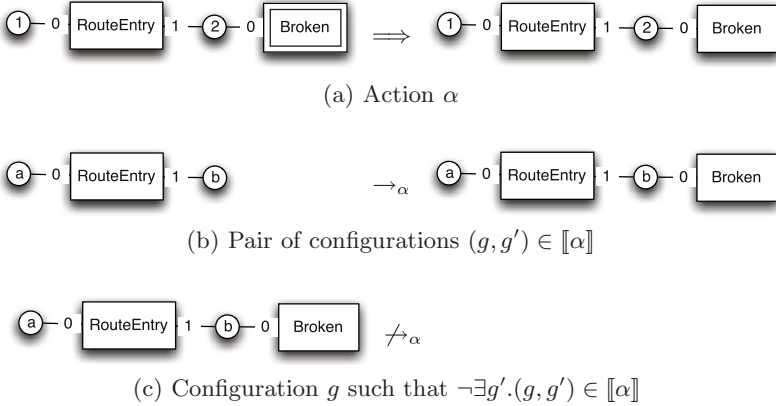
In our system model, configurations are represented by hypergraphs. Transitions are specified by *actions*, which are (hypergraph) rewrite rules.

**Definition 1.** *An action is a pair  $\langle L, R \rangle$ , where  $L = \langle N_L, E_L, \mathcal{G}_L^- \rangle$  is a pattern and  $R = \langle N_R, E_R \rangle$  is a hypergraph with  $N_L \subseteq N_R$  (i.e., actions can create nodes, but not delete them). The action  $\alpha = \langle L, R \rangle$  denotes the set  $\llbracket \alpha \rrbracket$  of pairs of configurations  $(g, g')$ , with  $g = \langle N_g, E_g \rangle$ ,  $g' = \langle N_{g'}, E_{g'} \rangle$  and  $N_g \subseteq N_{g'}$  such that there is an injection  $h : N_R \rightarrow N_{g'}$  satisfying:*

- $g \preceq L$  is witnessed by the restriction of  $h$  to  $N_L$
- $N_{g'} = N_g \cup h(N_R)$
- $E_{g'} = (E_g \setminus h(E_L)) \cup h(E_R)$ . □

*Example.* Figure 2(a) shows an action  $\alpha = \langle L, R \rangle$ . The pattern  $L$  is to the left of the arrow ( $\implies$ ) and  $R$  to the right. The action does not create any nodes, i.e.,  $N_L = N_R$ . Figure 2(b) shows a pair  $(g, g') \in \llbracket \alpha \rrbracket$ , i.e.,  $g$  can be rewritten via  $\alpha$  to  $g'$ . The subsumption  $g \preceq L$  is witnessed by the injection  $h = \{1 \mapsto a, 2 \mapsto b\}$ . The injection  $h$  satisfies  $N_{g'} = N_g \cup h(N_R) = \{a, b\}$  and  $E_{g'} = (E_g \setminus h(E_L)) \cup h(E_R) = h(E_R)$ . Figure 2(c) shows a configuration  $g$  such that there is no  $g'$  with  $(g, g') \in \llbracket \alpha \rrbracket$ , since  $g \not\preceq L$ . In other words,  $g$  cannot be rewritten via  $\alpha$ .

**Definition 2.** *A system model is a pair  $\langle \gamma_0, \mathcal{A} \rangle$  consisting of an initial configuration  $\gamma_0$  together with a finite set of actions  $\mathcal{A}$ . □*



**Fig. 2.** Example of an action and its semantics

For a set  $\Gamma$  of configurations and an action  $\alpha$ , let  $pre(\alpha, \Gamma) = \{g \mid \exists g' \in \Gamma. (g, g') \in \llbracket \alpha \rrbracket\}$ , i.e., the configurations which in one step can be rewritten to  $\Gamma$  using  $\alpha$ . Similarly, for a set of actions  $\mathcal{A}$ , let  $pre^*(\mathcal{A}, \Gamma)$  denote the set of configurations which can reach a configuration in  $\Gamma$  by a sequence of rewritings using actions in  $\mathcal{A}$ .

## 4 Symbolic Verification

We formulate a verification scenario as the problem whether a set of configurations, represented by a set of patterns, is reachable. More precisely, given a system model  $\langle \gamma_0, \mathcal{A} \rangle$ , and a set of patterns  $\Phi$ , the *reachability problem* asks whether there is a sequence of transitions from  $\gamma_0$  to some configuration in  $\llbracket \Phi \rrbracket$ .

In our approach, we analyze a reachability problem using *backward reachability analysis*, in which we compute an over-approximation of the set  $pre^*(\mathcal{A}, \llbracket \Phi \rrbracket)$  of configurations, and check whether it includes  $\gamma_0$ . We clarify why and when the computation is not exact in the *Approximation* paragraph below. In general, the reachability problem is undecidable, and our analysis is not guaranteed to terminate. However, the technique is sufficiently powerful to verify several nontrivial network protocols (see Section 6).

We attempt to compute  $pre^*(\mathcal{A}, \llbracket \Phi \rrbracket)$  by standard fixed point iteration, using predecessor computation, as shown in Procedure 1. In the procedure,  $V$  and  $W$  are sets of patterns whose predecessors already have ( $V$ ) and have not ( $W$ ) been computed. In each iteration of the while loop, we choose a pattern  $\varphi$  from  $W$ . If  $\gamma_0 \in \llbracket \varphi \rrbracket$  then we have found a path from  $\gamma_0$  to  $\llbracket \Phi \rrbracket$ . Otherwise, we check whether  $\varphi$  is redundant, meaning that it is subsumed by some other pattern which will be or has been explored. If not, we add to  $W$  a set of patterns over-approximating  $pre(\mathcal{A}, \llbracket \varphi \rrbracket)$ . As a further optimization, not shown in Procedure 1, at line 7 we also remove patterns from  $V$  and  $W$  that are subsumed by  $\varphi$ ; keeping  $V$  and  $W$  small speeds up the procedure.

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**Procedure 1.** Backward Reachability Analysis
 

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**Require:** System model  $\langle \gamma_0, \mathcal{A} \rangle$  and a set  $\Phi$  of (bad) patterns

**Ensure:** If terminates; answers whether a configuration in  $\llbracket \Phi \rrbracket$  is reachable from  $\gamma_0$ 

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1  $V := \emptyset, W := \Phi$ 
2 while  $W \neq \emptyset$  do
3   choose  $\varphi \in W$ 
4    $W := W \setminus \{\varphi\}$ 
5   if  $\gamma_0 \in \llbracket \varphi \rrbracket$  then
6     return “Reachable”
7   if  $\forall \psi \in (V \cup W). \neg(\varphi \preceq \psi)$  then
8      $V := V \cup \{\varphi\}$ 
9     for each  $\alpha \in \mathcal{A}$  do
10       $W := W \cup \text{PRE}(\alpha, \varphi)$ 
11 return “Unreachable”

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The central part of Procedure 1 is the (nontrivial) computation of predecessors of a pattern; it is done as in Procedure 2, whose description follows. Procedure 2 terminates on any input, as all loops are finite.

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**Procedure 2.**  $\text{PRE}(\alpha, \varphi)$ 


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**Require:** Action  $\alpha = \langle L, R \rangle$ , pattern  $\varphi = \langle N_\varphi, E_\varphi, \mathcal{G}_\varphi^- \rangle$ 
**Ensure:**  $\Phi$  is a set of patterns satisfying  $\text{pre}(\alpha, \llbracket \Phi \rrbracket) \subseteq \llbracket \Phi \rrbracket$ 

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1  $\Phi := \emptyset$ 
2 Rename nodes in  $N_\varphi$  so that  $N_\varphi$  is disjoint from  $N_R$ 
3 for each partial injection  $h : N_R \rightarrow N_\varphi$  do
4   Rename each node  $h(n)$  in the range of  $h$  to  $n$ 
5   if  $\exists n \in \text{DOMAIN}(h) - N_L . \text{EDGES}_+(n, \varphi) \not\subseteq \text{EDGES}_+(n, R) \vee$   

    $\text{INCONSISTENT}(\varphi + R)$  then
6     skip
7   else
8      $\varphi' := (\varphi \ominus_\alpha R) + L$ 
9     for each  $G^- \in \mathcal{G}_\varphi^-$  do
10      if  $\text{INCONSISTENT}((L \ominus_E R) + G^-)$  then  

11         $\varphi' = \varphi' - G^-$ 
12      if  $\neg \text{INCONSISTENT}(\varphi')$  then
13         $\Phi := \Phi \cup \varphi'$ 
14 return  $\Phi$ 

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Let a *partial injection*, or *matching*, from a set  $N$  to a set  $N'$  be an injection from a nonempty subset of  $N$  to  $N'$ . For two patterns  $\varphi = \langle N_\varphi, E_\varphi, \mathcal{G}_\varphi^- \rangle$  and  $\psi = \langle N_\psi, E_\psi, \mathcal{G}_\psi^- \rangle$ , we use  $\varphi + \psi$  to denote  $\langle N_\varphi \cup N_\psi, E_\varphi \cup E_\psi, \mathcal{G}_\varphi^- \cup \mathcal{G}_\psi^- \rangle$ . When adding patterns, if the node and edge sets are not disjoint, the result is a “merge”. No automatic renaming is assumed.

We use the following two subtraction operations in Procedure 2. First, for a pattern  $\varphi = \langle N_\varphi, E_\varphi, \mathcal{G}_\varphi^- \rangle$ , and an action  $\alpha = \langle L, R \rangle$ , let  $\varphi \ominus_\alpha R$  be the pattern



$\psi = \langle N_\psi, E_\psi, \mathcal{G}_\varphi^- \rangle$ , with  $E_\psi = E_\varphi \setminus E_R$  and  $N_\psi = N_\varphi \setminus (N_R \setminus N_L)$ . Second, for a pattern  $\varphi = \langle N_\varphi, E_\varphi, \mathcal{G}_\varphi^- \rangle$ , and a hypergraph  $g = \langle N_g, E_g \rangle$ , let  $\varphi \ominus_E g$  be the pattern  $\psi = \langle N_\psi, E_\psi, \mathcal{G}_\varphi^- \rangle$ , with  $E_\psi = E_\varphi \setminus E_g$  and  $N_\psi = \text{NODES}(E_\psi)$ .

For a NAC  $G^-$ , we use  $\varphi + G^-$  to denote  $\langle N_\varphi, E_\varphi, \mathcal{G}_\varphi^- \cup G^- \rangle$  and  $\varphi - G^-$  to denote  $\langle N_\varphi, E_\varphi, \mathcal{G}_\varphi^- \setminus G^- \rangle$ . If  $n \in N_\varphi$ , let  $\text{EDGES}_+(n, \langle N_\varphi, E_\varphi, \mathcal{G}_\varphi^- \rangle)$  denote the set of edges in  $E_\varphi$  connected to  $n$ .

Procedure 2 first renames the nodes (line 2) to avoid unintended node collisions between  $\varphi$  and  $\alpha$ . Thereafter, the loop starting at line 3 performs a sequence of operations for each possible matching between some nodes of  $N_R$  and  $N_\varphi$ .

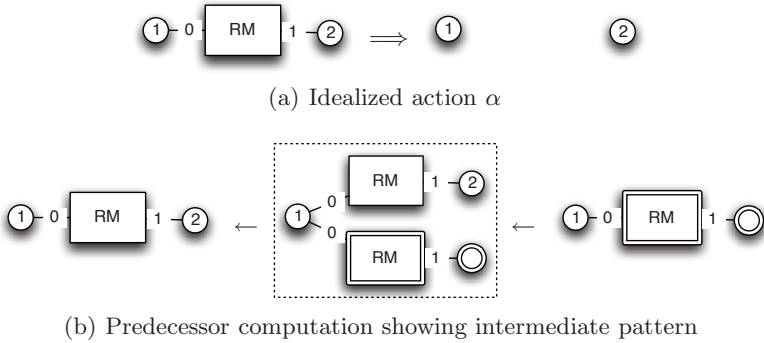
On line 4 each node  $h(n)$  in the range of  $h$  is renamed to  $n$ , in order to “merge”  $R$  and  $\varphi$  according to  $h$ . Since nodes that are created by  $\alpha$  must also have all their edges created by  $\alpha$ , we should discard matchings which violate this (line 5). On line 5 we also discard inconsistent matchings. The procedure  $\text{INCONSISTENT}(\varphi)$  returns true iff pattern  $\varphi$  is not consistent.

On line 8 the action  $\alpha$  is “executed” backwards to obtain a pattern  $\varphi'$  that is a potential predecessor of  $\varphi$ . Using the special subtraction  $\ominus_\alpha$  nodes and edges created by  $\alpha$  are removed from  $\varphi$ . On lines 9–11, we remove all NACs from  $\varphi'$  which contradict subgraphs removed by  $\alpha$ . This may introduce approximation (see the paragraph below). Since by definition  $\alpha$  cannot remove nodes, we use the special subtraction  $\ominus_E$  which ignores nodes not connected to edges. On line 12, we discard the resulting predecessor pattern if it is inconsistent – this can happen if a NAC in  $L$  contradicts a positive subgraph of  $\varphi'$ . Finally, if we reach line 13, we have found a predecessor pattern, which is added to  $\Phi$ .

*Approximation.* The predecessor computation in Procedure 2 sometimes introduces an approximation at line 11. If  $\alpha$  removes a subgraph which is forbidden by  $\varphi$ , then  $\text{pre}(\alpha, \llbracket \varphi \rrbracket)$  should say that there is exactly one subgraph of this form. However, patterns cannot express “exactly one” occurrence of a subgraph. In this situation, Procedure 2 therefore lets the resulting pattern say that “there is at least one occurrence” of this subgraph. As an example, consider the simple situation in Figure 3, where  $\alpha$ , shown in Figure 3(a), removes an RM-edge between two nodes, and  $\varphi$ , the rightmost pattern in Figure 3(b), says that there is no RM-edge. The exact predecessor of  $\varphi$  is: “there is exactly one RM-edge between two nodes”. However, the resulting predecessor (the leftmost pattern in Figure 3(b)) represents that there is *at least* one RM-edge connected to graph node 1. To illustrate the effect of lines 9–11 of Procedure 2, an intermediate pattern, where the contradiction has not yet been resolved, is shown in Figure 3(b).

*Optimizations.* To make the analysis more efficient, we have (implemented) two mechanisms for the user to specify simple type constraints. One is to annotate nodes with types that are respected in the analysis, with the semantics that nodes may only “match” nodes of same type. Another is to add patterns that describe multiplicity constraints on edges. For example, our DYMO models use “a network node can have at most one routing table”, by specifying a pattern where a node has two routing tables as “impossible”.

We need to model integer-valued variables, as DYMO uses sequence numbers and hop counts. This is done by representing integers as nodes, and greater



**Fig. 3.** Approximation due to upwards-closure

than ( $>$ ) and equality ( $=$ ) relations as edges between these nodes. We do not represent concrete integer values. Hence, we cannot compare integers which are not connected by a relational edge. We have extended our tool to handle the transitive closure of  $>$  and  $=$ , as part of the predecessor computation. For each predecessor pattern generated, the closure of all transitive numerical relations present in the pattern is computed. New relational edges are then added to the pattern accordingly. The reason is that our syntactic subsumption check cannot deduce such semantic information about relations. The check for created nodes on line 5 of Procedure 2 was also extended to take into account the transitivity of numerical relations.

## 5 Modeling and Verification of DYMO

In this section we describe how we modeled the DYMO protocol (more precisely, the latest version at the time of writing, version 10 [11], and version 5). See our project home page [14] for the complete models. In total, our DYMO v10 model consists of one initial graph (“an empty network”) and 77 actions. Of these, 38 actions model routing table update rules, similar to the one in Figure 4 below. We have only used unary and binary hyperedges in our models, although our implementation supports hyperedges of any arity.

*Modeling network topology and message transmission.* We represent arbitrary network topologies by letting the initial system configuration be an empty network (i.e., an empty graph), and including an action for creating an arbitrary network node; thus any initial topology can be formed. We do not explicitly model connectivity in the network. Instead all nodes can potentially react on all messages in the network; this reaction on a message can be postponed indefinitely, corresponding to a node being out of range or otherwise incapable of receiving the message. Messages can also be non-deterministically removed, corresponding to message loss. In our modeling of message transmission, messages are left in the network after a node has handled them (until they are potentially dropped): this accounts for messages being duplicated.

*Handling of timeouts and hop limits.* DYMO uses timeouts to determine if a RREQ should be retransmitted, if a link is broken, or if a routing table entry should be removed. We over-approximate timeouts as “event  $x$  can happen at any time”, which covers all possibilities for a timeout. It is known from previous work on the AODV protocol [8], that if entries are removed from the routing table, loops may form. The reason is that obsolete information can then be accepted. In DYMO, routing table entries are invalidated (set to broken) after some time, and later removed; temporary loops are thus tolerated. We exclude removal of routing table entries from our analysis; they can only be invalidated. In practice, we thus verify loop-freedom under the assumption that routing table entries are kept “long enough”.

We do not model DYMO hop limits [11], used to limit packet traversal. However, since we include actions for arbitrary dropping of RMs and RERRs, we implicitly cover all possible hop limit settings.

*Routing table update rules.* The DYMO specification [11] prescribes when a node should update its own routing table upon receiving routing data, i.e., when received routing data should replace existing data. Existing data is represented by a routing table entry, with fields `RouteSeqNo`, `RouteHopCnt`, and `Broken`. Received data is represented by a routing message with fields `OrigSeqNo`, `NodeHopCnt` and message type `RM` – either a route request (RREQ) or a route reply (RREP). The table entry should be updated in the following cases:

1. `OrigSeqNo > RouteSeqNo`
2. `OrigSeqNo = RouteSeqNo ∧ NodeHopCnt < RouteHopCnt`
3. `OrigSeqNo = RouteSeqNo ∧ NodeHopCnt = RouteHopCnt ∧ RM = RREP`
4. `OrigSeqNo = RouteSeqNo ∧ NodeHopCnt = RouteHopCnt ∧ Broken`

The rules say that an update is allowed if (1) the message has a higher sequence number for the destination, or (2) the message has the same sequence number, but a shorter route, or (3) the message has the same routing metric value, and the message is a route reply, or (4) the table entry is broken. See Figure 4 for an illustration of how we model the update rules. The figure corresponds to rule (2). In our framework, we have to model each combination of network nodes used in the rules, such as when `IPSource` equals `Orig`, or `RouteNextHopAddress` equals `RouteAddress`, etc., as separate actions; however, we have tool support for doing this.

*Formalizing the non-looping property.* A central property of ad hoc routing protocols is that they never cause routing loops, as a routing loop prevents a packet from reaching its intended destination. A routing loop is a nonempty finite sequence of nodes  $n_1, \dots, n_k$  such that for some destination  $D$  it holds that for all  $i : 1 \leq i \leq k$  node  $n_{(i+1)(\text{mod } k)}$  is the next hop towards  $D$  from node  $n_i$ , and  $n_i \neq D$ .

We define the ordering  $<_D$  on nodes in a configuration as:  $n <_D n'$  iff  $\text{RouteSeqNo}_n(D) > \text{RouteSeqNo}_{n'}(D) \vee (\text{RouteSeqNo}_n(D) = \text{RouteSeqNo}_{n'}(D) \wedge \text{RouteHopCnt}_n(D) < \text{RouteHopCnt}_{n'}(D))$ . There can be no routing loops towards a destination  $D$ , if each hop from a node  $n$  towards  $D$  goes to a node  $n'$  with

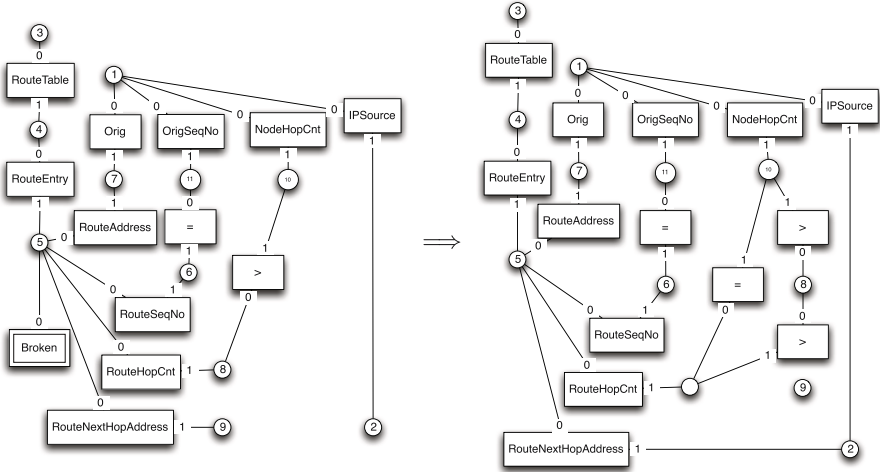


Fig. 4. Action modeling a routing table update

$n' <_D n$ . Since  $<_D$  is a partial order, any routing path towards  $D$  can contain a node at most once. The same ordering was used in the proof of loop freedom for AODV in [8]. The following property,  $LP$ , implies the pairwise ordering along routing paths; if  $LP$  is invariant for DYMO, there are no routing loops.

$$\left. \begin{array}{l} \forall A, B, D \\ A \neq B, B \neq D, \\ A \neq D \end{array} \right| \text{RouteNextHopAddress}_A(D) = B \implies B <_D A \quad (LP)$$

By negating the loop property (LP), we obtain a characterization of the bad system configurations. Loops may thus form if the sequence number strictly decreases, or the sequence number stays the same but the hop count does not decrease, between a node  $A$  and its next hop  $B$  on a route towards a destination node  $D$ . In our verification of DYMO, we verify unreachability for a set of six bad patterns. Three represent a disjunct of  $(\neg LP)$  under quantification; two represent a network node with a routing table entry pointing to the node itself; and one pattern represents that a node has a next hop (which is not  $D$ ) towards some destination  $D$ , but the next hop has no entry for  $D$ . As an example, a pattern representing one of the disjuncts of  $(\neg LP)$  is shown in Figure 5.

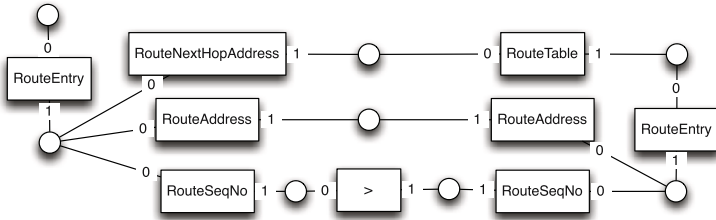


Fig. 5. Graph pattern representing a set of bad system configurations in DYMO

## 6 Experimental Results

We have modeled and verified the DYMO protocol as described in Sections 5 and 4. Recall that the analysis is under an assumption of routing table entries not being removed. The analysis has been performed using our tool GBT (Graph Backwards Tool). GBT and the models are available at our project home page [14]. The tool uses the `.dot` format for describing hypergraphs and patterns (input and output). If the initial configuration can be reached, an error trace, showing a sequence of actions leading to one of the bad patterns, is provided. Note that this trace may be spurious, due to over-approximation.

We have verified the latest DYMO version at the time of writing, namely version 10 of the Internet draft [11], as well as an older draft (version 5). Our results are presented in Table 1. In the “dest. reply” models, only the destination node replies to an RREQ, whereas in “interm. reply”, intermediate nodes may also reply (in case they have a fresh enough route, see [11]). Column *Actions* contains the number of actions in the model. *Checked* contains the total number of unique non-impossible patterns generated by the predecessor computation, plus the ones given as input. *Covered* contains the patterns which were subsumed (see Section 4). *Left* contains the patterns left after the analysis has finished; none of them contain the initial graph. *Time* contains the total verification time (GBT start to end) on a machine with an AMD Opteron 2220 2.8 GHz processor.

**Table 1.** Measurement results from using GBT

Protocol	Actions	Checked	Covered	Left	Verified	Time
DYMO draft 10						
- dest. reply	56	185751	185695	56	Yes	2h 24 min
- interm. reply	77	295164	295108	56	Yes	4h 31 min
DYMO draft 05	50	118685	118637	48	Yes	1h 20 min
Pub/priv srv I	12	498	484	14	Yes	0.73 s
Pub/priv srv II	13	629	609	20	Yes	0.94 s
Firewall I	6	129	126	3	Yes	0.11 s
Firewall II	6	129	126	3	Yes	0.11 s

In Table 1 we have also included GBT verification results for the “Public/private servers” and “Firewall” examples, used by König and Kozioura [19]. These examples required modifications to work with our tool: a NAC was added to the left hand side of an action in “Public/private servers II” and the transitivity handling in our tool was extended to include communication channels.

## 7 Conclusions and Future Work

We have described and implemented a general framework for modeling and verification of protocols using a variant of graph transformation systems, and applied

it to automatically prove loop freedom of the DYMO v10 ad hoc routing protocol. We expect that several of the actions used in our DYMO model need only small modifications to work for other ad hoc routing protocols categorized as reactive (i.e., on-demand). The reason is that reactive ad hoc routing protocols generally use the same kind of flooding route discovery mechanism; examples include AODV[21], DSR[16], and LUNAR[24] (see [20] for an extensive list).

As GTSs with NACs make up quite a generic modeling framework, there should be possibilities for interesting case studies, and further development. Directions for future work include further optimizations of the predecessor computation, e.g., by early detection of unfruitful matchings. We are currently working on a new DYMO model, to investigate the effect on run-time performance when using hyperedges of arity greater than two. Termination of the reachability analysis can be obtained by bounding and truncating the generated patterns, at the cost of over-approximation, e.g., by enforcing a maximum size. The possibility of spurious counter-examples, due to approximations in the predecessor computation, motivates looking at counter-example guided abstraction refinement.

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