Erratum

Analog Integrated Circuits for Communication: Principles, Simulation and Design

Second Edition

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Note to Instructors:

The website address is missing a˜. The correct link is http://eecs.oregonstate.edu/~karti/book/spicefiles

Chapter 3:

In Figure 3.10, the Fourier components of the typical circuit are to be ignored.

Chapter 6:

For Problem 6.3, the transistor parameters are missing. Please use kp = $30\mu A/V^2$, Vt = 0.7V, W/L = 50.

Chapter 11:

In Problem 11.3, the $10\,k\Omega$ resistance in series with the inductor should be $10\,\Omega$.

In Problem 11.7, part (e) should be labeled (d) and (f) should be labeled (e).

In Problem 11.8, the 2nd part (a) should be labeled (b), (b) -> (c), (c) -> (d).

In Problem 11.8, part (f) should read: Using the result of (e) calculate the amplitude of the gate voltage.

Chapter 14:

p. 459, last line of 2nd paragraph: 14.3 kHz should be 13.3 kHz.

p. 476, last line on page: b_1 = 59 mV and HD_2 = 2.3%.

p. 477, first line on page: HD_3 = 0.9% and THD = 3.6%.

In Problem 14.6, $+V_{CC}/-V_{EE}$ are $+10/-10$ V.