HARDWARE DESIGN OF TWO WEIGHTED NEURAL NETWORK AND APPLICATION FOR OBJECT RECOGNITION

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Abstract: In this paper, the design methodology of neural network hardware has been discussed, and two weighted neural network implemented by this method been applied for object recognition. It was pointed out that the main problem of the two weighted neural network hardware implementation lies in three aspects. At final, two weighted neural network implemented by this method is applied for object recognition , and the algorithm were presented . We did experiments on recognition of omnidirectionally oriented rigid objects on the same level, using the two weighted neural networks. Many animal and vehicle models (even with rather similar shapes) were recognized omnidirectionally thousands of times. For total 8800 tests, the correct recognition rate is 98.75%, the error rate and the rejection rate are 0.5 and 1.25% respectively.

Key words: Pattern recognition, Two weighted Neural Networks, High dimensional Geometry

1. INTRODUCTION

As a suitable calculation method for parallel computing, the two weighted neural networks have been widely used in application for control theory1, speaker recognition2 etc. But how to realize the two weighted neural network computing, there are various methods such as realizing it by software or by full curing special neural network chip. The purpose of this paper is to research the application realized by semiconductor hardware and the process of enlarging the application. In section 2, we described its structure. In section 3, it was pointed out that the main problem of the two weighted neural network hardware implementation lies in three aspects. Finally, two weighted neural network implemented by this method is applied for object recognition, and the algorithm were presented . We did experiments on recognition of omnidirectionally oriented rigid objects on the same level, using the neural networks. Many animal and vehicle models (even with rather similar shapes) were recognized omnidirectionally thousands of times. For total 8800 tests, the correct recognition rate is 98.75%, the error rate and the rejection rate are 0.5 and 1.25% respectively.

2. STRUCTURE OF TWO WEIGHTED NEURAL NETWORKS

The model is

$$Y = f\left[\sum_{j=1}^{n} \left(\frac{W_j}{|W_j|}\right)^s | W_j(X_j - W_j)|^P - \theta\right]$$

In which:

Y is the output of neuron ;

f(...) is the neuron activation function;

 X_i is neuron jth input;

 $W_i W_i$ are direct weight and kernel weigh;

 θ is the threshold;

n is the dimension of input.

We can easily get: if S=1,P=1 and all W are zero, then the above equation becomes the traditional BP neuron; if S=0,P=2 and all the W are one, then the neuron becomes RBF neuron; if S=0, change the value of W and P, and if the basis of function f(...) is zero, the track of input X becomes the following closing hypersurfaces. Figure 1, ts topography structure Figure 2.



Figure 1. closing hypersurfaces



Figure 2. Structure of two weighted neural network

3. ANALYSIS OF THE PROPERTY OF TWO WEIGHTED NEURAL NETWORKS HARDWARE

Artificial two weighted neural networks are a computing method and device with a large number of computing units specially connected to form complex computing functions.

Let's take the currency degree for example: in scientific research, without the demand for particular use and structure, the two weighted neural networks generally need more powerful currency but the two weighted neural networks used for engineering need less powerful currency. However for those trained two weighted neural networks with fixed uses, (even the weights are fixed), there is no demand for currency. So we can divide all the demands into three fuzzy grades, that is high (H), middle (M) and low (L). (H) means the demand is high, that is to say: high computing speed or high precision or large net scale or strong flexibility or low price or easy to use or quantity. (L) However means the demand is lower and (M) means the demand is between the above two. For different purpose and different realization, we get the mapping relationship shown in figure by dividing all factors into three grades. As shown in table 1, we should select different approach for those two weighted neural networks with different demand. Improper approach may result the great inferior position on ratio of performance to price. A humor in Dan Hammerstrom[5] says: For a fully parallel network chip with $1 \mu m$ interconnects and 109 connections, the spare of the connection is $89m^2$.

4. EXPERIMENT FOR OBJECT RECOGNITION

By circling 8 animal and vehicle models (displayed in Fig.3) by 360 degrees respectively, each provides 400 samples. The whole of total 3200 samples of the 8 kinds is referred to the first sample set. Then, repeat the above process at a different time to get the second one. Finally, 2400 samples of another 6 animal models (displayed in Fig.4) are assembled in a similar way. The whole of the total 2400 samples is referred to the third sample set. The procedures and results of the experiment are labeled as follows:

According to the distance of samples of the same kind, select samples of each kind in inconsistent numbers ranging from 26 to 50 respectively from the total 3200 samples of the first sample set to compose a training set, which consequently consists of 328 samples from all 8 kinds.

Construct 8 recognition networks corresponding to the 8 models under recognition , where the number of neurons is equal to that of the samples in the training set .

Take the total 6400 samples of the first and second sample sets for correct recognition rate test. Consequently, 6384 samples were recognized successfully while the rest 16 ones failed. However, the rest 16 samples are totally attributed to the case of rejection. Therefore, the correct rate and rejection rate are 99.75% and 0.25% respectively.



Figure 3. Testing Sample Set (1)



Figure 4. Training Sample Set (2)

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| Factors and | | Application | | | | Implement Approach | | | |
|--------------------|-------------|-------------|---|---|----|--------------------|-----|------|------|
| Requests | | ST | Е | I | SP | s | DSP | PS | OTON |
| | | | | | | | A | TWNN | С |
| Computing Speed | H M L | Н | L | Н | Н | L | Н | Н | Н |
| Accuracy | H M L | н | М | Н | М | н | Н | Н | L |
| Net Scale | H M L | Н | М | М | М | н | Н | Н | L |
| Flexibility | H M L | н | М | М | М | Н | Н | Н | L |
| Cost | H M L | L | Н | М | L | Н | L | М | Н |
| Easy to Use | H M L | L | Ĺ | Н | Н | М | L | Н | М |
| Quality | H M L | L | L | М | М | М | L | М | Н |

Table 1. Two weighted Neurocompouter Chip