2014 CAV award announcement

Marta Kwiatkowska¹ · Moshe Y. Vardi² ·
Ahmed Bouajjani³ · Thomas Ball⁴

Published online: 6 April 2016
© Springer Science+Business Media New York 2016

Abstract The 2014 computer-aided verification (CAV) award was presented on July 19, 2014, at the 26th annual CAV conference in Vienna to Patrice Godefroid, Doron Peled, Antti Valmari, and Pierre Wolper for the development of partial-order-reduction algorithms for efficient state-space exploration of concurrent systems.

Keywords Computer-aided verification · CAV Award · Concurrency · Model checking · Partial order reduction

1 Introduction

The 2014 computer-aided verification (CAV) award was presented on July 19, 2014, at the 26th annual CAV conference in Vienna, to Patrice Godefroid, Doron Peled, Antti Valmari, and Pierre Wolper. The annual award, which recognizes a specific fundamental contribution or a series of outstanding contributions to computer-aided verification includes a \$10,000 award. The award was presented with the citation: “for the development of partial-order-reduction algorithms for efficient state-space exploration of concurrent systems.”

✉ Marta Kwiatkowska
Marta.Kwiatkowska@cs.ox.ac.uk

Moshe Y. Vardi
vardi@cs.rice.edu

Ahmed Bouajjani
abou@liafa.univ-paris-diderot.fr

Thomas Ball
tball@microsoft.com

¹ Department of Computer Science, University of Oxford, Parks Road, Oxford OX1 3QD, UK

² Department of Computer Science, Rice University, Houston, TX 77005, USA

³ LIAFA, Université. Paris Diderot (Paris 7), Case 7014, 75205 Paris Cedex 13, France

⁴ Microsoft Research, One Microsoft Way, Redmond, WA 98052, USA

The CAV conference is the premier international event for reporting research on computer-aided verification, a sub-discipline of computer science that is concerned with ensuring that software and hardware systems operate correctly and reliably. The CAV award was established in 2008 by the conference steering committee and was given this year for the seventh time.

2 The award-winning contribution

Concurrency is omnipresent in computer systems, at all levels, from concurrent software running over multi-core platforms to distributed applications running over large-scale networks. The automated verification of such systems is a challenging problem, due to the highly complex interactions between their components. Standard verification algorithms based on systematic state-space exploration face the well-known state-explosion problem when applied to such systems: the size of the state space of concurrent systems grows exponentially in the number of their components. A major approach for tackling this problem and developing scalable algorithms for automated verification of concurrent systems comes from so-called partial-order reduction (POR) techniques, which leverage the fact that there often are a huge number of computations that lead to the same observable states (i.e., states that are relevant for checking the property under consideration), but differ only in the order in which some parallel actions are interleaved. These actions are actually independent of one another (in the sense that they are not conflicting, so partially ordered), thus, their total ordering in a particular execution is not relevant. Therefore, the idea is to consider such computations as equivalent, so that only one representative of each equivalence class needs to be considered during the state-space exploration. The notion of independence was first formulated in 1977 by Antoni Mazurkiewicz, and has been studied since then in the context of concurrency semantics by a number of authors. Nevertheless, it was not until the first half of 1990s that these ideas have been applied in algorithmic verification.

While the POR idea seems natural and simple in principle, the difficulty is in designing efficient search algorithms that determine on-the-fly, i.e., during the state-space exploration of the system, which branches to prune and which ones to explore, while ensuring (1) that redundant explorations are avoided, and (2) that the state-space exploration is still complete, i.e., no computation equivalence class (modulo reordering of independent actions) is missed. In the period of 1990–1994, Godefroid, Peled, Valmari, and Wolper investigated this problem and defined algorithms for solving it, based on the notions of stubborn, sleep and ample sets, respectively. These algorithms constitute the basis of the POR approach to model checking, which has been subsequently developed further, extended to various classes of systems, and integrated into several verification methods and tools. These tools are now widely used and applicable both in academia and industry. Indeed, several robust and influential verification tools rely on POR, and this approach has been applied to many different contexts and continues to have impact to this day. Among the tools where POR techniques are used, one can mention SPIN, Verisoft, FLAVERS, ISP, JPF, CHESS, and many other model-checking and systematic testing tools. In summary, POR is one of the major contributions to the field of automated verification in the last two decades. Its development contributed in a crucial way to make model checking successful and practically applicable to concurrent systems. This success is due to the seminal work that Godefroid, Peled, Valmari, and Wolper did in the first half of the 1990's. With this award, the community recognizes the historical importance and the deep impact of their contribution.

3 CAV conference

The computer-aided verification (CAV) conference is an annual international conference dedicated to the advancement of the theory and practice of computer-aided formal analysis methods for hardware and software systems. The conference covers the spectrum from theoretical results to concrete applications, with an emphasis on practical verification tools and the algorithms and techniques that are needed for their implementation. The CAV conference was founded in 1989 by Edmund M. Clarke, Robert P. Kurshan, Amir Pnueli, and Joseph Sifakis. The first CAV conference was hosted in 1989 in Grenoble, France, and since then it has been held in multiple sites in North America, Europe, and the Middle East. The twenty-sixth CAV conference was held in Vienna, Austria, from July 18 to July 22, 2014, as part of the Vienna Summer of Logic.

4 CAV award recipients

2008	Rajeev Alur, University of Pennsylvania David L. Dill, Stanford University
2009	Conor F. Madigan, Kateeva, Inc. Sharad Malik, Princeton University João P. Marques-Silva, University College Dublin, Ireland Matthew W. Moskewicz, University of California, Berkeley Karem A. Sakallah, University of Michigan Lintao Zhang, Microsoft Research Ying Zhao, Wuxi Capital Group
2010	Kenneth L. McMillan, Cadence Research Laboratories
2011	Thomas Ball, Microsoft Research Sriram Rajamani, Microsoft Research
2012	Sam Owre, SRI John Rushby, SRI Natarajan Shankar, SRI
2013	Kim G. Larsen, University of Aalborg Paul Pettersson, Mälardalen University Wang Yi, Uppsala University
2014	Patrice Godefroid, Microsoft Research Doron Peled, Bar Ilan University Antti Valmari, Tampere University of Technology Pierre Wolper, Université de Liege
2015	Edmund M. Clarke, Carnegie-Mellon Orna Grumberg, Technion Ronald H. Hardin Somesh Jha, University of Wisconsin Yuan Lu Robert P. Kurshan Helmut Veith, FORSYTE Zvi Harel