Erratum

Mehmet Incekaraoglu and Ugur Cam

In the paper [1], we made a drawing error in Figure 3.b and a typing error in the second row of Table 1. The correct version of the Figure 3.b is given in Figure 1. The equations of Figure 3.b in [1], is also valid for the below configuration.

Fig. 1. The correct version of Fig 3.b in [1].

The equations of Figure 2.b should be read as in the following

Table 1. Actively realizable inductance forms

Figure	L_{eq}	G_{eq}	Type
2.b	$-\frac{C}{G_2 G_3}$	$-\frac{G_1}{G_2 G_3}$	$-L$ series with $-R$

[1] Incekaraoglu, M and Cam, U, “Realization of Series and Parallel R-L and C-D Impedances using Single Differential Voltage Current Conveyor,” *Analog Integrated Circuits and Signal Processing*, vol. 43, pp. 101–104, 2005.