Erratum to: Nanometer CMOS ICs

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H.J.M. Veendrick, *Nanometer CMOS ICs*,

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The original version of the book contained errors which have been corrected. The corrections are given below:

Chapter 1: Basic Principles

In page 4, three lines above the heading 1.3, the paragraph text is currently “ $n_p \gg n_a$ ” and it should be “ $n_p \gg n_n$ ”

Chapter 10: Testing, Yield, Packaging, Debug and Failure Analysis

There is an error in the figure 10.12. The expression/formula inside the figure is “ $Y = M \cdot e$ ”. Whereas, it should be is “ $Y = Y_s \cdot e^{-D_0 A}$ ” the same as in expression 10.1

The updated online versions of these chapters can be found at

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