

# NICE - New Ideal Coset Encryption -

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**Abstract.** Recently, a novel public-key cryptosystem constructed on number fields is presented. The prominent theoretical property of the public-key cryptosystem is a quadratic decryption bit complexity of the public key, which consists of only simple fast arithmetical operations. We call the cryptosystem NICE (New Ideal Coset Encryption). In this paper, we consider practical aspects of the NICE cryptosystem. Our implementation in software shows that the decryption time of NICE is comparably as fast as the encryption time of the RSA cryptosystem with  $e = 2^{16} + 1$ . To show if existing smart cards can be used, we implemented the NICE cryptosystem using a smart card designed for the RSA cryptosystem. Our result shows that the decryption time of NICE is comparably as fast as the decryption time of RSA cryptosystem but not so fast as in software implementation. We discuss the reasons for this and indicate requirements for smartcard designers to achieve fast implementation on smartcards.

**Key words:** public-key cryptosystem, fast decryption, quadratic order, smart card implementation.

## 1 Why NICE?

Plenty of public-key cryptosystem not relying only on the RSA cryptosystem have been proposed. They are stemming from deep number theory (hyper- and superelliptic curves) to geometry and combinatorics (LLL-based systems). One major advantage of RSA is its simplicity: it can be easily implemented, one only needs a moderate background in mathematics to understand it. Moreover, RSA is quite fast - there exist public key cryptosystems which are much faster, but the combination of simplicity, speed and confidence in its security makes it the most practical cryptosystem. Until now, only one other system may be considered as equally interesting: ElGamal type systems on elliptic curves. Elliptic curves are somewhat more complicated, and the best known algorithms to break elliptic curve cryptosystems are much slower, in the order of exponential complexity.

So, both speed and security are worth being paid by the higher mathematical complexity.

But: both systems have one drawback in common: the decryption/signing time is of cubic complexity in the bit length of the public key for both systems because these steps consist of modular multiplication(s). This becomes even more important when thinking of smart cards. The smart card is considered to be the personal security computing device of tomorrow. It contains all personal secret information, especially the private keys for public key systems like decryption and signing. The complexity of PC operating systems is too high to be reliably secure; every relevant security operation should be effected by the smart card. Non-relevant operations like public key encryption and signature verification could be done by the PC. Operations which cannot be transferred to the PC at all due to security reasons are decryption and signing. Considering RSA, the task which has to be effected by a low power computing device is precisely the most complex task. Moreover, we can expect that the key length of the public key will increase with the progress of hardware technology. In addition, there are no guarantees that new sub-exponential attacks for the basic number theoretic problem will not be suddenly proposed. Therefore it would be better to have a more efficient public key cryptosystem.

As an alternative, we might use a new public-key cryptosystem constructed over number fields [18]. The cryptosystem has a theoretically fast decryption process such as a quadratic decryption complexity of bit-length of a public-key, which consists of only simple fast arithmetical operations. So even if the key length gets bigger in the future, there will be no great increase of the computational complexity. This becomes even more important when thinking of smart cards. In this paper, we call the new cryptosystem NICE, (New Ideal Coset Encryption). We focus on the practical aspects of NICE cryptosystem. We implement the NICE cryptosystem over different architectures, namely software on a standard PC and on a smartcard designed for the RSA cryptosystem. Our implementation in software shows NICE is as fast as the encryption time of the RSA cryptosystem with  $e = 2^{16} + 1$ . Implementation on a smartcard designed for the RSA cryptosystem is comparably as fast as the decryption time of the RSA cryptosystem but not so fast as in software implementation. We discuss the reasons for this and indicate requirements to achieve fast implementation on smartcards.

This paper is organized as follows: In section 2, we give several applications based on NICE cryptosystem. In section 3, we explain the details of the algorithms of the NICE cryptosystem. In section 4, we show timings of the implementation in software. In section 5, we discuss a smart card implementation and its problems.

## 2 Applications

In section 3, we will present NICE in the formulation of an encryption scheme. An immediate application is therefore session key distribution from a powerful server

to a device which has a limited computing power or where time is important. An example for such a device is e.g. a mobile phone.

Another application of NICE is the use as an authentication scheme. The usual protocols (2-way and 3-way) can be adapted to use NICE as encryption component. Of course, it could be combined with RSA, where the modulus is the absolute value of the discriminant of the non-maximal order, i.e.  $\Delta_q$ . In that case, the 3-way protocol can be realized in such a way that the client (= the low computing power device) only effects the fast components of both algorithms.

As a last application, we propose an undeniable signature scheme. NICE itself cannot be used as a classical signature scheme; undeniable signature schemes have their own use e.g. in online transactions. A signature of this kind cannot be verified without the interaction of the signer. The standard example for its application is its use by a software development company: the distributed software is signed by means of an undeniable signature of the company to allow legal users to ensure themselves that they use unmodified software. Since interaction with the seller is needed to check the signature, illegal users either cannot check and risk to use some virus-infected software or will be traced by the software-seller as soon as they ask for interactive verification. Details can be found in [4]. Again, the low computing power device only effectuates the NICE decryption steps, so smart cards can be used for assuring the security of online transactions.

### 3 The NICE Cryptosystem

In this section, we present an overview of the NICE cryptosystem. Details can be found in [18]. The idea of NICE is roughly as follows: consider two finite abelian groups  $G$  and  $H$  which are related by a surjective map  $\pi : G \rightarrow H$ . Moreover, there exists a well-defined bijective mapping of sets  $\phi : H \rightarrow U$  of  $H$  onto a subset of  $U$  of  $G$  such that  $\pi(\phi(M)) = M$  for all  $M \in H$ . The representation of elements of  $G$  and the group operation algorithm of  $G$  are publicly known, as well as an element  $h$  of the kernel of  $\pi$ .  $U$  is chosen such that a consecutive set of representations of elements of  $G$  are representations of elements of  $U$ . This information is publicly known. Assume that you know the group  $H$  (i.e. representation of group elements and group operation) and how to compute  $\pi$ , but no one else does. The message space consists of the publicly known elements of  $U$ . Now, a message  $m$  is probabilistically encrypted by randomly multiplying an element  $h^r$  of  $\text{Ker}\pi$  onto it: the ciphertext is  $c = m * h^r$ . Decryption simply works as follows: compute  $\phi(\pi(m * h^r))$ .

This is a secure cryptosystem if the computation of the map  $\pi$  cannot be deduced from the given information, namely the group  $G$ , the kernel element  $h$  and the test for  $U$ . There exist some constructions of this scheme using number theoretic problems, e.g. [17]. An overview can be found in [19].

The following implementation of this scheme is especially interesting: Generate two random primes  $p, q > 4$  such that  $p \equiv 3 \pmod{4}$  and let  $\Delta_1 = -p$ . Let  $H = Cl(\Delta_1)$  be the ideal class group of the maximal order with discriminant  $\Delta_1$  and  $G = Cl(\Delta_q)$  be the ideal class group of the non-maximal order with

conductor  $q$ .  $\Delta_q$  will be public, whilst its factorization into  $\Delta_1$  and  $q$  will be kept private.

Any element of  $G$  is given by a pair of numbers  $(a, b)$  such that  $0 < a \leq \sqrt{|\Delta_q|/3}$ ,  $-a < b \leq a$  and  $b^2 \equiv \Delta_q \pmod{4a}$  (and some other minor requirements). Elements of  $H$  are represented in the same way with  $\Delta_1$  instead of  $\Delta_q$ . The group operation works as follows:

**Composition in  $\text{Cl}(\Delta_q)$**

**Input:**  $(a_1, b_1), (a_2, b_2) \in \text{Cl}(\Delta_q)$ , the discriminant  $\Delta_q$

**Output:**  $(a, b) = (a_1, b_1) * (a_2, b_2)$ .

1. */\* Multiplication step \*/*

1.1. Solve  $d = ua_1 + va_2 + w(b_1 + b_2)/2$  for  $d, u, v, w \in \mathbb{Z}$  using the extended Euclidean algorithm

1.2.  $a \leftarrow a_1 a_2 / d^2$

1.3.  $b \leftarrow b_2 + (va_2(b_1 - b_2) + w(\Delta_q - b_2^2)/2) / d \pmod{2a}$

2. */\* Reduction step \*/*

2.1.  $c \leftarrow (\Delta_q - b^2) / 4a$

2.2. WHILE  $\{-a < b \leq a < c\}$  or  $\{0 \leq b \leq a = c\}$  DO

2.2.1. Find  $\lambda, \mu \in \mathbb{Z}$  s.t.  $-a \leq \mu = b + 2\lambda a < a$  using division with remainder

2.2.2.  $(a, b, c) \leftarrow (c - \lambda \frac{b+\mu}{2}, -\mu, a)$

2.3. IF  $a = c$  AND  $b < 0$  THEN  $b \leftarrow -b$

2.4. RETURN  $(a, b)$

This algorithm has quadratic bit complexity  $O((\log_2 \Delta_q)^2)$  and is only needed for the encryption step. For the decryption step, we need only to compute  $\pi$ . The computation of the map  $\pi$  works as follows:

**Computation of  $\pi$**

**Input:**  $(a, b) \in \text{Cl}_{\Delta_q}$ , the fundamental discriminant  $\Delta_1$ , the discriminant  $\Delta_q$  and the conductor  $q$

**Output:**  $(A, B) = \pi((a, b))$ .

1.  $b_{\mathcal{O}} \leftarrow \Delta_q \pmod{2}$

2. Solve  $1 = uq + va$  for  $u, v \in \mathbb{Z}$  using the extended Euclidean algorithm

3.  $B \leftarrow bu + ab_{\mathcal{O}}v \pmod{2a}$

4.  $C \leftarrow (\Delta_1 - B^2) / 4A$

5. WHILE NOT  $(\{-A < B \leq A < C\}$  or  $\{0 \leq B \leq A = C\})$  DO

5.1 Find  $\lambda, \mu \in \mathbb{Z}$  s.t.  $-A \leq \mu = B + 2\lambda A < A$  using division with remainder

5.2  $(A, B, C) \leftarrow (C - \lambda \frac{B+\mu}{2}, -\mu, A)$

6. IF  $A = C$  AND  $B < 0$  THEN  $B \leftarrow -B$

7. RETURN  $(A, B)$

This algorithm  $\pi$  has quadratic bit complexity  $O((\log_2 \Delta_q)^2)$  ([18]). Moreover, only simple well-known operations are needed, thus this algorithm can easily be implemented on an existing smart card.

Finally, the test belonging to  $U$  is simply whether for an element  $(a, b)$   $a$  is smaller than  $\lceil \sqrt{|\Delta_1|/4} \rceil$  (or a lower bound thereof of the form  $2^k$ ). The computation of the map  $\phi$  will not be needed in that implementation.

We explain how the NICE encryption scheme works: In the key generation, we choose an element  $(h, b_h)$  from the kernel  $\text{Ker}\pi$  and make  $(h, b_h)$  public. The message  $m$  is embedded in an element  $(m, b_m)$  of  $Cl(\Delta_q)$  with  $m$  smaller than  $\lceil \sqrt{|\Delta_1|/4} \rceil$ . Encryption is done in the class group  $Cl(\Delta_q)$  by computing  $(c, b_c) = ((m, b_m) * (h, b_h)^r)$ , where  $r$  is a random integer smaller than  $2^l$  with  $l = \log_2 \left( q - \left( \frac{\Delta_1}{4} \right) \right)$ . Then, having the secret information, namely the knowledge of the conductor  $q$ , one can go to the maximal order and the image of the message  $(m, b_m)$  in the maximal order is revealed, since  $\pi(c, b_c) = \pi(m, b_m)$  and  $m$  can be recovered without computing  $\phi$ .

### The NICE encryption protocol

1. **Key generation:** Generate two random primes  $p, q > 4$  with  $p \equiv 3 \pmod{4}$  and  $\sqrt{p/4} < q$ . Let  $\Delta_1 = -p$  and  $\Delta_q = \Delta_1 q^2$ . Let  $k$  and  $l$  be the bit lengths of  $\lceil \sqrt{|\Delta_1|/4} \rceil$  and  $q - \left( \frac{\Delta_1}{4} \right)$  respectively. Choose an element  $(h, b_h)$  in  $Cl(\Delta_q)$ , where

$$\pi((h, b_h)) = (1, 0) \tag{1}$$

Then  $((h, b_h), \Delta_q, k, l)$  are the system parameters, and  $q$  is the secret key.

2. **Encryption:** Let  $(m, b_m)$  be the plaintext, in  $Cl(\Delta_q)$  with  $\log_2 m < k$ . Pick up a random  $l - 1$  bit integer and we encrypt the plaintext as follows using binary exponentiation and precomputation techniques:

$$(c, b_c) = (m, b_m) * (h, b_h)^r \tag{2}$$

Then  $(c, b_c)$  is the ciphertext.

3. **Decryption:** Using the secret key  $q$ , we compute  $(d, b_d) = \pi((c, b_c))$ . The plaintext is then  $m = d$ .

A message embedding technique and security aspects of this cryptosystem, we refer to [18]. Again, please note that this cryptosystem can easily be implemented using well-known techniques and existing smart cards. This will be shown in the next section.

## 4 NICE Running Times in Software

The prominent property of the proposed cryptosystem is the running time of the decryption. Most prominent cryptosystems require decryption time  $O((\log_2 n)^3)$ , where  $n$  is the size of the public key. The total running time of the decryption process of our cryptosystem is  $O((\log_2 \Delta_q)^2)$  bit operations. In order to demonstrate the improved efficiency of our decryption, we implemented our scheme using the LiDIA library [2]. It should be emphasized here that our implementation was not optimized for cryptographic purposes — it is only intended to

provide a comparison between RSA and NICE. The results are shown in table 1. In these tests, we did choose  $p \approx q \approx n^{1/3}$ , so that breaking RSA and NICE by factoring is approximately equally hard. Other variations and a discussion of the variants can be found in [18].

**Table 1.** Average timings for the new cryptosystem with 80-bit encryption exponent  $r$  compared to RSA with encryption exponent  $2^{16} + 1$  over 100 randomly chosen pairs of primes of the specified size on a Pentium 266 Mhz using the LiDIA library

$\log_2(\Delta_q)$	1024	1536	2048	3072
RSA encryption	2.2 ms	4.8 ms	7.5 ms	15.2 ms
RSA classical decryption	259.2 ms	751.3 ms	1643.9 ms	4975.6 ms
RSA decryption with CRT	110.5 ms	291.7 ms	629.7 ms	1855.5 ms
NICE encryption	602.9 ms	1180.1 ms	1902.0 ms	3933.5 ms
NICE decryption	3.8 ms	6.2 ms	10.0 ms	19.3 ms

Observe that one can separate the fast exponentiation step of the encryption as a “precomputation” stage. Indeed, if we can securely store the values  $(p, b_p)^r$ , then the actual encryption can be effected very rapidly, since it requires only one ideal multiplication and one ideal reduction. Moreover, using well-known techniques for randomized encryption, we can even reduce the encryption time much more. Note that no square root technique like the Pollard-rho method or Shanks’ algorithm are directly applicable to the ciphertext  $(c, b_c)$ , because the encryption consists of  $(c, b_c) = (m, b_m)(p, b_p)^r$  where  $r$  is a random exponent and  $(m, b_m)$  is the secret plaintext. This means that we can use a very short random exponent  $r$  having e.g. about 80 bits.

It should be mentioned that the size of a message for our cryptosystem is significantly smaller than the size of a message for the RSA encryption (e.g. 256 bit vs. 768 bit, or 341 bit vs. 1024 bit). In connection with the very fast decryption time, an excellent purpose for our cryptosystem could be (symmetric) key distribution. In that setting, the short message length is not a real drawback. On the other hand, the message length is longer than for ElGamal encryption on “comparably” secure elliptic curves (e.g. 341 bit vs. 180 bit).

**Table 2.** Rate of the speed increasing when the bit-length of a public-key becomes larger

$\log_2(n)$	1024	1536	2048	3072
RSA encryption	1	2.18	3.41	6.91
RSA classical decryption	1	2.90	6.34	19.20
RSA decryption with CRT	1	2.64	5.70	16.79
NICE encryption	1	1.96	3.15	6.52
NICE decryption	1	1.63	2.63	5.08

Note that even if a public-key becomes large, the rate at which the speed of decryption of NICE increases is not so large as that of the RSA cryptosystem. This shows the effectiveness of a quadratic decryption time of NICE cryptosystem. The ratio is given in table 2.

## 5 A Smart Card Implementation and Its Problems

Moreover, we implemented the NICE decryption on a smart card. More precisely, we implemented the NICE decryption algorithm using the Siemens development kit for chip card controller ICs based on Keil PK51 for Windows. As assembler we used A51, as linker L51 to generate code for the 8051 microcontroller family. The software simulation were made using dScope-51 for Windows and the drivers for SLE 66CX160S. Thus, we realized a software emulation of an assembler implementation of the NICE decryption algorithm to be run on the existing Siemens SLE 66CX160S. Unfortunately, the timings of this software simulation were unrealistic. So, we did run some timings on a hardware simulator for SLE 66CX160S. Thanks to Deutsche Telekom AG, Produktzentrum Telesec in Siegen and Infineon/Siemens in Munich for letting us use their hardware simulator. See the timings for decryption in table 3 for a smart card running at 4.915 MHz.

**Table 3.** Timings for the decryption of the new cryptosystem compared to RSA using the hardware simulator of Siemens 66CX160S at 4.915 MHz

$\log_2(n)$	1024
RSA decryption with CRT	490 ms
New CS decryption for $p \approx q \approx \Delta_q^{1/3}$	1242 ms
Improved version	1035 ms

The very first implementation was very inefficient; the straightforward algorithms used in the software comparison proved to be much slower on the smart card than the existing RSA on the card. This was surprising, but after a while this could be easily explained: the cryptographic coprocessor has been optimized for modular exponentiation. On the other side, NICE uses mostly divisions with remainder and comparisons. These operations are slow on the coprocessor, so we had to modify the decryption algorithm to speed it up in hardware. We describe here two significant changes:

The computation of the inverse of  $a$  modulo  $q$  (step 3 in the computation of  $\pi$ ) using the extended Euclidean algorithm took (with  $q \approx p \approx 341$  bit) about 9 seconds (!), whereas computing the inverse using Fermat's little theorem - by using fast exponentiation mod  $q$  - took less than 1 second. Note that the decryption time using this method is no longer of quadratic complexity.

In the reduction process the quotient in the division with remainder step is most of the time very small (say  $\leq 10$ , see Appendix A); to effect a division is

this case is much more time consuming than subsequent subtractions. We did replace reduction step 5

- 5.1 Find  $\lambda, \mu \in \mathbb{Z}$  s.t.  $-A \leq \mu = B + 2\lambda A < A$  using division with remainder
- 5.2  $(A, B, C) \leftarrow (C - \lambda \frac{B+\mu}{2}, -\mu, A)$

by the following algorithm.

- 5.1 WHILE  $B \leq -A$  OR  $B > A$  DO
  - 5.1.1 IF  $B < 0$  THEN  $B \leftarrow B + 2A; C \leftarrow C - (B + A);$
  - 5.1.2 ELSE  $B \leftarrow B - 2A; C \leftarrow C - (B - A);$

Every time that the bitlength of  $B$  was exceeding the bitlength of variant  $B$  by at least 3. Using this improvement, we could decrease the running time from about 1.8 s to 1.2 s. This is already faster than a 1024 bit RSA decryption without Chinese remainder theorem (approx. 1.6 ms).

The timings in table 3 were made including these two improvements. Moreover, a detailed timing analysis in Siegen showed that both our static memory management and as well as the cryptographic coprocessor are not optimal for this algorithm. We discuss this in the sequel. An average overview of the most time consuming parts is given in table 4.

**Table 4.** Detailed timings for different functions in the decryption of the new cryptosystem

Function	Average time over the whole computation
mul, multiplications on the coprocessor	170 ms
div, divisions on the coprocessor	231 ms
left.adjust, length correction of the variables	376 ms
C2XL, moving numbers into the coprocessor	118 ms
XL2C, moving numbers out of the coprocessor	223 ms
others (comparisons, small operations)	114 ms
Overall time	1242 ms

One major difference between RSA and NICE is the number of variables needed during the computation of the decryption algorithm. In our implementation, we need to store 11 variables of length at most 2048 bit. Computations of the cryptographic coprocessor are shortening these variables. Thus, we had to adjust the length of the variables after each important operation. This was done by moving the top nonzero bytes of the number to the fixed address of the number and so "erasing" leading zero bytes. To do this, we used the cryptographic coprocessor. Now the exact timings showed that about 33 % of the running time is spent by the function `left.adjust`, which effects this correction.

Now changing the memory management from static to dynamic (i.e. in the XL2C and C2XL functions making the appropriate changes and having additionally some registers holding the starting address of the numbers), we got an

improvement to **637 ms** using the software simulator. Both Infineon/Siemens and Deutsche Telekom reported the overall time of the hardware simulator now to be **1035 ms**. Note that the amount of memory required is **965 Bytes** and thus fits into a real SLE 66CX160S.

As one can see from table 4, another important time consuming operation is to move numbers into and out of the coprocessor. At this point, we would get a speedup of about 350 ms if we could leave the numbers in registers inside the coprocessor. It is clear that the currently used processor is not prepared for such operations, since it is optimized for RSA, thus operations with very few variables. At this point we ask the hardware community to present solutions to this problem.

## 6 Conclusion and Acknowledgements

The NICE cryptosystem is fast and well suited for software implementation. To get an equally fast speedup compared to RSA on a smart card, we think that the underlying hardware must be developed adequately. Nevertheless, if this is done, the think that NICE can be a competitor to RSA whenever fast decryption is needed.

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## A An Example of the Reduction Step

Input: discriminant  $\Delta_1$ , an ideal  $\mathfrak{A} = (A, B)$  before the reduction step

```
D1 = -3919553298811157368475523990994777486712879620160636686074980926686635774565281654416535613225312452663
A = 1470919698723621747031034479033655458221345099558043935477761485226131553543707299391765504474531255189264719094757
659808191301659419506155735441417414491
B = -1612078661117333293617543382531888102723531401772217996101303796600583164587338163291108859457317212840954820324227
661206969587778502329803762655721150213
```

Reduction step 5.1.: quotient  $\lambda$ , remainder  $\mu$  such that  $B = 2A\lambda + \mu$

```
(quotient, remainder) =
(-1, 132976073632991020044452557553542281371915879734386987485421917385167994250007643549240414949174529753757461786528
7658409413015540336682507708227113678769)
(-2, -1276124359032717472842039409625630472883197794024205168731345688046856992818597251036644668758126413199147109490
159283556463934886789503896413069632105)
(5, -78528290011262457388456843941329248503454444074743493793783111760068351530406923212045847152018597171039409030826
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Output: the reduced ideal  $(A1, B1)$  equivalent to  $\mathfrak{A}$

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B1 = -742593578199047322036735798245427373773251928151497

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